

Date: 25th April 2023

COURSE COMPLETION CERTIFICATE

Name: MS TONAPE UJWALA

Reference Number:291839653921

College Name: Bharath Institute of Higher Education and Research, Chennai

Degree & Department: B. Tech- Electronics and Communication Engineering,
Chennai.

Batch ID: 2020 – 2024

Program language: VERILOG ,VHDL

*{Introduction to VLSI, Combinational and Sequential Circuit Design, Memory System Design
Design Methodology and Tools}*

Duration: 27th February 2023 and 25th April 2023 **(2 Months)**

MS TONAPE UJWALA, displayed professional traits during the Course period and managed to complete all assigned tasks as required. She was Hardworking, dedicated and committed it was a pleasure with us in this short period.

We Wish all the best for your future.

Yours Sincerely,


J. Velmurugan
Project Team Head



DLK CAREER DEVELOPMENT