

COMP40

Homework 7: Laboratory Notes

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Machine Model: Intel(R) Core(TM) i7-6700 CPU@3.40GHz

Legend: *small* - midmark benchmark

mid - partial solution to advent

big - sandmark benchmark

No.	Benchmark	Time (User Mode)	Instructions	Rel to start	Rel to prev	Improvement /Change	Remarks
1	small	6.90s	5.3543×10^{10}	1.000	1.000	-	Starting Point (Without optimization flags)
	mid	60.63s	-	1.000	1.000		
	big	168.56s	-	1.000	1.000		
2	small	3.74s	3.4895×10^{10}	0.542	0.542	Compiled with optimization <code>-O1</code> flag turned on and linked against <code>-lcii40-O1</code>	-
	mid	33.64s	-	0.554	0.554		
	big	93.59s	-	0.555	0.555		
3	small	3.17s	3.3253×10^{10}	0.459	0.848	Compiled with optimization <code>-O2</code> flag turned on and linked against <code>-lcii40-O2</code>	Since this is faster, any improvements moving forward will be compiled with the the <code>-O2</code> flag
	mid	28.20s	-	0.465	0.838		
	big	81.39s	-	0.483	0.870		
4	small	2.87s	3.0400×10^{10}	0.416	0.905	Added a conditional within <code>run_prog</code> so that the variables <code>prog_seg</code> and <code>curr_length</code> are updated only when necessary (i.e. when the load program instruction is called)	This reduces the number of times <code>Seq_get</code> and <code>UArray_length</code> have to be called
	mid	25.75s	-	0.425	0.913		
	big	71.43s	-	0.424	0.878		
5	small	2.95s	3.1753×10^{10}	0.428	1.02	Took the checks for memory and register values out from the <code>ops_interface</code> functions, and instead put them within the <code>while</code> loop in <code>run_prog</code> -- Undid change	The change made it slower because the program was now checking all the variables within the <code>while</code> loop, when previously it was only checking the relevant variables within each function
	mid	26.57s	-	0.438	1.03		
	big	75.74s	-	0.449	1.06		
6	small	2.85s	3.0396×10^{10}	0.413	0.966	Modified the process in <code>init_prog</code> in which we determined how	Negligible improvement from change #4, but still a decrease in the
	mid	25.48s	-	0.420	0.959		

	big	72.47s	-	0.421	0.957	many <code>uint32_t</code> words there were in the file given. Instead of a <code>while</code> loop and using a counter variable, we utilized the C library's <code>fseek</code> function.	number of instructions nonetheless.
7	small	2.83s	3.0396×10^{10}	0.410	0.993	Put all functions in <code>mem_interface</code> , <code>ops_interface</code> , <code>io_dev</code> and <code>bitpack.c</code> in main	While the number of instructions seems to remain constant, apart from the anomalous increase of timing with mid, the overall timing does decrease slightly
	mid	25.57s	-	0.422	1.004		
	big	71.00s	-	0.430	0.980.		
8	small	1.70s	1.3380×10^{10}	0.246	0.601	Used <code>static inline</code> for all functions within <code>um.c</code> , aside from main and functions linked from <code>seq.h</code>	-
	mid	15.19s	-	0.251	0.594		
	big	42.63s	-	0.253	0.600		
9	small	1.70s	1.3326×10^{10}	0.246	0.000	Removed the <code>exit_condition</code> conditional; changed the <code>while</code> loop to always be true; <code>run_prog</code> returns when <code>prog_count</code> reaches the end of program	Not a significant decrease in time, but reduces the total number of instructions
	mid	14.93s	-	0.246	0.983		
	big	42.28s	-		0.992		
10	small	1.33s	1.0325×10^{10}	0.193	0.782	Changed the data structure used to represent the words within each segment, from a <code>UArray</code> to a dynamic C <code>uint32_t</code> array. As a corollary, also changed the <code>mem_seg</code> struct to include a <code>numwords</code> tracker that helps us keep track of the length of the C array	-
	mid	12.55s	-	0.207	0.841		
	big	33.25s	-	0.197	0.786		
11	small	0.93s	6.3407×10^9	0.135	0.699	Changed the data structure used to represent the registers, from a <code>UArray</code> to a static C <code>uint32_t</code> array	-
	mid	8.01s	-	0.132	0.638		
	big	23.32s	-	0.138	0.701		
12	small	0.38s	3.4349×10^9	0.055	0.409	Changed the data structure used to represent the memory,	-
	mid	2.71s	-	0.045	0.338		

	big	9.14s	-	0.054	0.392	<p>from a <code>Seq</code> to a dynamic C array of <code>mem_seg</code>.</p> <p>As a corollary, also created a <code>memory</code> struct that includes the C array and a <code>memlength</code> tracker that helps us keep track of the length of the C array (i.e. number of segments)</p>	
13	small	0.29s	3.0711×10^9	0.042	0.763	<p>Changed the data structure used to keep track of the identifiers of unmapped identifiers, from a <code>Seq</code> to a dynamic C <code>uint32_t</code> array.</p> <p>As a corollary, also created an <code>unmapped_list</code> struct that includes the C array, a <code>lastindex</code> tracker that helps us keep track of the significant element of the C array, and a <code>listlength</code> variable for the size of the array</p>	-
	mid	2.54s	-	0.042	0.937		
	big	7.57s	-	0.045	0.828		
14	small	0.29s	3.0765×10^9	0.042	0.000	<p>Replaced <code>fseek</code> process used in change #6 with <code>stat</code> function outside of <code>init_prog</code></p>	Interestingly, the number of instructions increased, but timings became quicker
	mid	2.43s	-	0.040	0.957		
	big	7.30s	-	0.433	0.964		
15	small	0.27s	2.9905×10^9	0.039	0.931	<p>Got rid of the <code>mem_seg</code> struct, and changed the data structure used to represent the memory, from a dynamic C array of <code>mem_seg</code> to a 2D dynamic C <code>uint32_t</code> array within the <code>memory</code> struct.</p> <p>The first element of the array is the old <code>mapped</code> boolean flag, the second element represents the old <code>numwords</code> tracker, and the rest of the array elements are the</p>	-
	mid	2.13s	-	0.035	0.877		
	big	6.76s	-	0.401	0.926		

						words of the segment.	
15	small	0.25s	2.8971×10^9	0.036	0.926	Got rid of the unmapped_list struct, and moved all its variables into the memory struct.	-
	mid	2.01s	-	0.033	0.944		
	big	6.36s	-	0.038	0.941		
16	small	0.24s	2.8941×10^9	0.034	0.960	Compiled with optimization -O3 flag turned on and linked against -lcii40	We will be using this compile flag in the final Makefile
	mid	1.94s	-	0.032	0.965		
	big	6.12s	-	0.036	0.962		