ISA CONTROL DECODE TABLE:

Instruction	Function	OP	RegWrite	Branch	Jump	WriteEnable	MemtoReg	ALU CONTROL 2:0
load	000	XX	1	0	0	0	01	XXX
store	001	XX	0	0	0	1	XX	XXX
beq	011	XX	0	1	0	0	XX	110
slt	100	XX	1	0	0	0	00	111
not	110	11	1	0	0	0	00	101
add	010	XX	1	0	0	0	00	000
and	110	01	1	0	0	0	00	010
xor	111	XX	1	0	0	0	00	001
slr	110	00	1	0	0	0	00	011
jump	110	10	0	0	1	0	XX	XXX
init	101	XX	1	0	0	0	10	XXX