

Molecular-Beam Epitaxy of Two-Dimensional In_2Se_3 and Its Giant Electroresistance Switching in Ferroresistive Memory Junction

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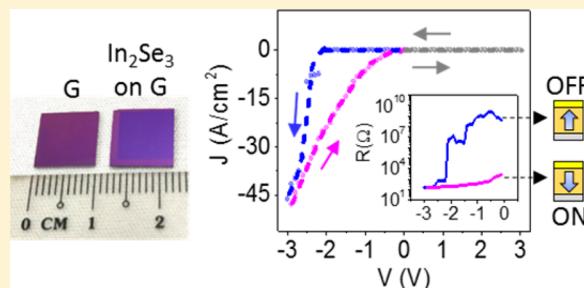
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Supporting Information

ABSTRACT: Ferroelectric thin film has attracted great interest for nonvolatile memory applications and can be used in either ferroelectric Schottky diodes or ferroelectric tunneling junctions due to its promise of fast switching speed, high on-to-off ratio, and nondestructive readout. Two-dimensional α -phase indium selenide (In_2Se_3), which has a modest band gap and robust ferroelectric properties stabilized by dipole locking, is an excellent candidate for multidirectional piezoelectric and switchable photodiode applications. However, the large-scale synthesis of this material is still elusive, and its performance as a ferroresistive memory junction is rarely reported. Here, we report the low-temperature molecular-beam epitaxy (MBE) of large-area monolayer α - In_2Se_3 on graphene and demonstrate the use of α - In_2Se_3 on graphene in ferroelectric Schottky diode junctions by employing high-work-function gold as the top electrode. The polarization-modulated Schottky barrier formed at the interface exhibits a giant electroresistance ratio of 3.9×10^6 with a readout current density of $>12 \text{ A/cm}^2$, which is more than 200% higher than the state-of-the-art technology. Our MBE growth method allows a high-quality ultrathin film of In_2Se_3 to be heteroepitaxially grown on graphene, thereby simplifying the fabrication of high-performance 2D ferroelectric junctions for ferroresistive memory applications.



KEYWORDS: In_2Se_3 , two-dimensional materials, molecular-beam epitaxy, ferroelectric Schottky diode, ferroresistive memory junction, giant electroresistance ratio

Ferroelectric Schottky diodes (FSDs) with polarization-modulated Schottky barriers are promising for applications in resistive switching memories, but its performance in terms of its on-to-off ratio and readout current densities are much poorer than ferroelectric tunneling junctions (FTJ).^{1,2} This is because the insulating properties of oxide-based ferroelectric materials limit the maximum diode current, which precludes the stable detection of memory status using sense amplifiers, especially in miniaturized circuit elements.¹ Using thinner materials to improve current density, however, compromises the ferroelectric properties due to the presence of depolarization field, which strongly suppresses spontaneous polarization in thin materials.³ The ideal ferroelectric material should, therefore, possess a modest band gap and also the ability to retain spontaneous out-of-plane polarization when scaled down to the

ultrathin level. Recently, 2D indium selenide (α - In_2Se_3) was reported to exhibit spontaneous electric polarization.^{4–6} Its unique noncentrosymmetric crystal structure leads to an intrinsic stabilization of in-plane and out-of-plane polarizations through dipole-locking,⁷ giving rise to multidirectional piezoelectricity for applications such as electronic skin and energy-harvesting cells^{4,6} as well as ferroelectricity for switchable diode devices.⁵ The ferroelectric properties of In_2Se_3 may provide additional control in applications of optoelectronics,^{8–10} photovoltaics,^{11–13} and thermoelectrics.¹⁴ To harness its interesting optical and electronic properties, the large-scale

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synthesis of ultrathin, high-quality α -In₂Se₃ is needed. To date, only isolated micrometer-sized α -In₂Se₃ crystals had been grown using chemical vapor deposition (CVD)^{5,15} and physical vapor deposition (PVD)^{4,16,17} approaches. The MBE growth of In₂Se₃ has mostly been reported for thick film^{18–21} using indium elemental sources, which is incompatible with the growth of ultrathin films due to its low melting point.

Herein, we report the molecular-beam epitaxy of continuous, centimeter-sized monolayer α -In₂Se₃ on single-layer graphene using the twin evaporation of In₂Se₃ and Se precursors. The optimized growth temperature of 250 °C presents process advantages because it is significantly lower than CVD and PVD growth temperatures, which are typically >600 °C. Our growth method is highly versatile, and the conditions can be tuned to grow monolayer α -In₂Se₃, γ -In₂Se₃, or InSe. Piezoresponse force microscopy (PFM) was used to characterize the ferroelectric properties of as-grown α -In₂Se₃ film of ~6 nm thickness with coercive voltages observed at +0.5 and -2.5 V. A MBE-grown few-layer α -In₂Se₃-on-graphene structure was readily fabricated into a ferroresistive Schottky diode by using gold as the top electrode, achieving a giant electroresistance with an on-to-off ratio of up to 3.9×10^6 , which is comparable with the state-of-the-art FTJs.^{22–24} The current density obtained at the reading voltage of -1.4 V is at least 12 A/cm², which is sufficient for detection by sense amplifier, even when miniaturized. The on-to-off ratio and readout current density are one of the highest values reported for FSDs,^{1,25,26} and unlike FTJs, the fabricated In₂Se₃ ferroelectric junction does not need additional engineering of interface barrier material.^{23,24,27}

Monolayer indium selenide was epitaxially grown on single-layer graphene substrate at 250 °C using powdered In₂Se₃ and Se precursors, as shown schematically in Figure 1a. To obtain high-quality α -In₂Se₃ film, a high flux ratio of Se/In₂Se₃ ≈ 6:1 is needed. Keeping all other growth parameters constant, when only In₂Se₃ is evaporated, monolayer InSe is grown instead (Figure S1). This can be attributed to the decomposition of In₂Se₃ in Se-deficient environment.²⁸ The phases of the grown films were unambiguously determined by Raman spectroscopy.^{29,30} For the film grown without Se precursor, four peaks are observed at ~116, 178, 200, and 227 cm⁻¹ (Figure 1b), which can be assigned to the A₁, E, A₁(LO), and A₁ Raman peaks of InSe, respectively.³¹ When the film was grown with the coevaporation of In₂Se₃ and Se precursors, pure-phase α -In₂Se₃ could be grown, as judged by the presence of the A₁(LO + TO), A₁(TO), and A₁(LO) Raman signature peaks of α -In₂Se₃^{32,33} at ~108, 176, and 205 cm⁻¹, respectively (Figure 1d). We also investigated the growth condition of Se/In₂Se₃ ≈ 3:1 and obtained the Raman spectrum dominated by peaks associated with InSe but with an extra peak at ~208 cm⁻¹ (black arrow, Figure 1c), which most likely originated from α -In₂Se₃.

The morphology of the films grown under different conditions was analyzed using atomic force microscopy (AFM). In contrast to InSe (Figure 1e), the nucleation density of α -In₂Se₃ is much lower and its grains are much larger, better-faceted, and highly oriented (Figure 1g). However, the AFM of the mixed-phase indium selenide (Figure 1f) shows a similar grain size to that of pure InSe; it is notable that there is buckling of grain boundaries, which may be due to lateral lattice mismatch of InSe and α -In₂Se₃. Such mixed-phase 2D materials can be developed for interesting electronic, chemical, and optical applications.^{34–36}

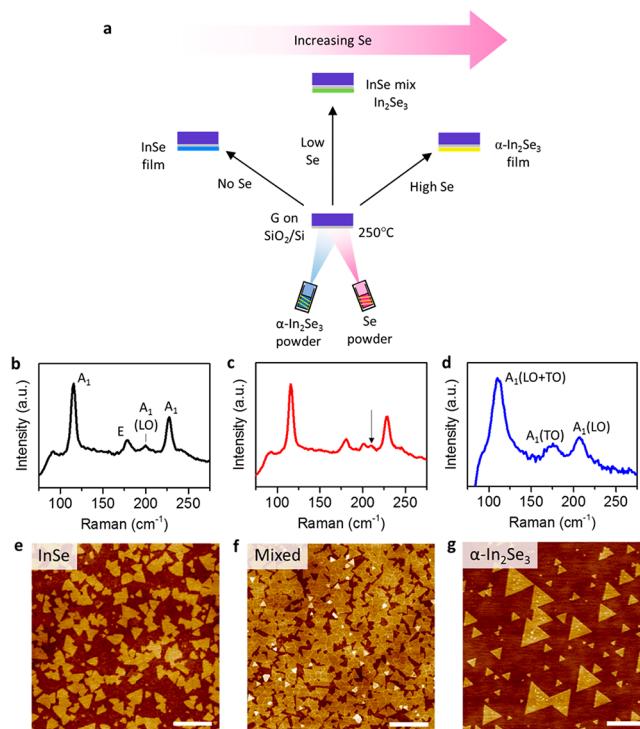


Figure 1. Controlled MBE growth of InSe to In₂Se₃. (a) Schematic illustration of the different growth conditions required to selectively grow the different binary phases of indium selenide. Raman of the (b) InSe grown under condition of only In₂Se₃ precursor, (c) mixed InSe-In₂Se₃ grown with precursor ratio Se/In₂Se₃ = 3:1, and (d) In₂Se₃ grown with precursor ratio Se/In₂Se₃ = 6:1. AFM topography of the as-grown (e) InSe, (f) mixed InSe-In₂Se₃, and (g) In₂Se₃. Scale bars: 400 nm.

The growth mechanism of the α -In₂Se₃ films followed the Frank-van der Merwe growth mode,^{37,38} in which lateral growth occurs much faster than vertical growth. We found that the optimum MBE growth of α -In₂Se₃ occurs at 250 °C because further increase in growth temperature results in a high desorption rate of grown In₂Se₃ film (Figure S2). The growth of α -In₂Se₃ at this temperature is sufficient for the grains to coalesce and form continuous monolayer film (Figure 2a–c) with a growth rate of one monolayer in 2 h, which is much faster than the reported >10 h/ML MBE growth rate of monolayer transition metal dichalcogenides (TMDCs).^{39,40} We noted that the monolayer α -In₂Se₃ is easily damaged under voltage-biased AFM tip or laser illumination due to its low thermal conductivity⁴¹ and have exploited this property for the precise nanoscale etching and patterning of the ultrathin film^{42,43} without the use of any mask or complicated lithography procedure (Figure 2d). The rectangular pattern was etched in the 4 nm thick α -In₂Se₃ with a 10 V biased AFM tip.

Other than studying growth parameters such as substrate temperature and precursor flux ratio, we also investigated the effect of substrate on the phase of the as-grown material, which is especially relevant for In₂Se₃ due to its rich polymorphism. AFM topographic scans show that the grains grown on the epitaxial graphene substrate are well-aligned with 60° orientation. For controlled experiment, we performed the same growth on nonepitaxial SiO₂/Si substrate, a commonly used substrate for CVD growth of single crystal TMDC.⁴⁴ A nonlayered growth morphology (Figure 2e) due to the growth of γ -In₂Se₃³³ was obtained, as verified by the Raman signature peaks of this phase

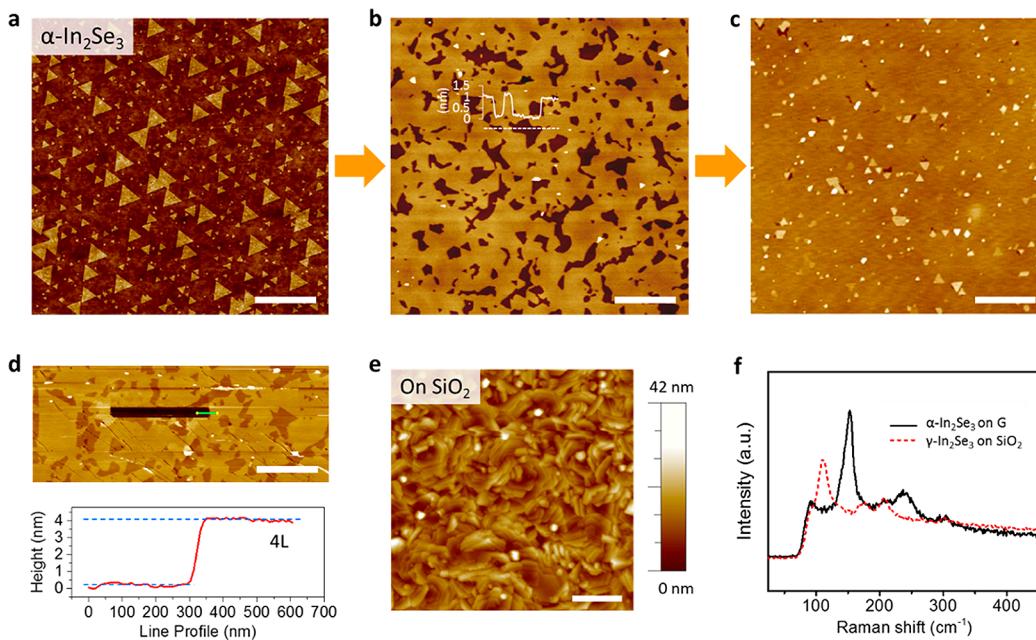


Figure 2. AFM characterization of MBE grown α -In₂Se₃ and γ -In₂Se₃. AFM topography showing the evolution in α -In₂Se₃ morphology as the growth progresses from (a) isolated facets to (b) coalescence and to (c) forming a continuous monolayer. (d) Etched hole of area of $3\ \mu\text{m} \times 300\ \text{nm}$ on the as-grown 4 nm thick α -In₂Se₃ by 10 V-biased AFM contact tip. The bottom plot shows the corresponding height profile. (e) AFM topography of the γ -In₂Se₃ grown on SiO₂/Si substrate and its (f) corresponding Raman spectrum compared to that of α -In₂Se₃ grown on graphene. Scale bars: (a–c) 1 μm , (d) 2 μm , and (e) 400 nm.

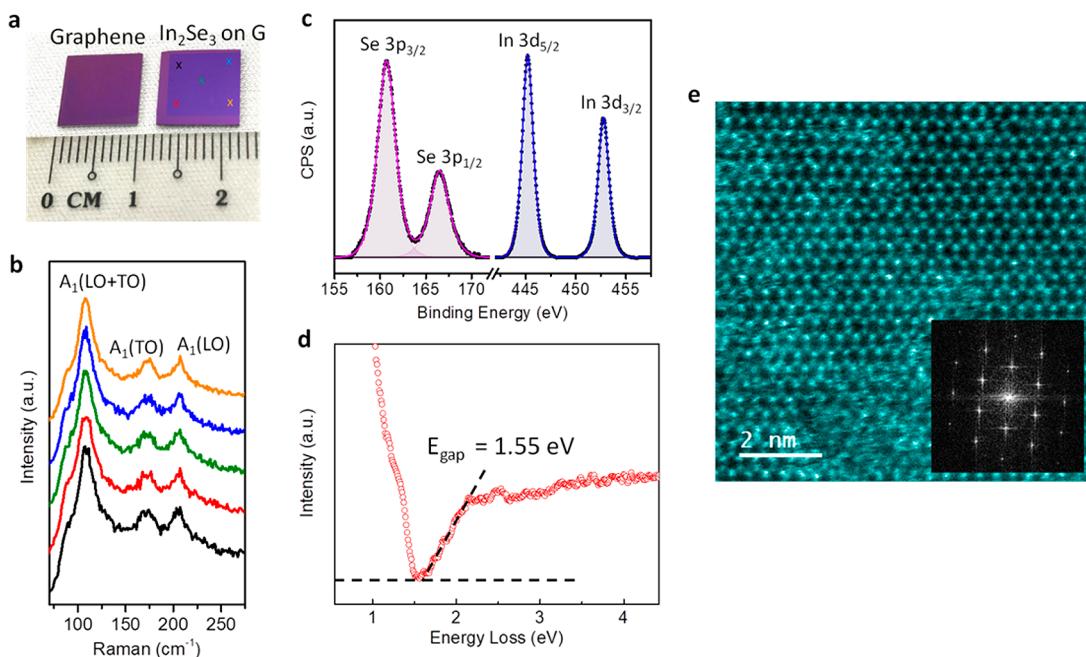


Figure 3. Large-area MBE growth of α -In₂Se₃ and its spectroscopic and microscopic characterizations. (a) Photograph of the 1 cm^2 graphene on SiO₂/Si substrate and $\sim 1\ \text{cm}^2$ MBE grown α -In₂Se₃ on the graphene/SiO₂/Si substrate. (b) Raman spectra of the respective colored spots from the MBE-grown 1 cm^2 In₂Se₃ shown in panel a. (c) XPS of the In 3d and Se 3p core orbital peaks of the grown α -In₂Se₃ film. (d) HREELS measured at specular angle of the grown α -In₂Se₃ film. The onset indicates the electronic bandgap. (e) STEM-ADF image of the grown α -In₂Se₃ monolayer. The inset shows the corresponding fast Fourier transform image.

at 152, 209, and 237 cm^{-1} (Figure 2f). This shows that for the MBE growth of van der Waals 2D film, a substrate with matching crystal symmetry is required, in agreement with previous MBE studies of 2D transition-metal dichalcogenides.^{38,45}

The MBE growth of α -In₂Se₃ film is highly scalable, and we demonstrate it on a 1 cm^2 CVD graphene layer on SiO₂/Si,

prepared by the method described in ref 46 (Figure 3a). Raman spectra recorded from random spots on the grown sample are almost identical, attesting to the uniformity of the grown film (Figure 3b). X-ray photoemission spectroscopy (XPS) was used to probe the elemental composition and oxidation states of the grown film, as shown in Figure 3c. The spectra exhibit two

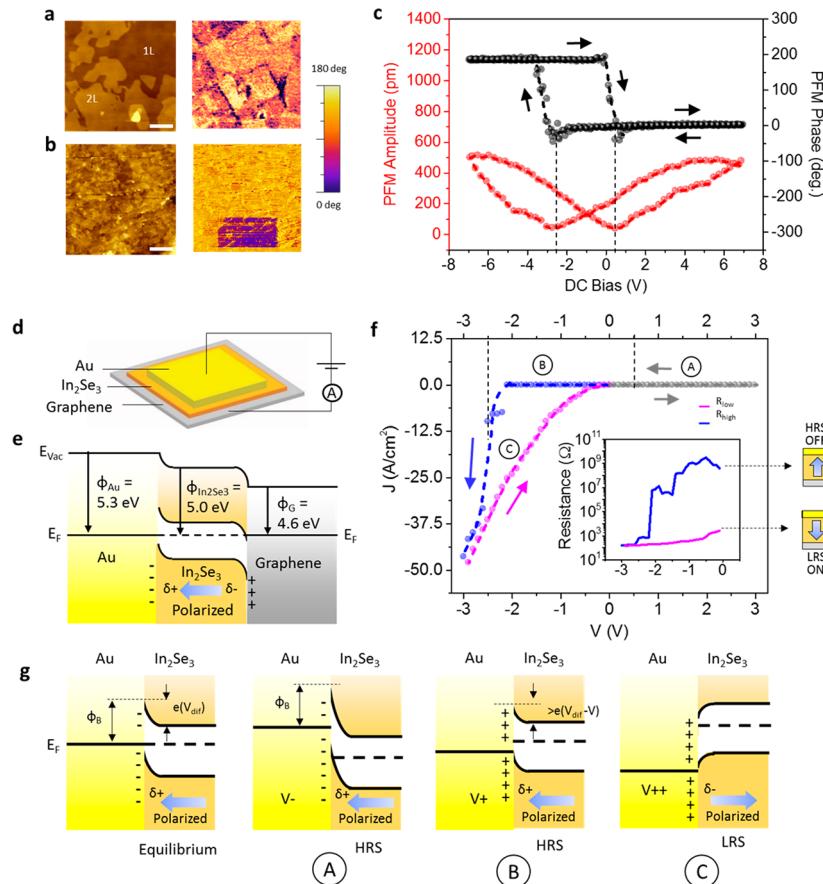


Figure 4. Ferroelectric properties and ferroresistive memory device performance of MBE-grown α -In₂Se₃. (a) Topography and the corresponding PFM-phase image of the as-grown α -In₂Se₃. Scale bar: 1 μ m. (b) Topography and the corresponding poled PFM-phase image. An area of $5.2 \times 2.5 \mu\text{m}^2$ is poled by a -6 V biased tip. Scale bar: 2 μ m. (c) Local PFM hysteresis curves of the as-grown film. (d) Schematics of the asymmetric ferroelectric memory junction fabricated on the as-grown 6 nm α -In₂Se₃ and its (e) corresponding band diagram alignments at equilibrium. (f) $J(V)$ curve measured from the ferroelectric memory junction with respect to the graphene electrode. The inset shows the corresponding resistance of the blue (0 to -3 V) and pink (-3 to 0 V) curves. (g) Schematics showing the band diagram changes when voltage is applied, in correspondence to the marked A, B, and C regions of the (f) $J(V)$ plot.

doublets with binding energies of 166.5, 160.7, 452.7, and 445.2 eV, which are assigned to the In₂Se₃ XPS peaks for Se 3p_{1/2}, Se 3p_{3/2}, In 3d_{3/2}, and In 3d_{5/2} core level orbitals, respectively.⁴⁷ The elemental composition of In and Se was obtained by peak fitting with a mixed Gaussian–Lorentzian function after a Shirley background subtraction. XPS chemical composition analysis indicates an In-to-Se ratio of 2:3, confirming the presence of a pure In₂Se₃ phase. There is very little surface oxidation because XPS only detected physisorbed oxygen (Figure S3).

The electronic band gap of the as-grown α -In₂Se₃ sample can be measured using high-resolution electron energy loss spectroscopy (HREELS). The energy-loss region was scanned in a specular geometry with incident electron energy of 16 eV, and the energy gap is determined from the sharp spectral onset delineated by an enhanced intensity from the background, which originates from interband electronic transitions.^{48,49} As shown in Figure 3d, an energy onset of ~ 1.55 eV was observed, which corresponds to the direct electronic bandgap of α -In₂Se₃.⁵⁰ Scanning transmission electron microscopy-annular dark-field (STEM-ADF) image of the monolayer film (Figure 3e) shows a periodic hexagonal *P63/mmc* crystal lattice (Figure S4) with a lattice constant of 4.0 Å, which corresponds to the crystal structure of α -In₂Se₃.⁵¹ Therefore, both spectroscopic and

microscopic characterizations unambiguously identified the grown film as the α phase of In₂Se₃.

The piezoelectric and ferroelectric properties of the MBE-grown α -In₂Se₃ were characterized using high-resolution PFM. Figure 4a shows the topography and the corresponding out-of-plane PFM-phase domains of the MBE-grown 1-2L In₂Se₃. The low contrast in the polarization of the PFM phase can be attributed to the small out-of-plane polarization.⁵ Notably, the pristine PFM domains are distinct from the topography, indicating that the formation of domains did not arise purely due to topography differences or artifacts in measurement. Spontaneous polarization was induced by a DC bias of -6 V, and a $5.2 \mu\text{m} \times 2.5 \mu\text{m}$ domain was written on the ~ 6 nm thick In₂Se₃, which evidence the ferroelectric nature of the film (Figure 4b). Using switching-spectroscopy PFM, a typical ferroelectric butterfly loop and hysteresis loop are observed for the PFM amplitude and PFM phase, respectively (Figure 4c). The measured coercive voltages are approximately $+0.5$ and -2.5 V.

Giant electroresistance ratio of at least 10^4 is highly desirable for ferroresistive memory device.² However, this is rarely observed in nontunneling ferroelectric diode devices.²⁶ The use of a 2D ferroelectric narrow-band-gap semiconductor allows better tuning of the Schottky barrier through enhancing the

Coulomb interactions from its reduced dielectric screening.⁵² To further enlarge the differences between the “on”-state and the “off”-state resistance, an asymmetric electrode configuration can be used. To demonstrate such a device, a ferroelectric junction was fabricated on the MBE grown 6 nm film by evaporating gold (Au) electrode on top and using the graphene substrate directly as the bottom electrode, as shown in Figure 4d. The resultant band alignments are shown in Figure 4e. The measured $J(V)$ characteristics with bias applied to the graphene electrode show a rectifying behavior and a strong hysteresis in the negative bias range (Figure 4f), in which a sharp increase of current density is observed at -2.5 V. This value coincides with the switching voltage measured by PFM, which, therefore, indicates that the $J(V)$ behavior is determined by the ferroelectric polarizations. The calculated resistance, R , equals voltage divided by current for the respective forward (blue, 0 V to -3 V) and backward (pink, -3 V to 0 V) measurements and is shown in the inset of Figure 4f. Remarkably, the ratio between the high-resistance state (HRS) and low-resistance state (LRS) is as high as 3.9×10^6 , exhibiting a giant electroresistance ratio.^{22,53} This device performance demonstrates that few-layer In_2Se_3 ferroelectric semiconductor junction can be used as random access memory, requiring a low voltage of ± 4 V to write and approximately -1 V to read. At the reading voltage of -1.4 V, the current density is at least 12 A/cm^2 , which is more than 200% higher than typical ferroelectric diode junctions.^{1,25} Such high current density can be ascribed to the 2D van der Waals nature of In_2Se_3 ⁵⁴ and provides unambiguous differentiation of the “on” and “off” states for memory sense amplifiers.

It should be noted that $\alpha\text{-In}_2\text{Se}_3$ grown on highly ordered pyrolytic graphite (HOPG) substrate gave the same performance as that grown on single layer graphene when fabricated as ferroelectric resistive memory device; thus, the thickness of graphene is not critical to the memory junction performance. The giant electroresistance switching effect can be explained by the interplay of two synergistic factors: (1) the use of 2D van der Waals ferroelectric semiconductor and (2) the use of highly asymmetric electrodes with higher and lower work function than the ferroelectric semiconductor. First, the use of a ferroelectric semiconductor enables the conductivity to be controlled by modulating the width and height of the Schottky barrier^{25,55} via the polarization of the ferroelectric. Second, the asymmetric electrodes provide a high rectification ratio. Films thicker than 6 nm were used due to their higher thermal stability and resistance to electron beam-induced damage (Figure 2d). Due to work function differences (UPS of In_2Se_3 in Figure S5), at the metal–semiconductor interfaces, electrons flow from graphene to In_2Se_3 and from In_2Se_3 to Au to form space-charged regions. The asymmetric space-charge interfaces thus result in an intrinsic preferential polarization state in the In_2Se_3 film due to Coulomb interactions (Figure 4e). This also explains the asymmetric coercive voltages measured by PFM. Because In_2Se_3 is an n -type semiconductor (Figure S5), the graphene– In_2Se_3 interface forms an injecting Ohmic contact, and the Au– In_2Se_3 interface forms a Schottky contact, with the potential barrier given as:⁵⁶

$$\phi_B = \phi_M - \chi$$

where ϕ_B is the Schottky barrier, ϕ_M is the work function of metal, and χ is the electron affinity of the semiconductor. ϕ_B is calculated to be 1.7 eV based on the work function of Au as 5.3 eV, and the electron affinity of In_2Se_3 was calculated as 3.6 eV.⁵⁷

Figure 4g illustrates the changes in the Schottky barrier when a voltage bias is applied. At equilibrium, there exists a space-

charged depleted region, with a calculated Schottky barrier of ~ 1.7 eV. When a negative bias is applied to the Au electrode (positive bias to graphene, as in the Figure 4f plot), the depletion zone width increases.⁵⁶ At a voltage bias greater than the coercive voltage (more positive than 0.5 V with respect to graphene), the polarization flips toward the Au electrode and reduces the diffusion potential of the depletion zone. However, the Schottky barrier seen by the transporting electrons does not reduce and electron flow is low (region A of Figure 4f plot). When a positive bias is applied at the Au electrode (the negative region in Figure 4f plot, with respect to graphene), the barrier seen by the electron flow is given by the diffusion potential:⁵⁶

$$e(V_{\text{dif}}) = \phi_M - \phi_S$$

where $e(V_{\text{dif}})$ is the diffusion potential and ϕ_S is the work function of the semiconductor. It is calculated to be 0.3 eV. With applied bias V , the barrier is reduced by the amount $e(V)$. Because the applied bias still is below that of the coercive voltage for flipping (magnitude lower than -2.5 V), the polarization in In_2Se_3 still points toward the Au electrode and adds on to the potential barrier, making it slightly higher than $e(V_{\text{dif}} - V)$. As such, it is still in HRS as shown in region B of Figure 4f, and transport is characterized by Schottky emission,⁵⁵ with the relationship⁵⁶ (Figure S6):

$$I = I_0 \left[\exp\left(\frac{eV}{kT}\right) - 1 \right]$$

When the magnitude of applied bias is more than the coercive field (-2.5 V with respect to graphene), the polarization in In_2Se_3 reverse and points toward the graphene electrode, resulting in the reversal of the band bending,^{22,23} and the transport characteristics is ohmic (Figure S6). The lowest resistance is obtained (region C of Figure 4f) when the ferroelectric polarization direction is parallel to the built-in electric field at the metal–semiconductor interface.⁵⁵ It is noted that during discharge, there will be no changes to the injecting ohmic contact at graphene– In_2Se_3 ; applied bias is distributed across the semiconductor junction, and is not taken up in the contact area, unlike the Schottky contact.⁵⁶

The use of graphene as the substrate serves two purposes. The first is as an epitaxial substrate for the growth of highly crystalline In_2Se_3 films. Another role is to serve as a low-work-function electrode to compliment the high-work-function gold electrode; such an asymmetric electrode design maximizes the polarization effect. In view of the fact that the growth of CVD graphene by roll-to-roll process has entered the first stage of commercialization, the epitaxial growth of ultrathin $\alpha\text{-In}_2\text{Se}_3$ on graphene by MBE provides convenient integration in the large-scale fabrication of ferroelectric junctions because high-work-function metal such as gold can be directly sputtered onto $\alpha\text{-In}_2\text{Se}_3$ –graphene. Another important point is that no “transfer” is needed in this process because the ferroelectric material is directly grown on graphene. Furthermore, the advantage of using $\alpha\text{-In}_2\text{Se}_3$ ferroelectric Schottky diode is that no additional engineering of the barrier is needed, unlike the case of FTJ. Highly competitive performances for on–off current and readout diode current can be achieved compared with FTJ and state-of-the-art ferroelectric diodes. The good performance of our ferroelectric junction may be related to the much lower growth temperature used in our MBE growth (250 °C) as compared with CVD methods (>600 °C) because high growth temperature can cause the desorption of highly volatile indium

or selenium species, leading to defects and interfacial traps, which affect reproducibility and stability of switchable diode behavior.⁵⁸

In conclusion, we have developed a method to grow large area α -In₂Se₃ films on graphene by MBE, using In₂Se₃ and Se powder precursors and at a low growth temperature of 250 °C. The epitaxial growth grains are highly crystalline and well-oriented, which coalesce to form a uniform and continuous monolayer of α -In₂Se₃. Our MBE growth method can be readily controlled to grow pure phases of monolayer α -In₂Se₃, InSe, and γ -In₂Se₃ as well as mixed phases such as InSe-In₂Se₃. We further designed an asymmetric ferroresistive memory device to exploit the 2D semiconducting ferroelectric nature of α -In₂Se₃ and achieve outstanding performances, with giant electroresistance ratios of 3.9×10^6 and readout current densities of >12 A/cm². The successful heteroepitaxial growth of large area ferroelectric semiconductor on graphene at modest growth temperatures allows the convenient fabrication of high-performance ferroelectric Schottky diodes for memory applications.

MBE Growth of Samples. Indium selenides were grown in a customized MBE chamber with a base pressure $\sim 6 \times 10^{-10}$ Torr. Prior to growth, the substrate was degassed in an ultrahigh-vacuum chamber for 30 min and annealed at 600 °C for 5 min. Single-layer graphene on SiO₂/Si is used as the substrate unless otherwise stated. Ultrapure In₂Se₃ powder (99.99%) and Se pellets (99.999%) were evaporated from a crucible heated by an electron beam source and a hot-lip effusion cell, respectively. For the growth of In₂Se₃, the temperature of Se crucible cells was maintained at 150 °C with a hot-lip at 220 °C. The flux ratio of the precursors are as described in the main text. The chamber pressure during growth was $\sim 1 \times 10^{-8}$ Torr when the selenium source is used and $\sim 6 \times 10^{-9}$ Torr when only In₂Se₃ is evaporated.

Atomic Force Microscopy. Atomic force microscopy was performed using Bruker Dimension FastScan Atomic Force Microscope in noncontact mode at room temperature. High-resolution PFM measurements were carried out with a commercial scanning probe microscope (Asylum Research MFP-3D) instrument at ambient atmosphere. Pt-coated tips (AC240TM, spring constant of ~ 2 N m⁻¹, Olympus, Japan) were used to measure the local switching spectrum and out-of-plane PFM image. The typical scan rate for all scan modes was 1 Hz. The drive frequency, drive amplitude (V_{AC}), and trigger force were ~ 270 kHz, 1 V, and 80 nN, respectively. To induce polarization in the ferroelectric material, -6 V DC voltage was applied to the conductive tip onto the sample and the sample scanned via normal PFM measurements.

Raman Spectroscopy. Raman spectra were recorded at room temperature using the confocal WiTec Alpha 300R Raman Microscope with laser excitation at 532 nm and power of <100 μ W.

Scanning Transmission Electron Microscopy. STEM-ADF imaging was performed using an aberration-corrected JEOL ARM200F, equipped with a cold field emission gun, operating at 80 kV.

High-Resolution Electron Energy-Loss Spectroscopy. HREELS measurements were performed using a Delta 0.5 spectrometer (SPECS, GmbH) with specular geometry in the coarse mode. The impinging electron energy is set to 16 eV with an energy resolution of 30 meV.

X-ray and Ultraviolet Photoemission Spectroscopies. XPS characterizations were performed using SPECS XR-50 X-ray Mg K α (1253.7 eV) source with a pass energy of 30 eV and a

spot size of 5 mm. Detection was done by a PHOIBOS 150 hemispherical energy analyzer (SPECS, GmbH). The binding energies of the XPS spectra were calibrated using Au 4f_{7/2} peaks. XPS peak fitting was carried out using a mixed Gaussian–Lorentzian function after a Shirley background subtraction. Area ratios of 3:2 between the In 3d_{5/2} and In 3d_{3/2} and 2:1 between the Se 3p_{3/2} and Se 3p_{1/2} peaks were employed in the fit with same full width at half-maximum. UPS measurements were performed with monochromated photon energy of 21.2 eV (He I) and 40.8 eV (He II) through a toroidal mirror monochromator (SPECS GmbH). The detector used is the same as XPS characterizations. The experiments were performed in a chamber of base pressure greater than 5×10^{-10} mbar.

Device Fabrication and Measurements. Top gold electrodes with thicknesses of 50 nm were deposited via thermal evaporation directly on the α -In₂Se₃–graphene samples using a copper grid mask of hole 200 μ m \times 200 μ m. Electrical measurements were carried out at room temperature using Keithley 6430 SourceMeter to record $J(V)$ curves (0 → +3.0 V → -3.0 V → 0).

■ ASSOCIATED CONTENT

S Supporting Information

The Supporting Information is available free of charge on the ACS Publications website at DOI: [10.1021/acs.nanolett.8b02688](https://doi.org/10.1021/acs.nanolett.8b02688).

Figures showing MBE growth, desorption of grown α -In₂Se₃, XPS analysis, identification of the structural phase of α -In₂Se₃, UPS of MBE-grown α -In₂Se₃, and conduction mechanisms ([PDF](#))

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Author Contributions

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Notes

The authors declare no competing financial interest.

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