CSEE223

Name: _____

Flip-Flops

Date: _____

MULTIPLE ANSWERS = YES

1.	A flip-flop is:	
	a bistable circuit.	A
	capable of storing one binary digit.	B
	capable of having output states of either High or Low.	C
	capable of storing one bit of digital information.	D
	a circuit whose output states are stable.	E
2.	The 2 outputs of a flip-flop are:	
	Q and its complement	A
	Q and Q'	B
	All of the above	C
	None of the above	D
3.	The RS flip-flop has the following modes of operation:	
	Set	A
	Reset	B
	No Change	C
	Forbidden	D
	Toggle	E
4.	The JK flip-flop has the following modes of operation:	
	Set	A
	Reset	B
	No Change	C
	Forbidden	D
	Toggle	E
5.	Synchronous Mode means:	
	the flip-flop requires external clocking to change states.	A
	the flip-flop requires a clock pulse to change states.	B
	the inputs force the outputs to change states independent of clocking.	C
	None of the above	D

6.	Asynchronous Mode means:	
	the flip-flop requires external clocking to change states.	A
	the flip-flop requires a clock pulse to change states.	B
	the inputs force the outputs to change states independent of clocking.	C
	None of the above	D
7. 1.	To operate in asynchronous mode, a JK flip-flop uses only the _____ and _____ to change states.	
	PRESET	A
	CLEAR	B
	J	C
	K	D
	CLK	E
8. 2.	To operate in synchronous mode, a JK flip-flop needs the _____ to change states.	
	PRESET	A
	CLEAR	B
	J	C
	K	D
	CLK	E
9.	Toggle mode is the JK flip-flop output Q switching to its opposite state when:	
	PRESET = 1	A
	CLEAR = 1	B
	a CLK is applied.	C
	J = 1	D
	K = 1	E
10.	Hold mode is the JK flip-flop output Q remaining at its previous state when:	
	PRESET = 1	A
	CLEAR = 1	B
	a CLK is applied.	C
	J = 0	D
	K = 0	E