16 VCC $Q_B \prod 1$ Q_C | 2 15 Q_A $Q_D \begin{bmatrix} 1 \\ 3 \end{bmatrix}$ 14 SER Q_E [] 4 13 OE $Q_F \begin{bmatrix} 1 \\ 5 \end{bmatrix}$ 12 RCLK $Q_G \begin{bmatrix} 6 \end{bmatrix}$ 11 SRCLK Q_H [] 7 10 SRCLR 9 Q_H GND 8

D, N, NS, J, DB, or PW Package 16-Pin SOIC, PDIP, SO, CDIP, SSOP, or TSSOP Top View

Table 5-1. Pin Functions

Table 5-1. Pin Functions				
PIN				
NAME	SOIC, PDIP, SO, CDIP, SSOP, or TSSOP	LCCC	I/O ⁽¹⁾	DESCRIPTION
GND	8	10	_	Ground Pin
ŌĒ	13	17	I	Output E nable
Q_A	15	19	0	Q _A Output
Q _B	1	2	0	Q _B Output
Q _C	2	3	0	Q _C Output
Q _D	3	4	0	Q _D Output
Q _E	4	5	0	Q _E Output
Q _F	5	7	0	Q _F Output
Q_{G}	6	8	0	Q _G Output
Q _H	7	9	0	Q _H Output
Q _H '	9	12	0	Q _H Output
RCLK	12	14	I	RCLK Input
SER	14	18	I	SER Input
SRCLK	11	14	I	SRCLK Input
SRCLR	10	13	I	SRCLR Input
NC	_	1	_	No Connection
		16		
		11		
		16		
V _{CC}	_	20	_	Power Pin

(1) Signal Types: I = Input, O = Output, I/O = Input or Output.