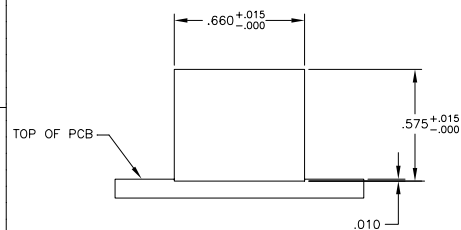
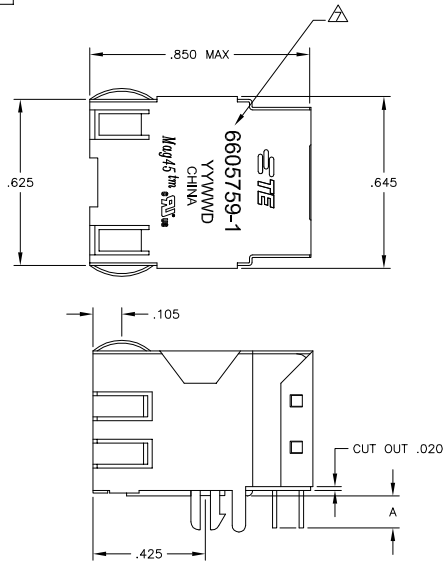
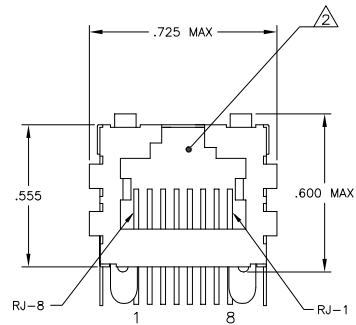


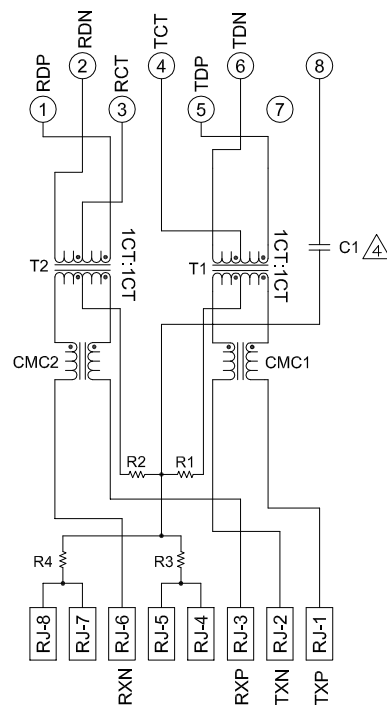
LOC	DIST	REVISIONS			
		REV	DESCRIPTION	DATE	BY
AA	22	D2	REVISED PER ECO-11-005150	21MAR11	RK
		E	ECO-11-015766	30MAY2011	EL

MECHANICAL:



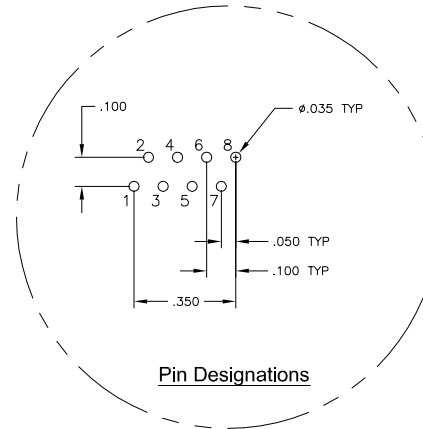
Suggested Panel Cutout

726 SERIES MAGNETIC CIRCUIT



C1 = 1000 pF, 2kV DECOUPLING CAPACITOR

R1-R4 = 75 OHMS, 1/16W RESISTORS.



Pin Designations

MATERIALS:

- HOUSING - THERMOPLASTIC PET POLYESTER FLAMMABILITY RATING UL 94V-0.
- SHIELD - 0.010" THICK, C26800 BRASS PREPLATED WITH 30μINCH MIN SEMI-BRIGHT NICKEL, SOLDER TABS POST DIPPED WITH 100μINCH MIN SAC SOLDER.
- MOD JACK CONTACTS - 0.0157" x 0.018" PHOSPHOR BRONZE, 50μINCH MIN OVERALL NICKEL UNDERPLATE, WITH SELECT 50μINCH MIN HARD GOLD FINISH PLATE.
- SOLDERTAILS WITH 100μINCH MIN MATTE TIN AND/OR SAC SOLDER DIP.

RJ45 JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUB PART F.

MAGNETICS

- IMPEDANCE: 100 OHMS
- TURNS RATIO (CHIP, CABLE): TX = 1:1, RX = 1:1
- OPEN CIRCUIT INDUCTANCE (OCL): 350μH MIN @100kHz, 0.1VRMS, 8mADC BIAS FROM 0°C TO 70°C, TX AND RX
- PERFORMANCE @ 25°C:
 - INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
 - RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 30MHz
 - 18-20LOG(f/30)dB MIN FROM 30.1MHz TO 60MHz
 - 12dB MIN FROM 60.1MHz TO 80MHz
 - CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
 - 33-20*LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
 - COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
 - ISOLATION VOLTAGE:2250VDC (MAX) FOR 60 SECONDS WITH A RISE TIME OF 500V/SEC.

IF NO CAPACITOR, TRACE IS CONTINUOUS.

5. OPERATING TEMPERATURE: FROM 0° TO +70°C.

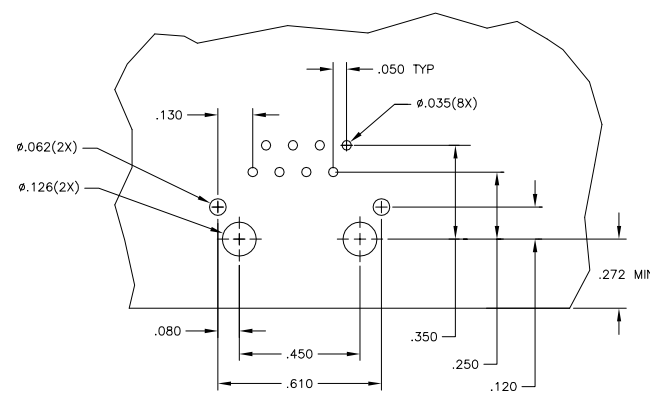
INDICATED CONNECTIONS ARE FOR NIC CONFIGURATION. THE MAGNETICS ARE SYMMETRIC TO SUPPORT AUTO-MDI/MDIX.

TE CONNECTIVITY LOGO, PART NUMBER, DATE CODE, COUNTRY OF ORIGIN AND AGENCY APPROVAL MARKING IN APPROXIMATE LOCATION SHOWN.

8. THESE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK WAVE SOLDERING TEMPERATURE IS 265°C MAX, 10 SECONDS MAX.

OBsolete PARTS: OBSOLETE CIS STREAMLINING PER

D.RENAUD/D.SINISI



Suggested PCB Layout (Component Side)

.090±.010	YES	1-6605759-1
.145±.010	NO	6605759-2
.145±.010	YES	6605759-1
DIM A	DECOUPLING CAPACITOR	PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.		SW: J. VARELA - 01MAR2005	CHK: D. FAROLE - 01MAR2005	NAME: 1X1 MAC45(TM), MODULAR JACK, 726 SCHEMATIC (10/100 ETHERNET), 726 SERIES CIRCUIT, SHIELDED, OPTIONAL DECOUPLING CAPACITOR, NO LEADS
DIMENSIONS: INCHES	QUANTITIES: REVISED ORDERING SPECIFIED:	APD: D. FAROLE - 01MAR2005	PRODUCT SPEC: 108-2100	APPLICATION SPEC: (10/100 ETHERNET), 726 SERIES CIRCUIT, SHIELDED, OPTIONAL DECOUPLING CAPACITOR, NO LEADS
0 PLG ± .	1 PLG ± .	2 PLG ± .010	3 PLG ± .005	4 PLG ± .
MATERIAL: A	FINISH: A	WEIGHT: -	SCALE: NTS	SHEET: 1 OF 1
CUSTOMER DRAWING		SIZE: A1	DATE CODE: 00779	DRAWING NO: C=6605759