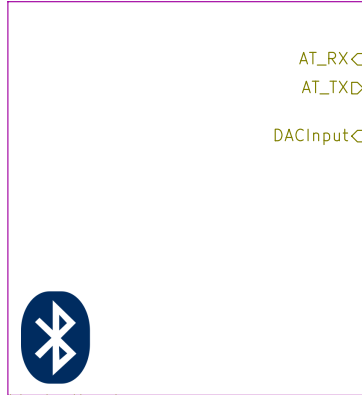


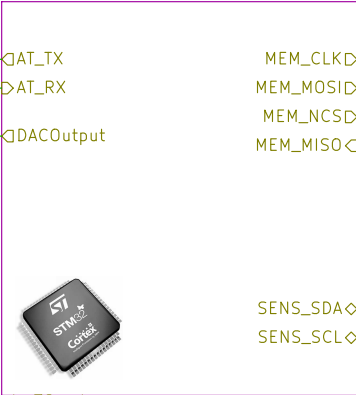
Bluetooth Sonar System for the Blind

Bluetooth



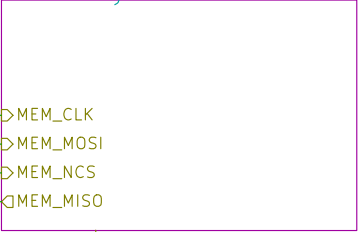
AT_RX
AT_TXD
DACInput

MCU

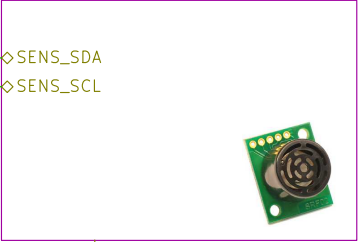


MEM_CLKD
MEM_MOSID
MEM_NCSD
MEM_MISO

Memory

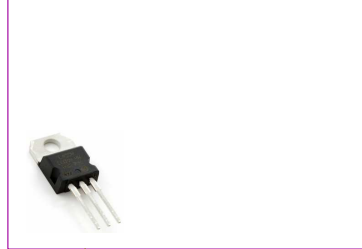


Sensors



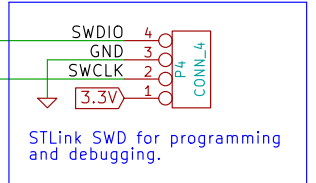
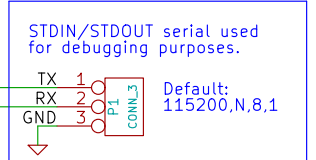
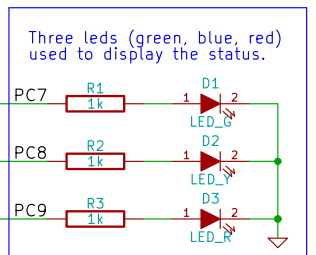
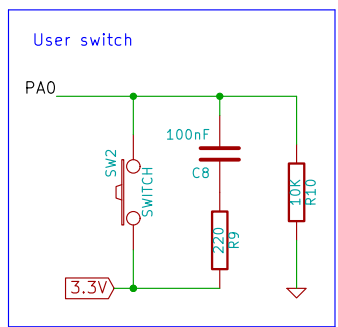
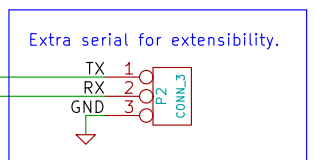
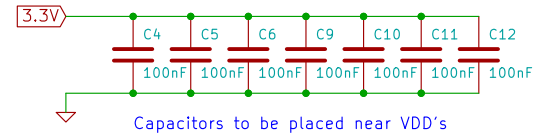
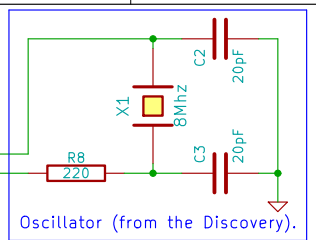
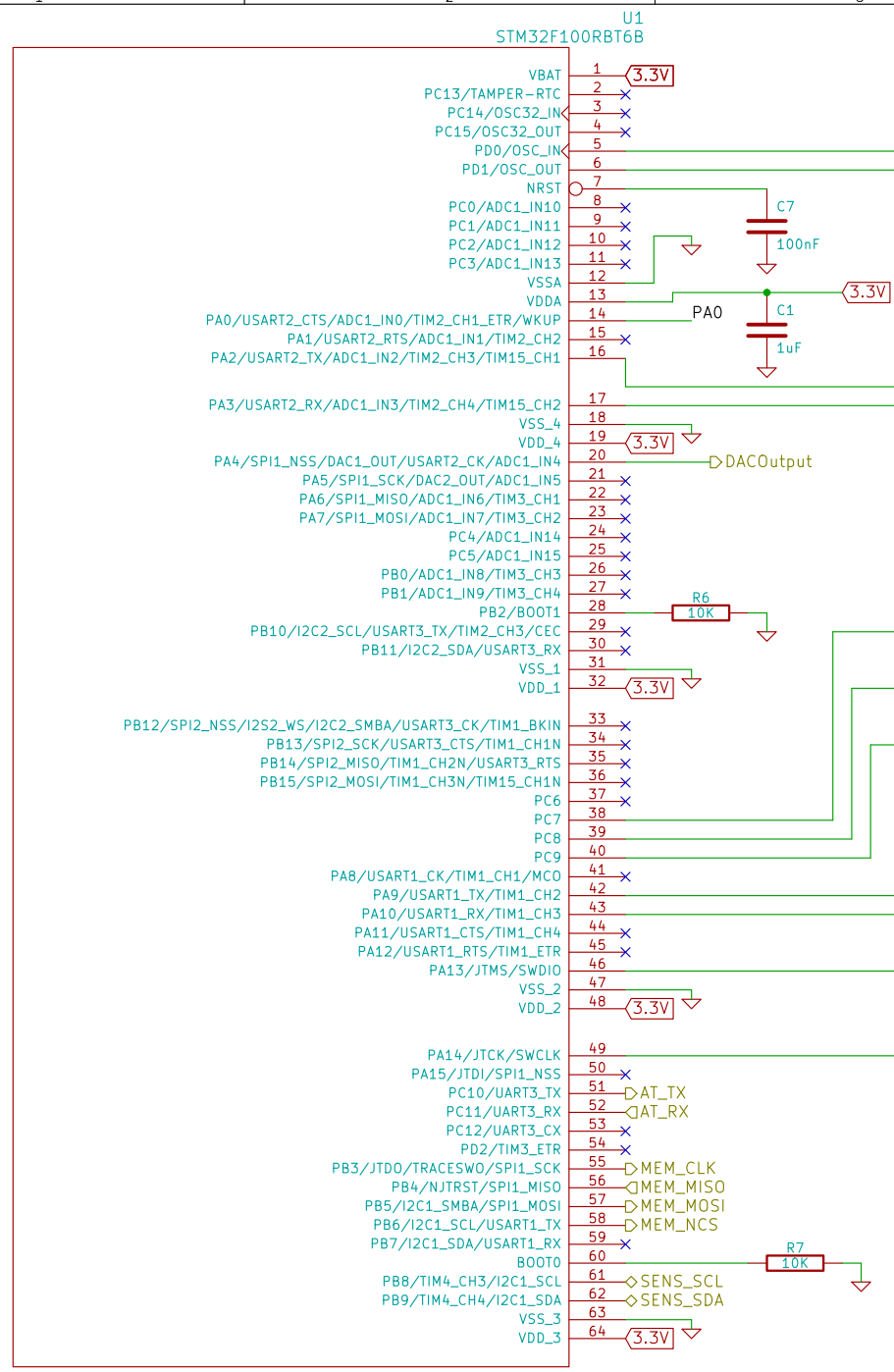
SENS_SDA
SENS_SCL

Power



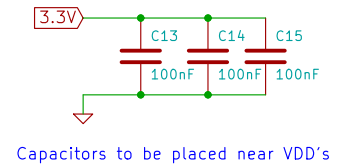
Authors: Pablo Odorico, Juan Bajo, Marcos Chaparro		
Directors: Ricardo Cayssials, Edgardo Ferro		
Departamento de Ingeniería Eléctrica y Computadoras, Universidad Nacional del Sur		
File: sonar_board.sch		
Sheet: /		
Title: Main Blocks		
Size: A4	Date: 13 apr 2012	Rev: 0.3
KiCad E.D.A.		Id: 1/6

MCU

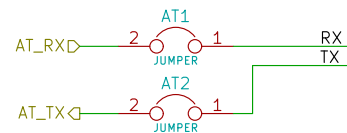


Authors: Pablo Odorico, Juan Bajo, Marcos Chaparro		
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Departamento de Ingeniería Eléctrica y Computadoras, Universidad Nacional del Sur		
File: stm32.sch		
Sheet: /MCU/		
Title: STM32 MCU		
Size: A4	Date: 13 apr 2012	Rev: 0.3
KiCad E.D.A.		Id: 2/6

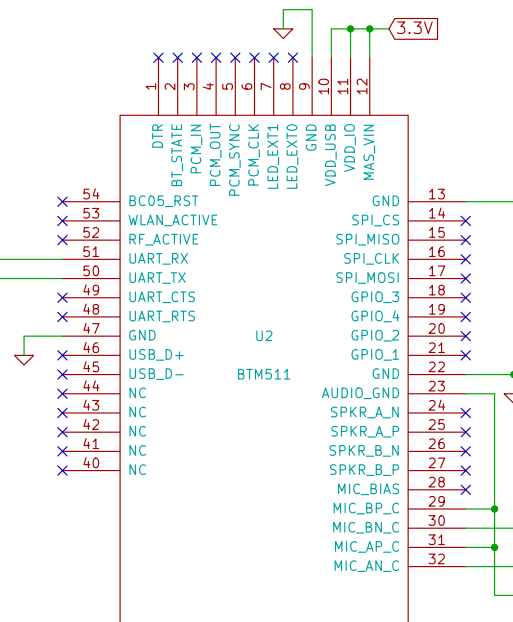
Bluetooth Module



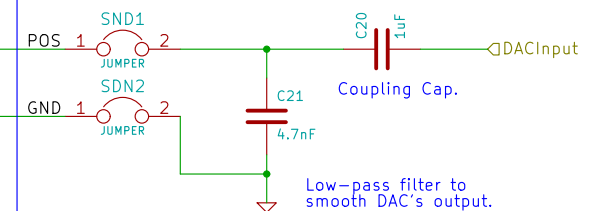
The jumpers allow the module's UART to be connected to a PC to be configured.



Default: 9600, N, 8, 1

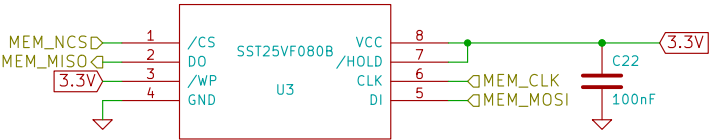


The jumpers can be used to use another audio input, or use the audio output elsewhere.



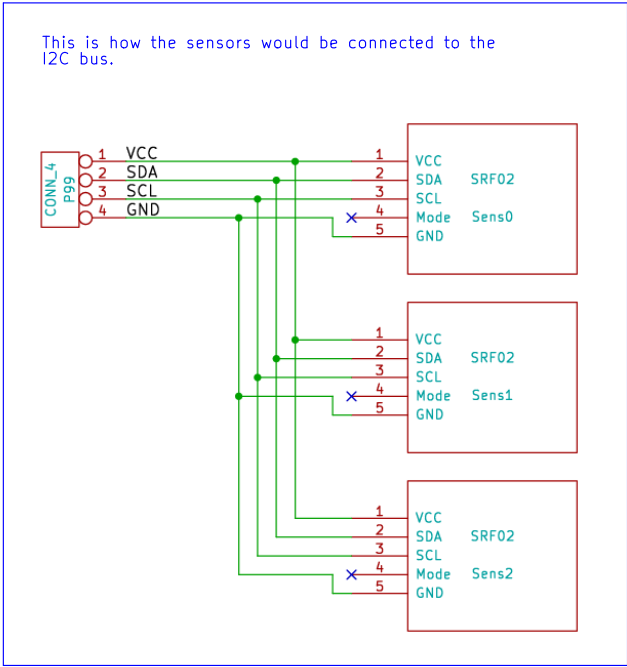
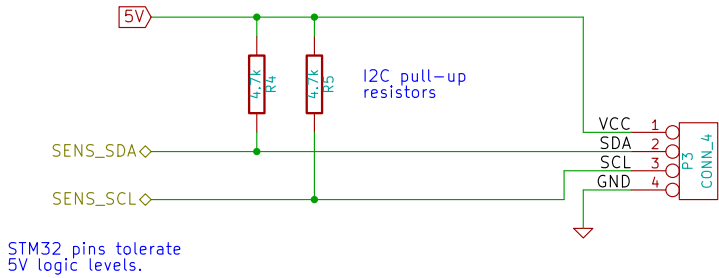
Authors: Pablo Odorico, Juan Bajo, Marcos Chaparro		
Directors: Ricardo Cayssials, Edgardo Ferro		
Departamento de Ingeniería Eléctrica y Computadoras, Universidad Nacional del Sur		
File: bluetooth.sch		
Sheet: /Bluetooth/		
Title: Bluetooth Module		
Size: A4	Date: 13 apr 2012	Rev: 0.3
KiCad E.D.A.		Id: 3/6

Flash Memory



Authors: Pablo Odorico, Juan Bajo, Marcos Chaparro		
Directors: Ricardo Cayssials, Edgardo Ferro		
Departamento de Ingeniería Eléctrica y Computadoras, Universidad Nacional del Sur		
File: memory.sch		
Sheet: /Memory/		
Title: Flash Memory		
Size: A4	Date: 13 apr 2012	Rev: 0.3
KiCad E.D.A.		Id: 5/6

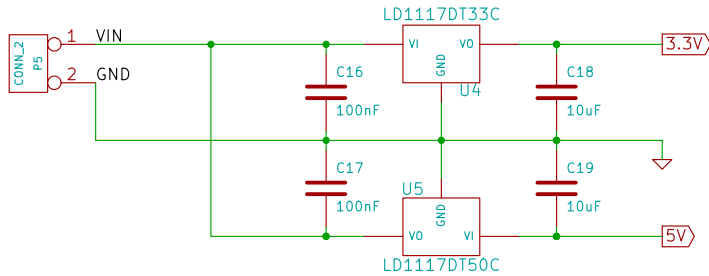
Sensors



Power Supply

LD1117xx50C dropout voltage is:
1V @ 100mA
1.15V @ 500mA
VIN should be between 6.15V and 15V.

LD1115xx Reference Design



Authors: Pablo Odorico, Juan Bajo, Marcos Chaparro		
Directors: Ricardo Cayssials, Edgardo Ferro		
Departamento de Ingeniería Eléctrica y Computadoras, Universidad Nacional del Sur		
File: power.sch		
Sheet: /Power/		
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Size: A4	Date: 13 apr 2012	Rev: 0.3
KiCad E.D.A.		Id: 6/6