

**No late turn-in accepted**

**The schematic you draw will help you design the pipelined RISC-V (You mostly likely should have the schematic in handy throughout the final project, especially when you debug your design with ModelSim).** So, while drawing the schematic, **try to think why you should have the input and output ports listed in the modules, and make such connections.** It would be best if you are able to draw a schematic in one page for `RV32I_System.v` and another schematic in one page for CPU (`rv32i_cpu.v`), so you can see the whole flow with bird's-eye view. Again, this will be used as your reference throughout the final project. **So, try your best to make it neat!**

- Upload **your drawing (pdf)** to Blackboard