

Long Tan Le

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Objective

An ambitious and goal-oriented young professional experienced in Computer Engineering, who is:

- ✚ Having a keen interest in Cloud/Edge Computing, Artificial Intelligence (AI), Software-Defined Networking (SDN) / Network Functional Virtualization (NFV), Cybersecurity and Reconfigurable Hardware.
- ✚ Aspiring to pursue a research career either in Academic or in Industry.
- ✚ Looking forward to the opportunity of applying for admission into Graduate Study Programs in the fields of Computer Science and Engineering.

Education

01/2018 - Present

Research and Teaching Assistant

Ho Chi Minh City University of Technology (HCMUT) – HCMC, VN

- ✚ Teaching Assistant of Faculty of Computer Science & Engineering, HCMUT
- ✚ Researcher of Faculty of Computer Science & Engineering, HCMUT

09/2013 – 04/2018

Honors Degree of Bachelor of Engineering in Computer Engineering

Ho Chi Minh City University of Technology (HCMUT) – HCMC, VN

- ✚ Classification: **Very good** (ranking top 10%)
- ✚ CGPA: **3.5/4**

Research Interest



Cloud/Edge Computing



Artificial Intelligence



Cyber Security



SDN/NFV



Reconfigurable Hardware

Professional Skill

English Proficiency

IELTS 6.5
(No band belows 6.0)

Good command /
Good working knowledge

Programming Language

C/C++

Verilog HDL

Java

Python

JavaScript

NodeJs

Tools

FPGA Platforms

IoT Hardware Platforms

Networking Platforms

Web Platforms

Personal Skill

- ✚ Critical Thinking
- ✚ Team Working
- ✚ Time Management
- ✚ Quick Learning
- ✚ Problem Analytical
- ✚ Organization

Activities and Awards

- ✚ Research School 2018: “**Enabling Technologies for IoT Applications & Smart City**”, University of Technology Sydney & HCMUT, HCMC, May 2018
- ✚ Certificate for joining the final round of the 20th Eureka Scientific Research Award
- ✚ Awards given for good student at HCMUT in junior year and senior year

Research Experience

01/2019 – now

On-going Research

Hardware/Software Co-design for Deep Learning Acceleration

- **Role:** Key Researcher
- **Field:** FPGA, Deep Learning, Hardware Accelerator
- **Task:** Devise a novel solution for accelerating deep neural networks via a hardware and software co-design approach.

01/2019 – now

On-going Research

Cloud-based IoT Platform for Smart Parking System

- **Role:** Key Researcher
- **Field:** Edge/Cloud Computing, Wireless Sensor Network
- **Task:** Propose a cloud-managed wireless sensor network based on LoRa to operate a smart parking system.

01/2018 – 01/2019

Research Project
(Sponsored by VNU-HCM)

OpenFlow-based Secure Switching System for Cloud Environments

- **Role:** Key Researcher
- **Field:** SDN/NFV, FPGA, Cybersecurity, Cloud Computing
- **Task:** Propose a secured framework based on parallel hardware switching devices for SDN.
- **Achievement:** Gained in-depth knowledge about SDN/NFV, Cloud Computing, Network Security, Reconfigurable Hardware.

01/2018 – 01/2019

Research Project
(Sponsored by HCMUT)

HPS-SDN: A High-Performance Security Model for SDN

- **Role:** Key Researcher
- **Field:** SDN/NFV, FPGA, Cybersecurity
- **Task:** Design, implement and integrate a distributed SDN Controllers clustering to improve fault-tolerance, availability, security, and performance.
- **Achievement:** Published a paper on an IEEE International Conference.

09/2017 – 01/2018

Bachelor Thesis

Integrated Security Features for Software-Defined Networking

- **Role:** Leader
- **Field:** SDN/NFV, FPGA, Cybersecurity
- **Task:** Propose and implement an Anomaly Detection and Mitigation system for SDN using some Machine Learning techniques.
- **Achievement:** Granted a score of 9.96/10 (Top 2 in Faculty)

01/2017 – 09/2017

Research Project
(Sponsored by HCMUT for Student)

DDoS Attacks Protection in SDN using NetFPGA-based OpenFlow Switch

- **Role:** Leader
- **Field:** SDN/NFV, FPGA, Cybersecurity
- **Task:** Propose and implement security modules in SDN Controller to detect and mitigate TCP SYN Flooding attacks aimed to SDN.
- **Achievement:** Reached the final round of the 20th Euréka Scientific Research Award organized by the Ho Chi Minh Communist Youth Union of Ho Chi Minh City, Vietnam.

Publication

1. T. N. Thinh, L. T. Le, M. A. T. Tran “**ODL-ANTIFLOOD: A Comprehensive Solution For Securing OpenDayLight Controller**”, in 2018 *International Conference on Advanced Computing and Applications (ACOMP)*, HCMC, Nov 2018, **Corresponding Author**.

Reference

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