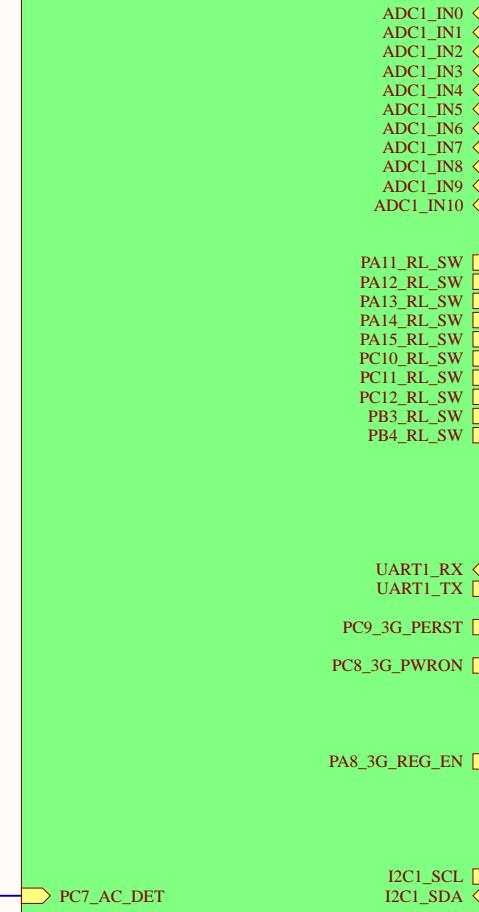
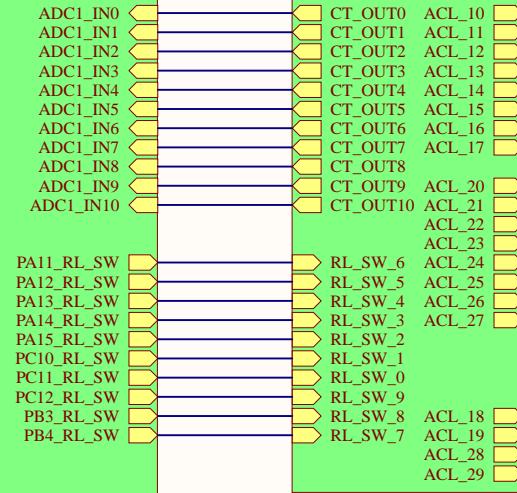
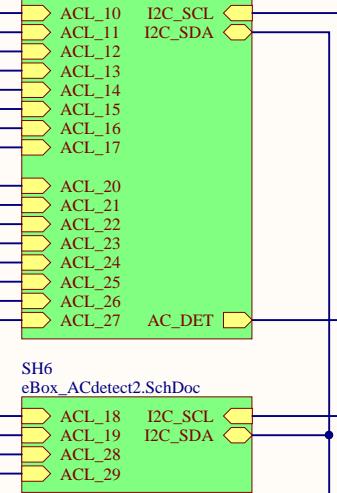
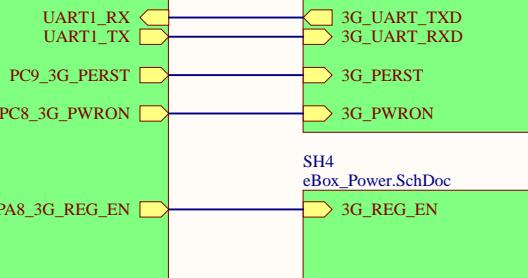
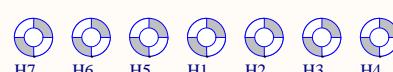


History		
Date (d/m/y)	Version	Note
02/12/2019	0.1	Draft version
04/12/2019	0.2	- Add resistors for CTs - Add 1.8V power for CTs - Add buffer ICs for MCU - Replace single transistors by darlington transistors array. - Fixbug
07/12/2019	0.3	- Add LED signal for relay - Change CTs - Add feedback signals from AC ports - Add WAKE-UP pin for 3G module
09/12/2019	0.4	- Add Buzzer - Add PCB document - Fixbug
12/12/2019	0.5	[x] Thêm chân nạp cho STM32 [x] Chính số cho nguồn 3G (R1,R2) hoặc recommend cái nào thì tò đậy [x] Thêm test point cho máy chở nguồn 12v, 3v3, 1v8 [x] Đèn led blink trạng thái chân B2 [x] Sim 5320 cũng ra hàng rào hoặc test point uart để test
24/12/2019	0.6	- Add PCB layout
29/12/2019	0.7	- Fix bug
27/02/2020	1.0	-

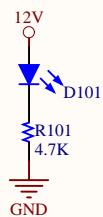
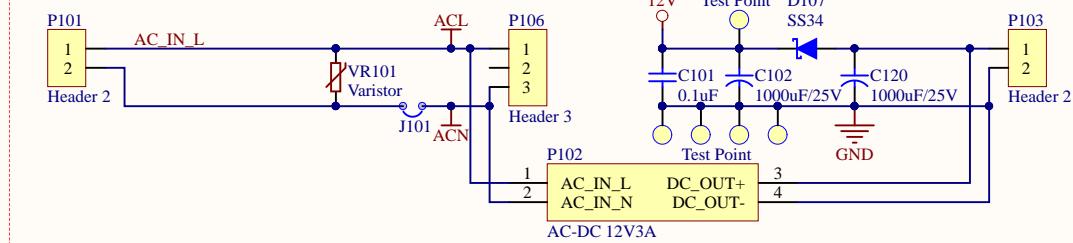
SH1
eBox_MCU.SchDocSH2
eBox_Peripherals.SchDocSH5
eBox_ACdetect.SchDocSH6
eBox_ACdetect2.SchDocSH3
eBox_3G.SchDocSH4
eBox_Power.SchDocTitle **eBox_Notes**

Size: A4	Number: 1	Revision: 1.0
Date: 18/03/2020	Time: 2:36:25 PM	Sheet 1 of 7
File: eBox_Notes.SchDoc		

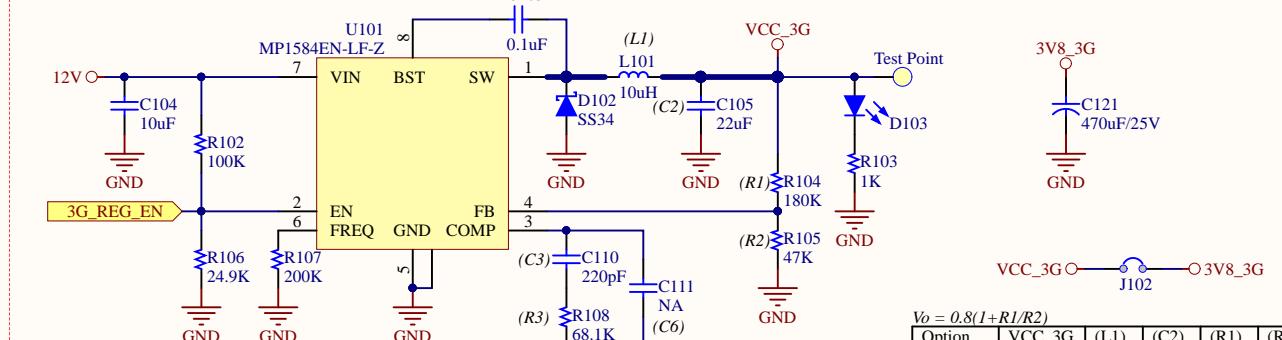
NPN Lab

1 2 3 4

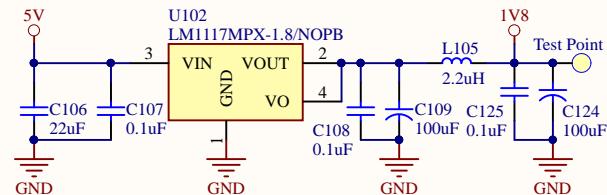
AC-DC MODULE



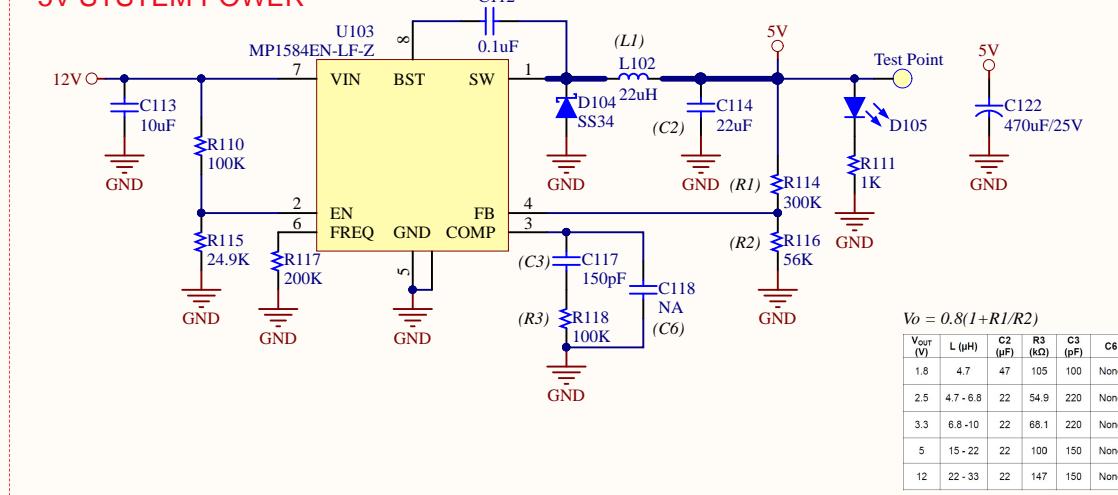
POWER SUPPLY FOR 3G MODULE



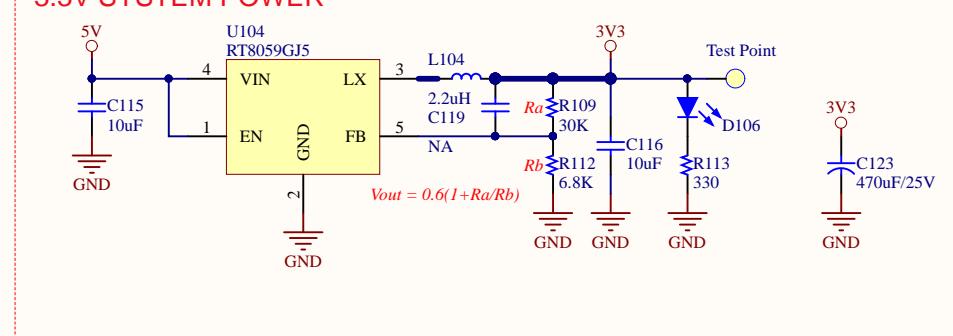
1.8V FOR TA12



5V SYSTEM POWER



3.3V SYSTEM POWER

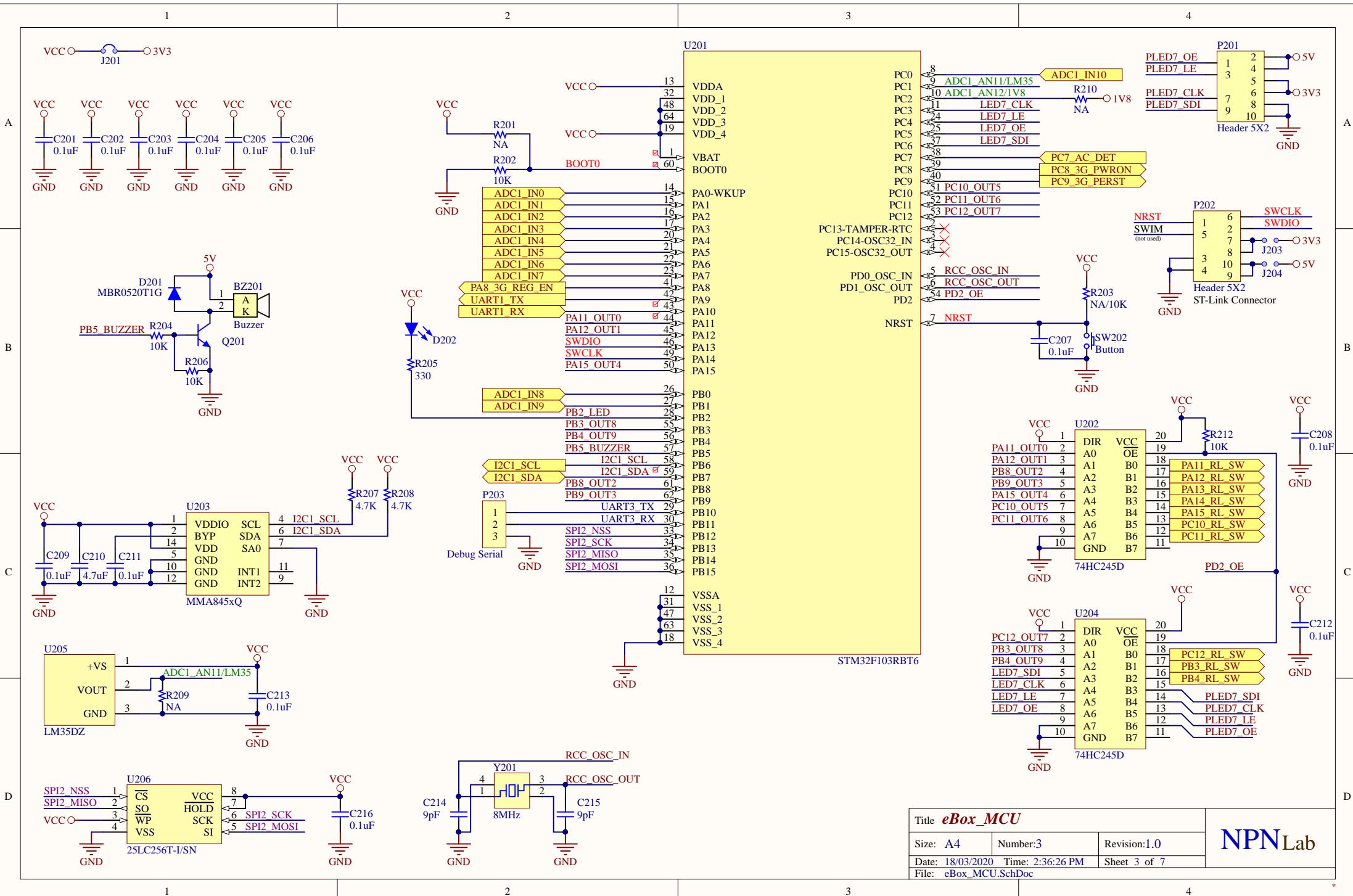


Title: eBox_Power

Size: A4 Number: 2 Revision: 1.0
Date: 18/03/2020 Time: 2:36:25 PM Sheet 2 of 6
File: eBox_Power.SchDoc

NPN Lab

1 2 3 4 *

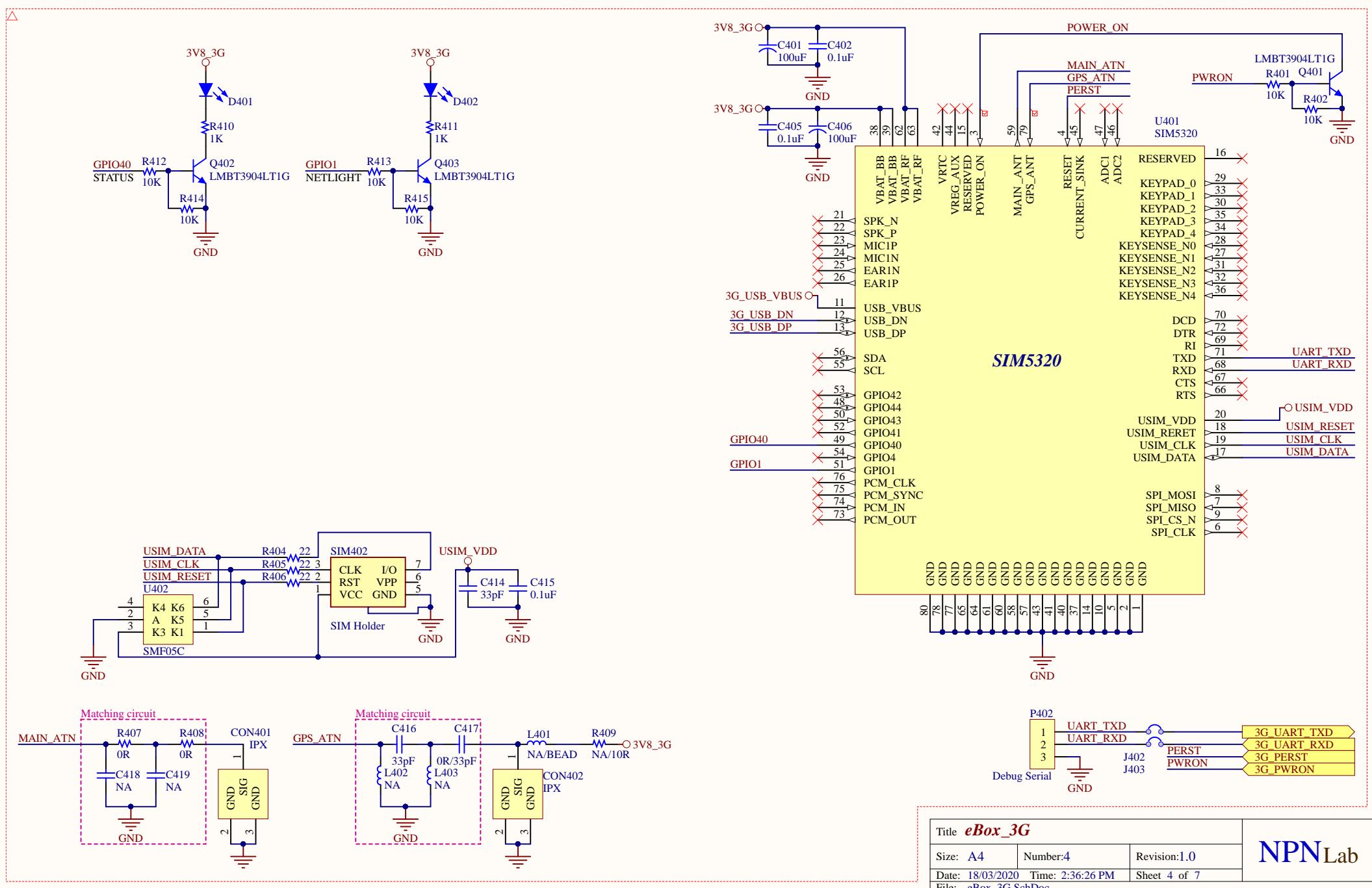


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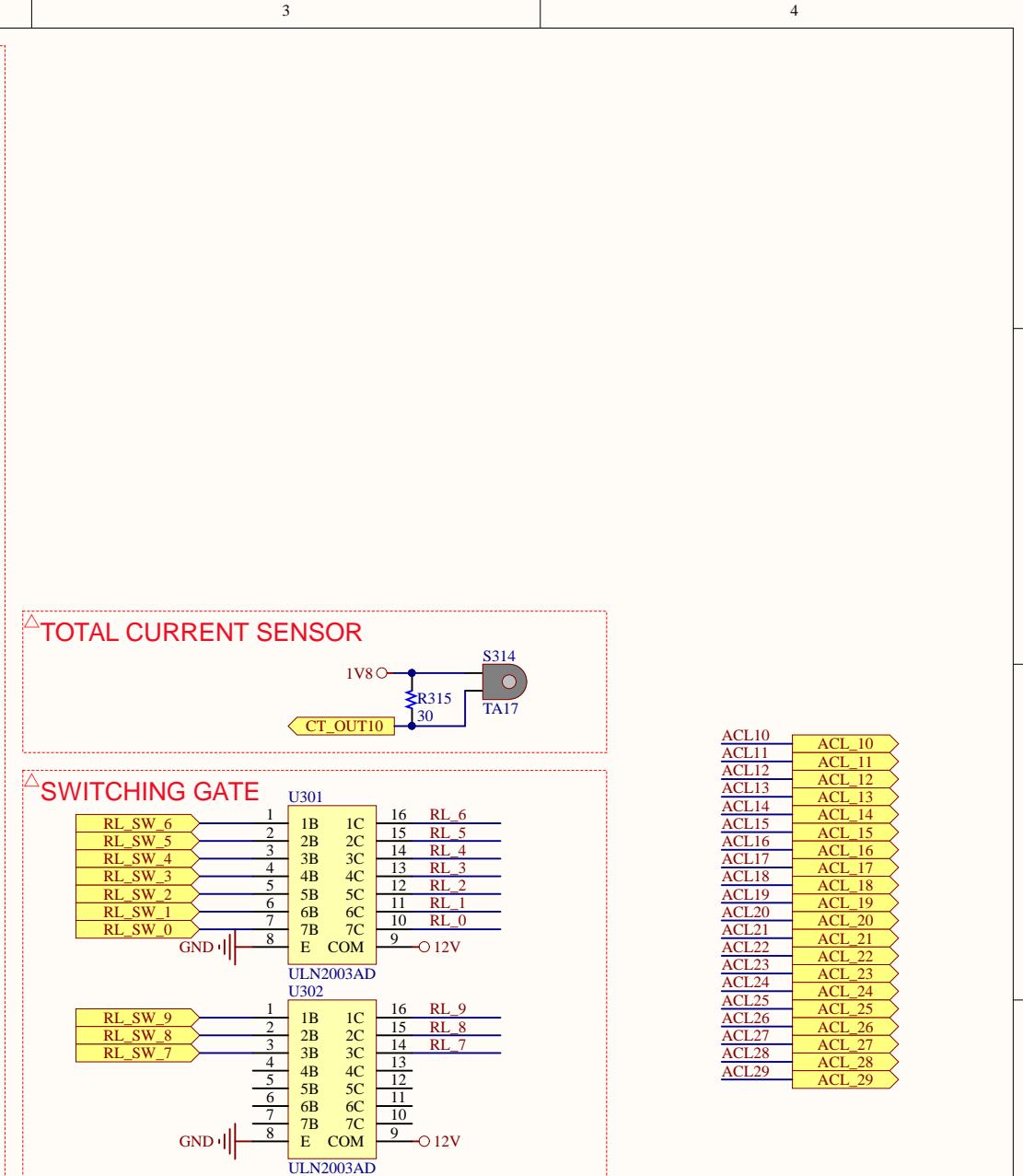
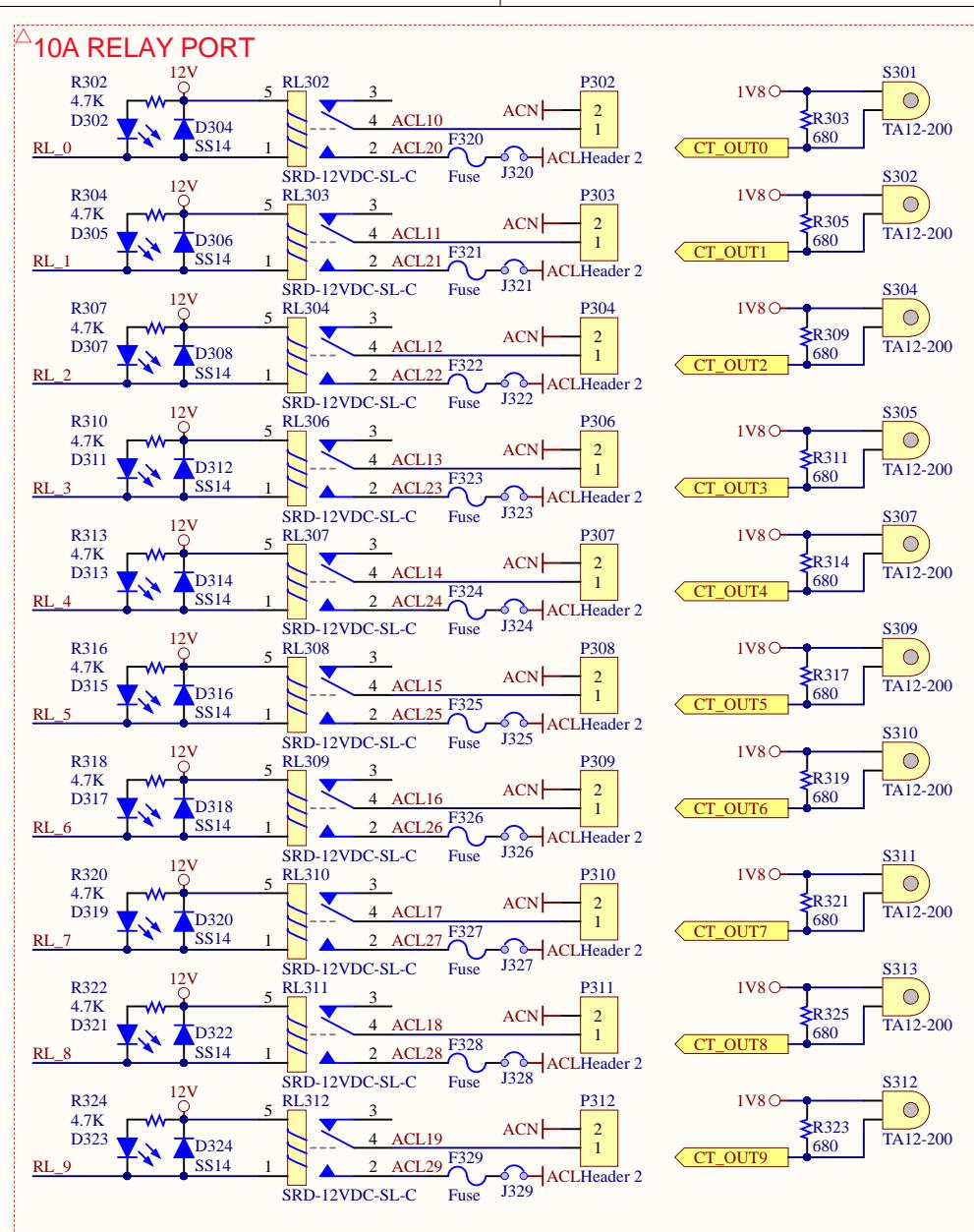


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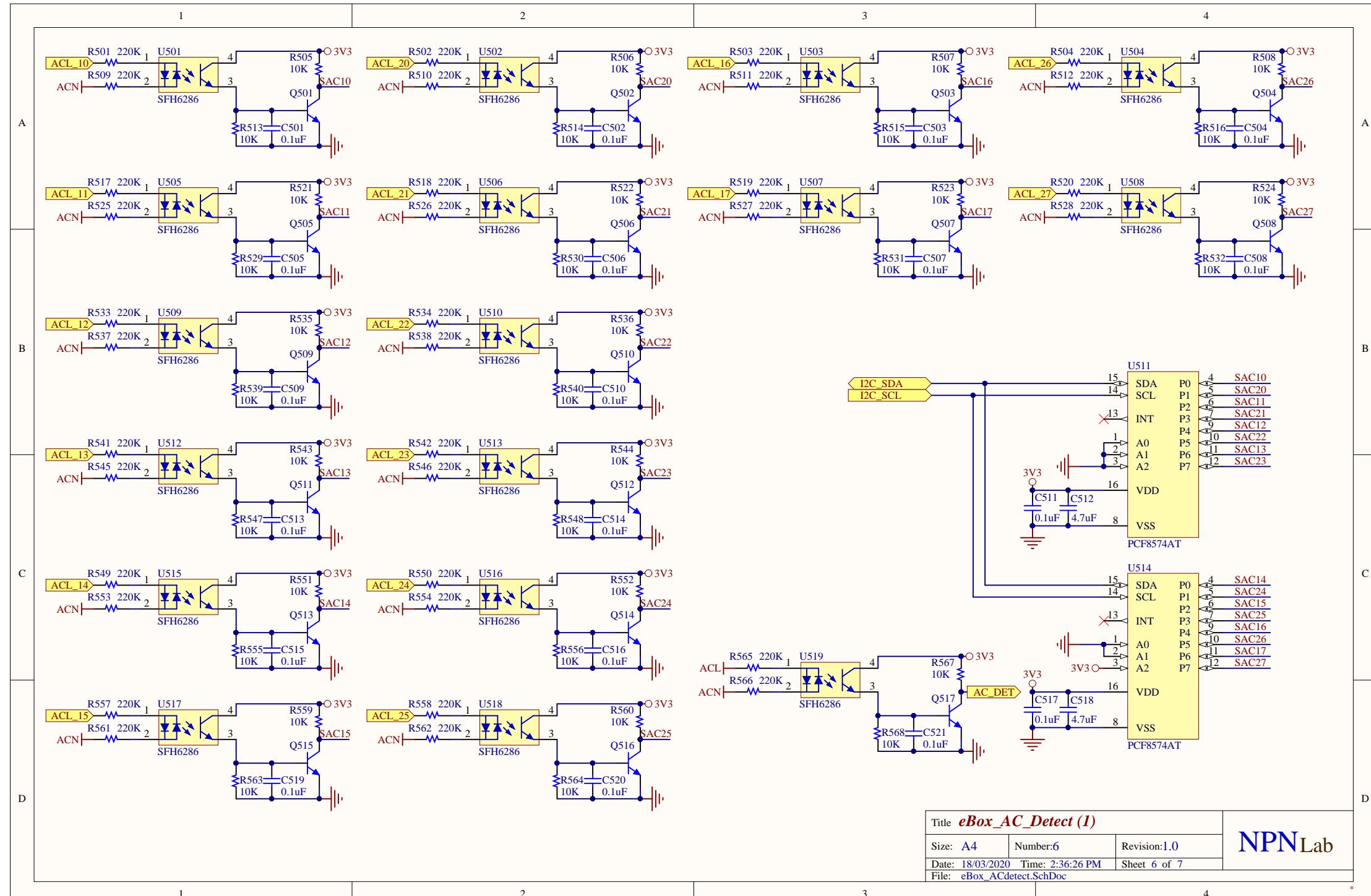
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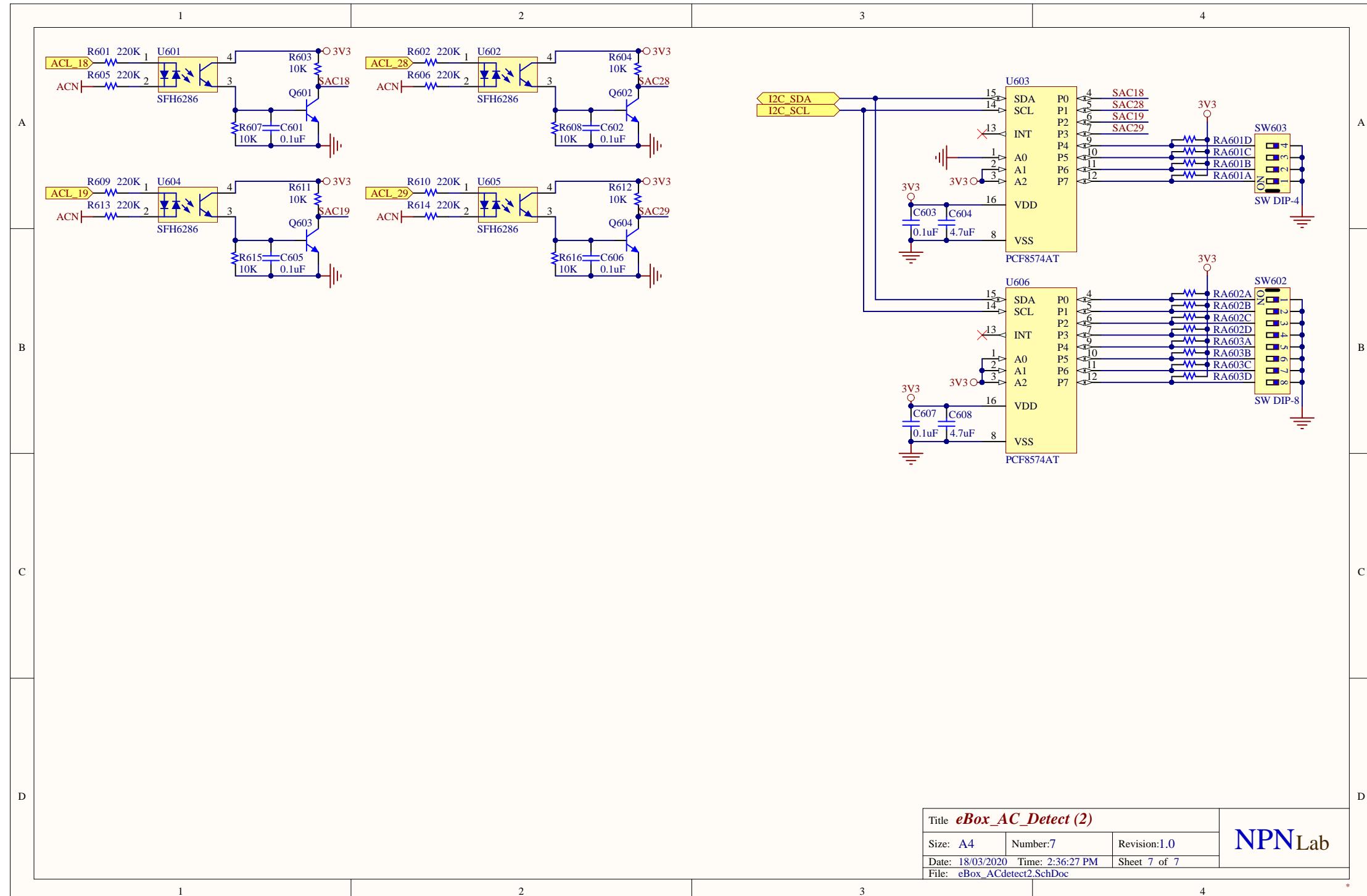


Title **eBox_Peripherals**

Size: **A4** Number: **5** Revision: **1.0**
 Date: **18/03/2020** Time: **2:36:26 PM** Sheet **5** of **7**
 File: **eBox_Peripherals.SchDoc**

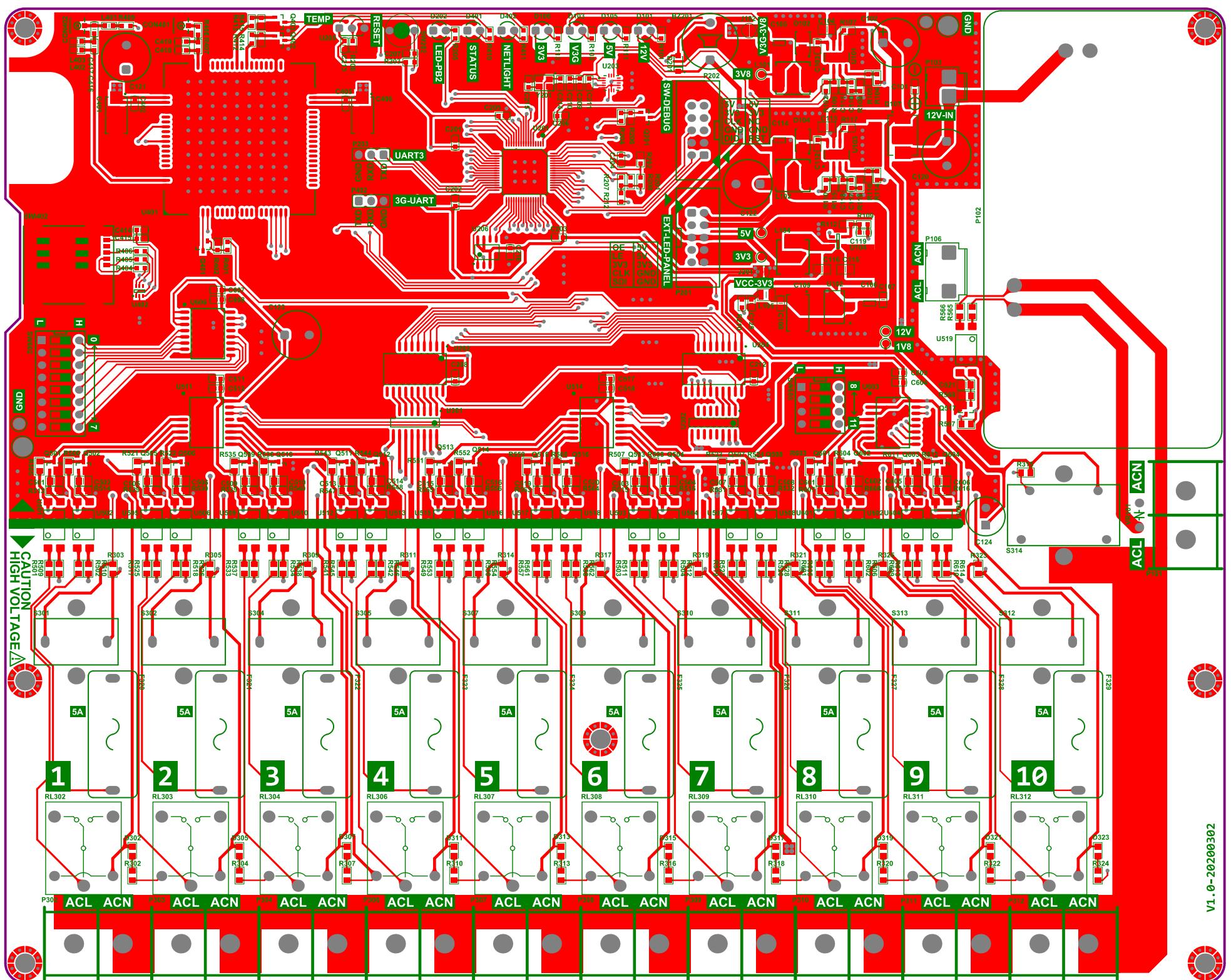
NPN Lab

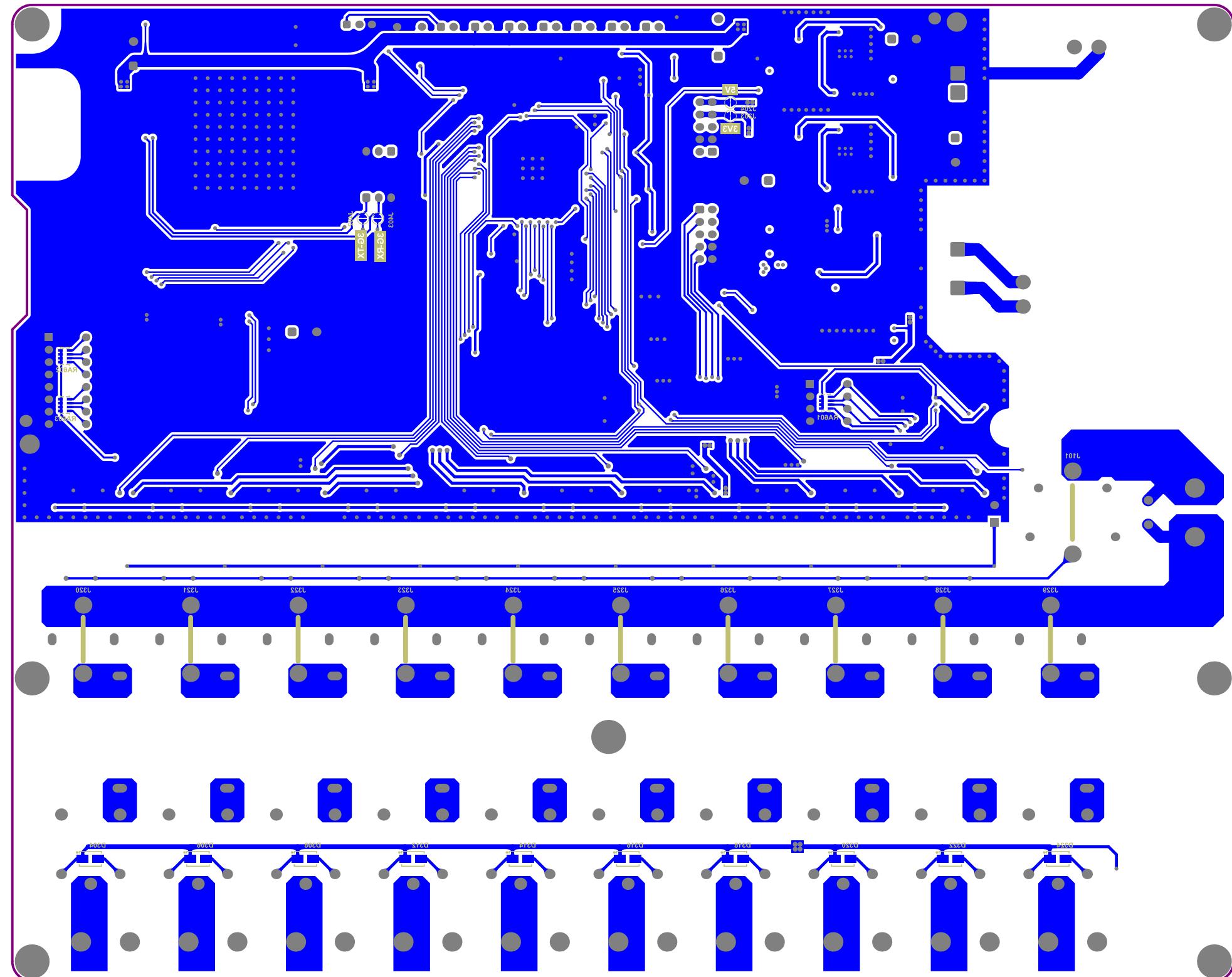


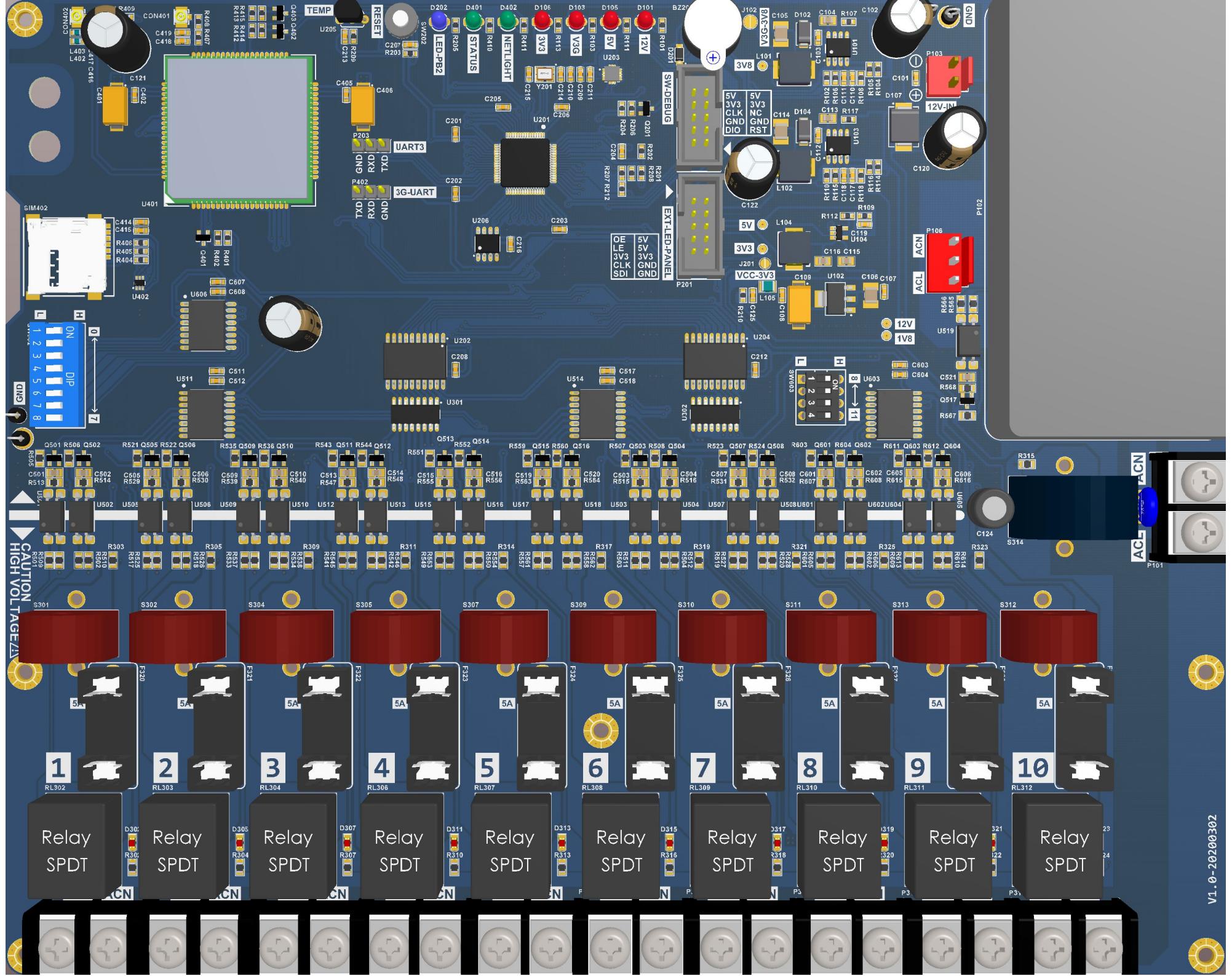


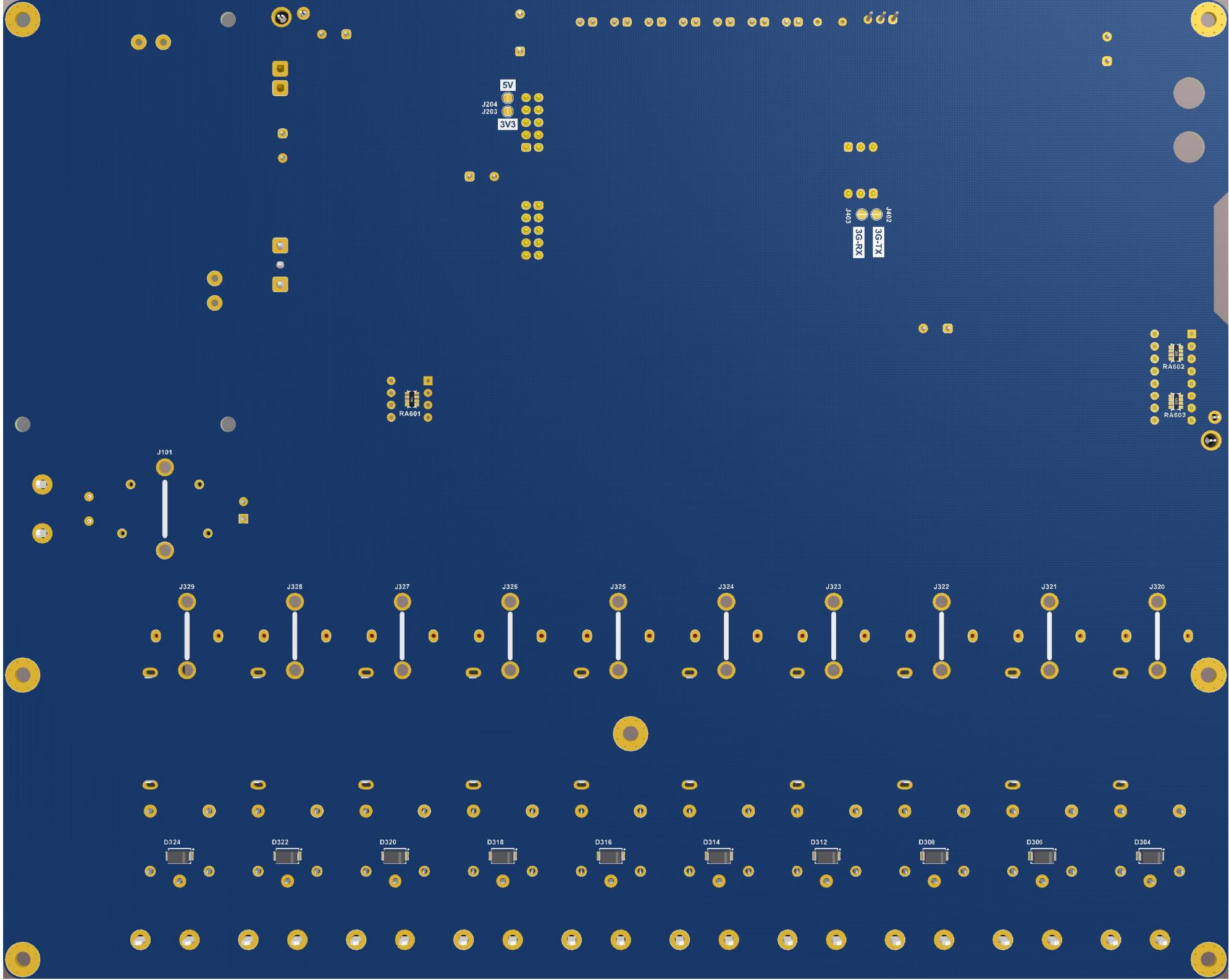
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Size: A4	Number: 7	Revision: 1.0
Date: 18/03/2020	Time: 2:36:27 PM	Sheet 7 of 7
File: eBox_ACdetect2.SchDoc		

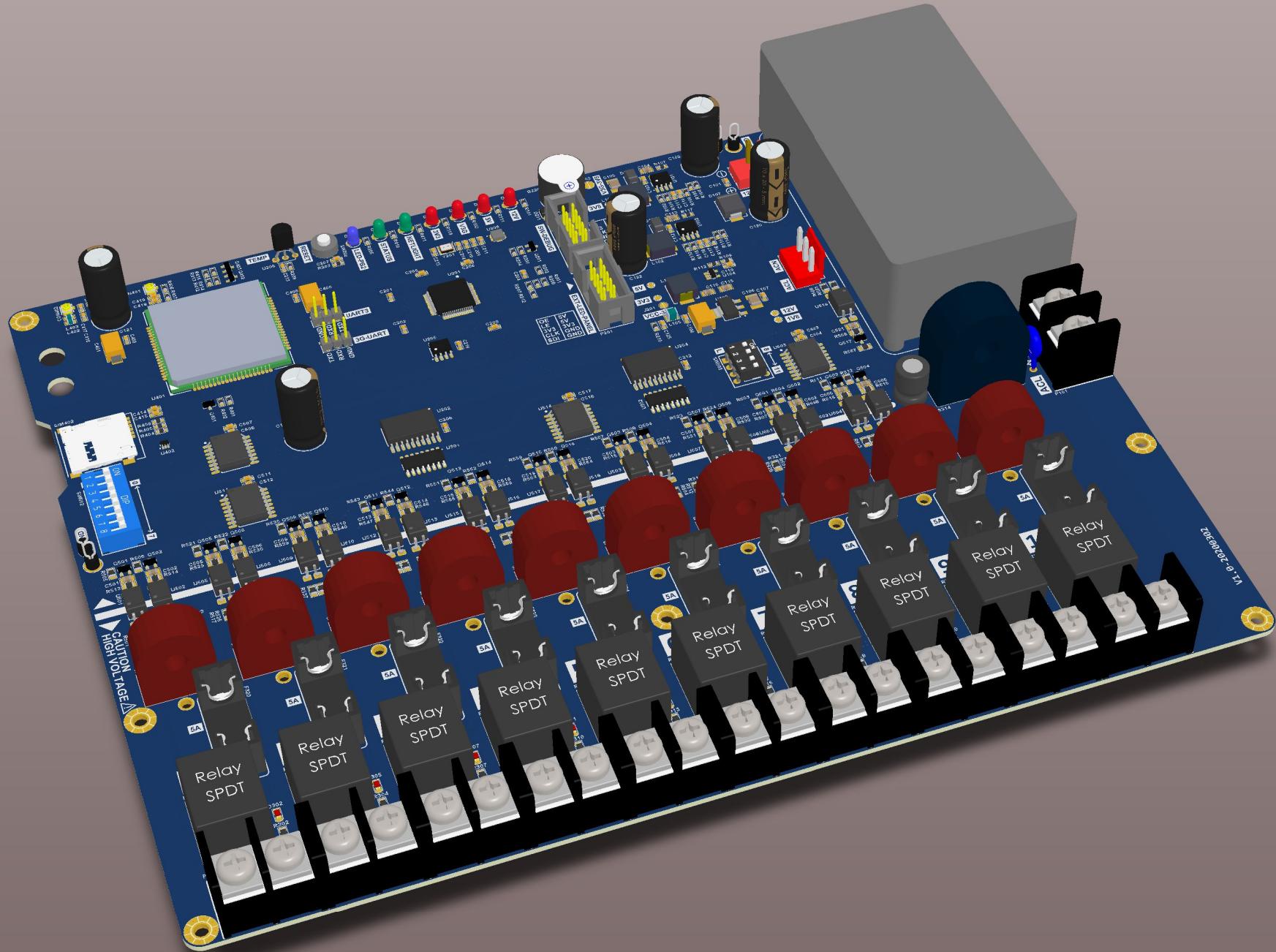
NPN Lab

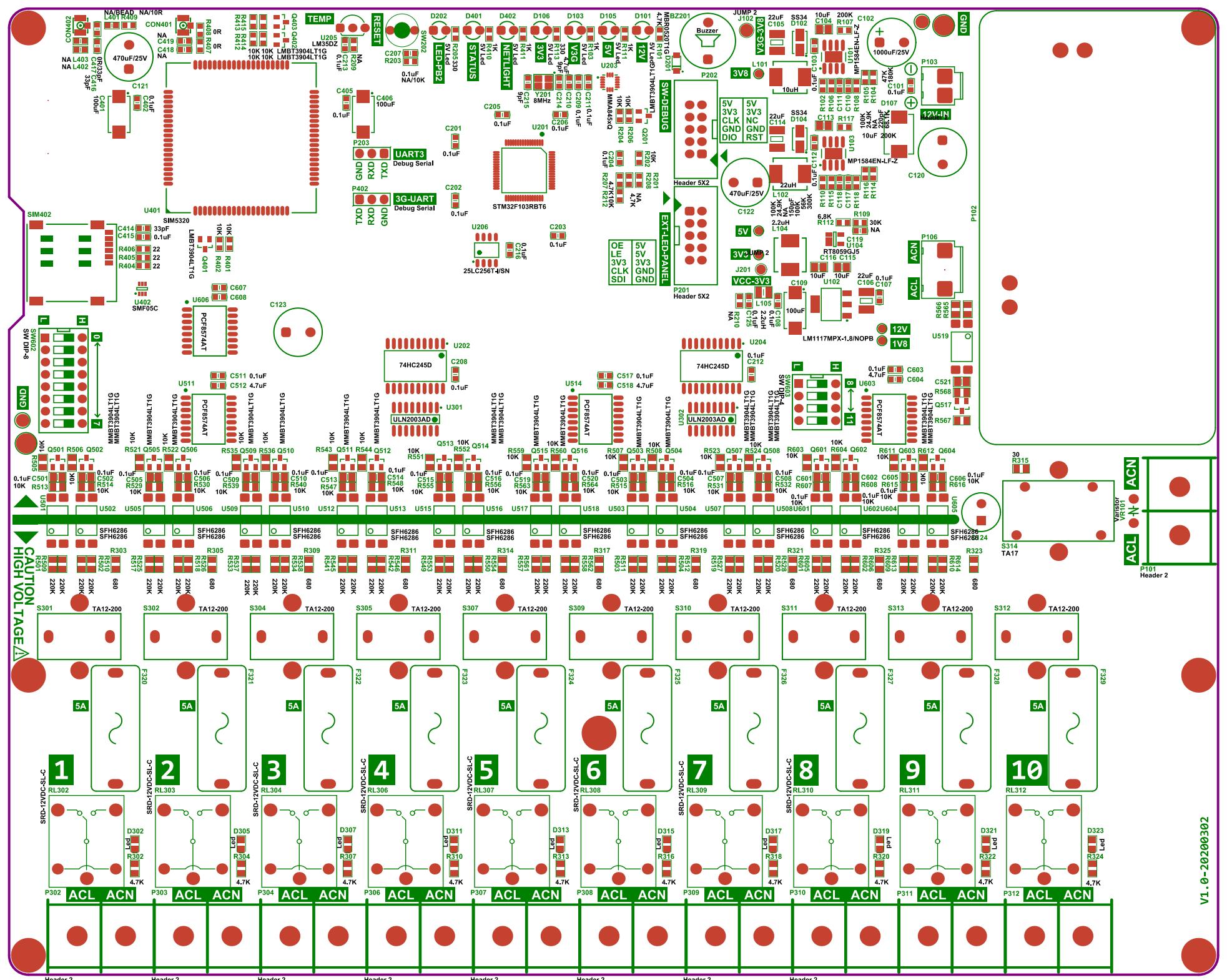


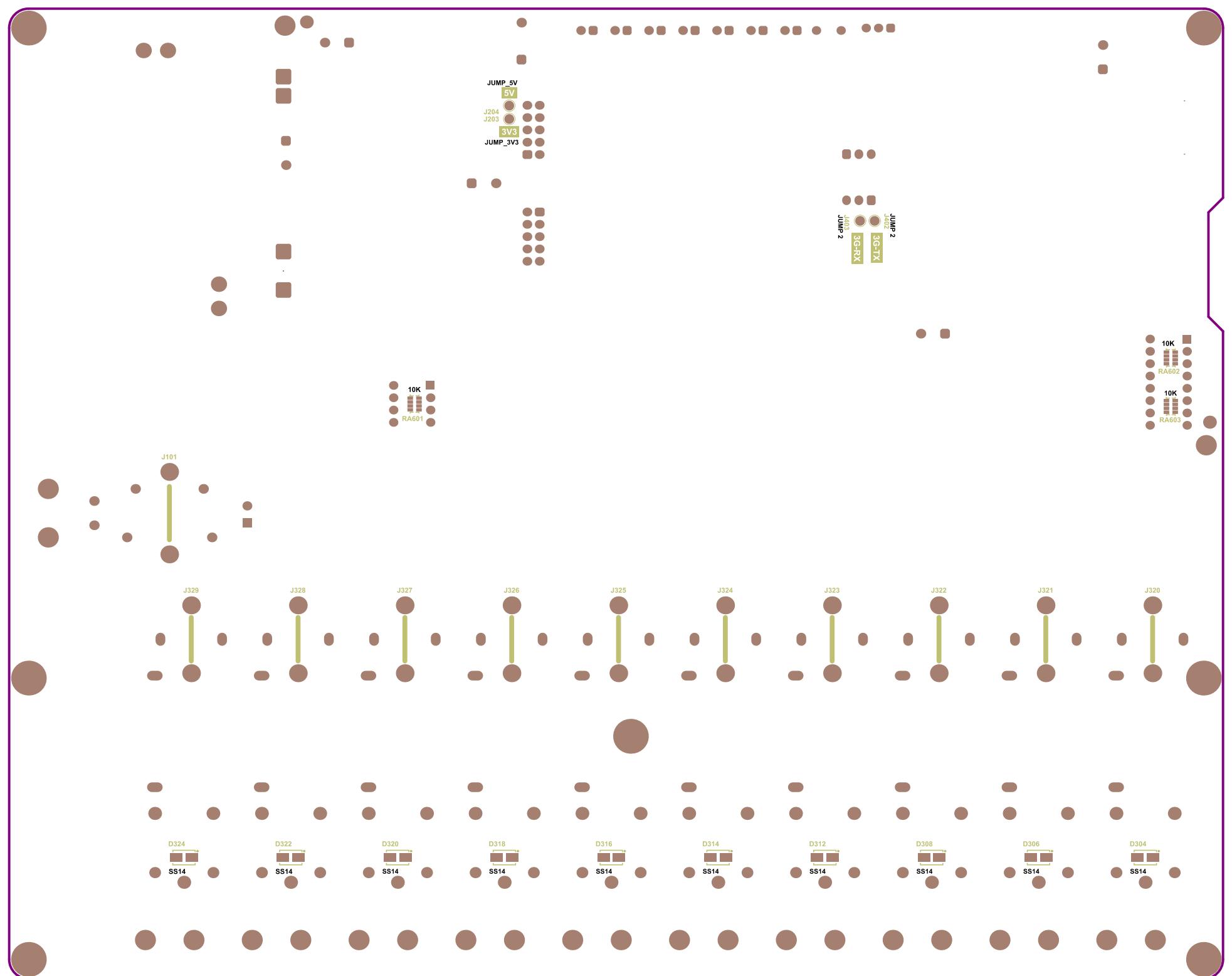












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