hsk-ecu-stub 65

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## **Chapter 1**

## **HSK XC878 Electronic Control Unit Stub**

This project is a stub to clone when starting the development of a new device.

The hsk-libs repository is included as a submodule, a git submodule update may be necessary to populate it.

See the Makefile for documentation on build parameters and Makefile.local to overide defaults locally.

After setting the new project up, run uVisionupdate.sh to update the  $\mu$ Vision project file. The u $\leftarrow$ Visionupdate.sh script also generates the list of ISR callbacks for  $\mu$ Vision's call tree/overlaying engine.

The list generation only recognizes direct assignments to  $hsk\_isr<number>$ . <source=> and calls with function pointer arguments to:

- hsk\_timer[01]\_setup()
- hsk\_ex\_channel\_enable()

More complex assignments might require an update of hsk-libs/scripts/overlays.awk.

See also

PDF Version

# **Chapter 2**

# File Index

## 2.1 File List

Here is a list of all documented files with brief descriptions:

config.h		
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main.c		
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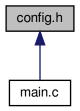
# **Chapter 3**

## **File Documentation**

## 3.1 config.h File Reference

Configuration for the Infineon XC800 Starter Kit.

This graph shows which files directly or indirectly include this file:



#### **Macros**

• #define CLK 800000UL

The external oscilator clock frequency.

• #define CAN0\_BAUD 1000000

The CAN0 baud rate in bits/s.

• #define CAN1\_BAUD 1000000

The CAN1 baud rate in bits/s.

• #define CAN0\_IO CAN0\_IO\_P10\_P11

The CAN0 IO pin configuration RX P1.0 TX P1.1.

• #define CAN1\_IO CAN1\_IO\_P14\_P13

The CAN1 IO pin configuration RX P1.4 TX P1.3.

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### 3.1.1 Detailed Description

Configuration for the Infineon XC800 Starter Kit.

Author

kami

#### 3.1.2 Macro Definition Documentation

3.1.2.1 CAN0\_BAUD

#define CANO\_BAUD 1000000

The CAN0 baud rate in bits/s.

3.1.2.2 CAN0\_IO

#define CAN0\_IO CAN0\_IO\_P10\_P11

The CAN0 IO pin configuration RX P1.0 TX P1.1.

3.1.2.3 CAN1\_BAUD

#define CAN1\_BAUD 1000000

The CAN1 baud rate in bits/s.

3.1.2.4 CAN1\_IO

#define CAN1\_IO CAN1\_IO\_P14\_P13

The CAN1 IO pin configuration RX P1.4 TX P1.3.

3.1.2.5 CLK

#define CLK 800000UL

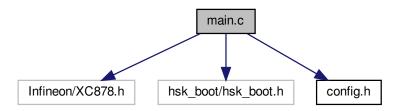
The external oscilator clock frequency.

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### 3.2 main.c File Reference

Device main.c Stub.

```
#include <Infineon/XC878.h>
#include <hsk_boot/hsk_boot.h>
#include "config.h"
Include dependency graph for main.c:
```



#### **Functions**

• void main (void)

Call init functions and invoke the run routine.

• void init (void)

Initialize ports, timers and ISRs.

• void run (void)

The main code body.

#### 3.2.1 Detailed Description

Device main.c Stub.

**Author** 

kami

#### 3.2.2 Function Documentation

```
3.2.2.1 init()
```

```
void init (
```

Initialize ports, timers and ISRs.

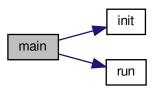
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### 3.2.2.2 main()

```
void main (
     void )
```

Call init functions and invoke the run routine.

Here is the call graph for this function:



### 3.2.2.3 run()

```
void run (
     void )
```

The main code body.

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