

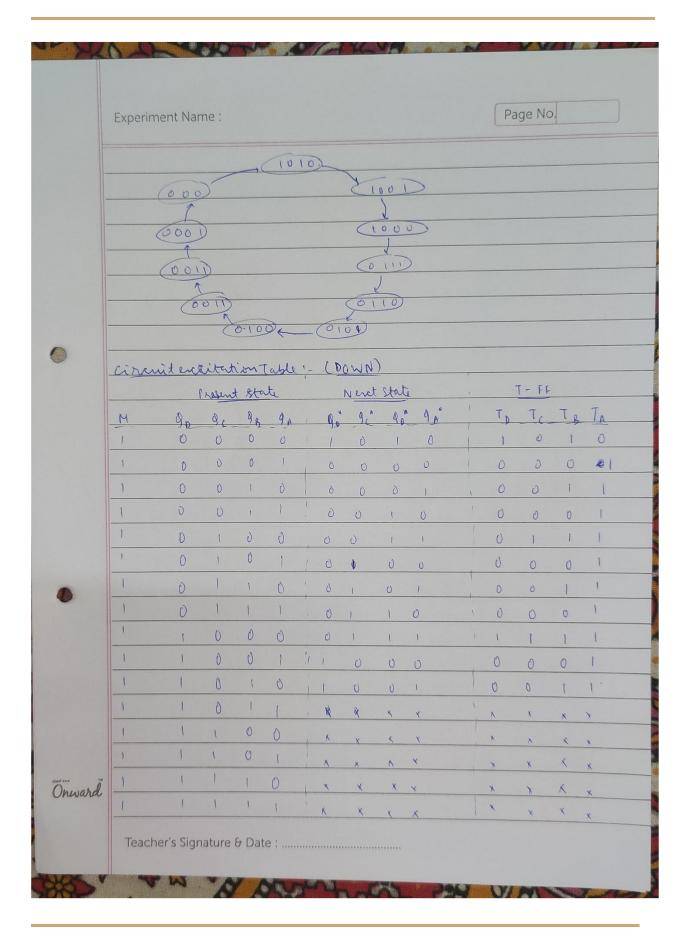
[Design a MOD-N UP/DOWN SYNCHRONOUS COUNTER]

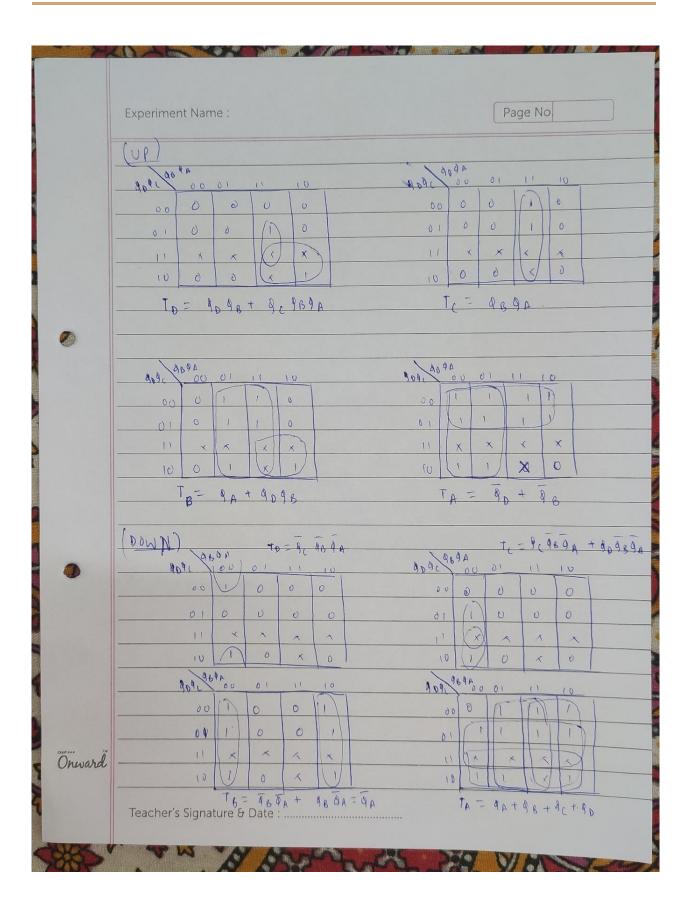
NAME: ROHIT SADHU

ROLL NO.: 002010501074

	Experiment Name: 8
	Objective: To implement a synchronous modulo UP/DOWN
	counter, where is the addition of last two ways
	dans roll no.
	Forme, n = 7+4 > n = 11 -> MOD-11.
	NO. of Flip Flops revenired >> log(n) (reiting)
	= heg (11) 73. 20, [No: of FF3 = 4.]
	20, NO. 01 FF3 - 4.1
	Excitation table for T- FF
	<u>9</u> ~ 9n+1
	0 0
	1 0 1
	1. 0.
6	
	State Diagram:
	(0000)
	(000)
	100
	(UP)
	(010)
Önward	(0110) (0101)
	Teacher's Signature & Date :
	A LA DESTRUCTION VINCEN

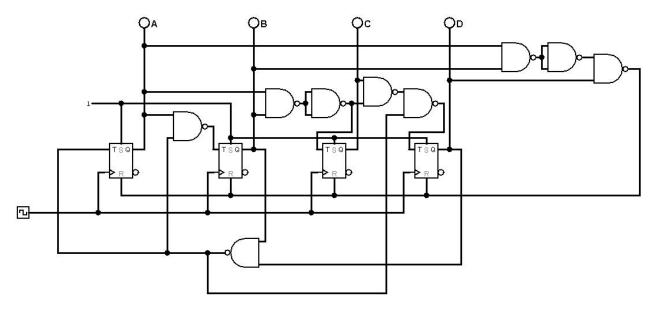
	M	90	9,	90	90	(9 4	0,1	98	9 A	1	T	TE	TB	T
	0	0	0	0	0	1	0	0	0	1	1	0	U	0	1
	0	0	0	0	1	1	0	0	- 1	0	1	0	0	1	1
	Ô	0	0	1	0	(Ò	0	1	1	-	0	0	Ō	1
	0	0	0	1	1	,	0	1	0	0		0	1	1	1
	0	0	1	O	0	1	0	1	O	1		0	0	0	1
	0	0	1	0	1	,	0	1	1	0		0	0	1	1
0	0	0	1	1	0	1	0	1	1)		0	0.	0	١
70	0	0	1	1	1		-	0	O	U	,	1	1	1	1
	0	1	0	0	0	١		0	0	1	1	0	0	0-	1
	0	1	0	0	1	1	1	0	(U	-	0.	0	1	1
	0	1	0	1	0	1	0	0	0	0	1	1	0	1	0
	0	1	0			1	4	X	X	χ	(X	Υ	(χ
	0	1	1	0	0		X	×	K	Y		X	Y)	X
	0	1	1	0	1	(K	×	X	Y	,	X	λ	X	X
 nward	0	1	1	1	0		K	X	Χ,	<		X)	X	X
	0	1	1	1			X	<	X)	1		γ	×	>	X
	Tanah	er's Sigr	antiura	4 Da	to										



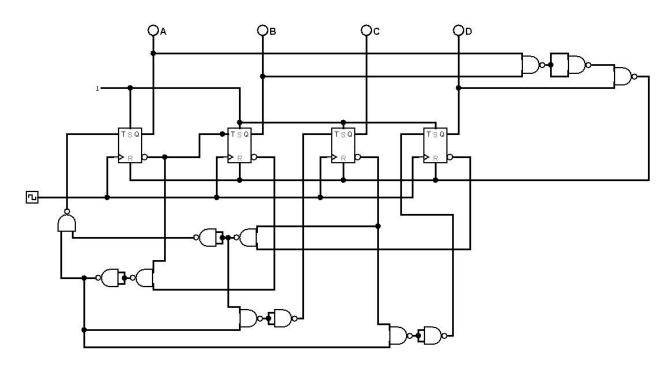


CIRCUIT DIAGRAM:

UP COUNTER:



DOWN COUNTER:



<u>T - FF:</u>

