ECE 422/522 project notes:
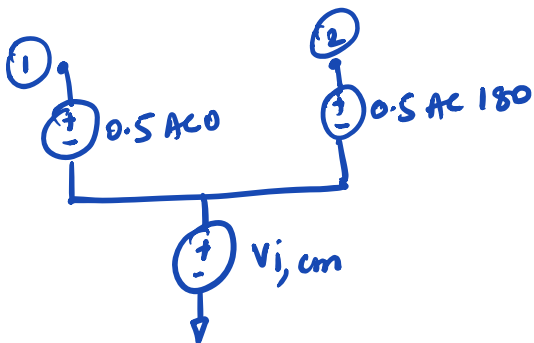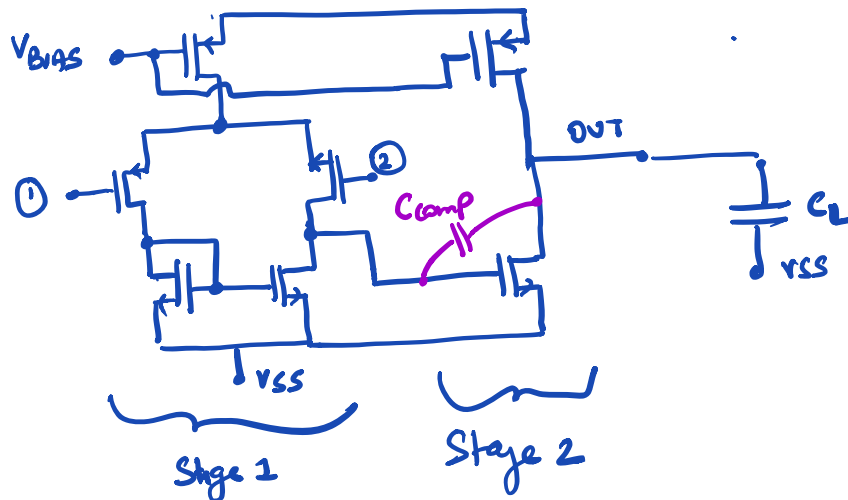
1. Topology: 2 stage works well. Other topologies can also work and are welcome.

a. generate VBIAS using transistors and resistors.

b. figure out compensation cap based on notes.