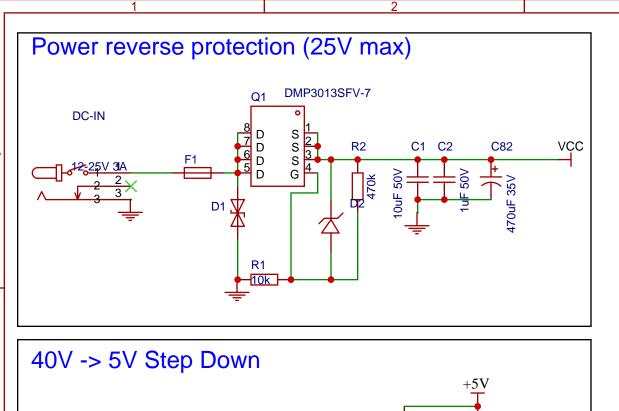
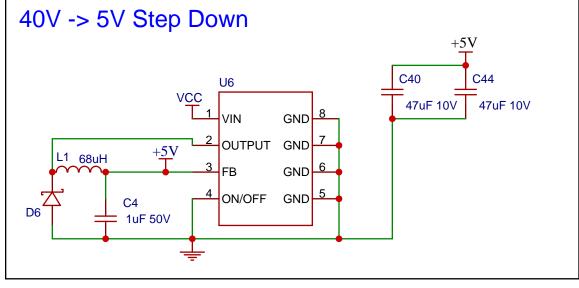
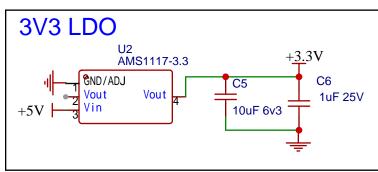


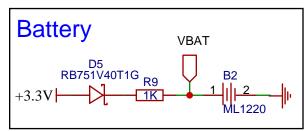
Encoders level conversion							
+3.3V C80 U8 100nF 16V M1_ENCA M2_ENCA M1_ENCB M2_ENCB M1_ENCX M2_ENCX M2_ENCX M2_ENCX M2_ENCX M2_ENCX M2_ENCX M2_ENCX M2_ENCX M2_ENCX M3 A4 A5 A6 A7 A8 0E TXB0108	C81 C81 VCCB 18 100nF 16V 18 17 M2_ENCA_IN M2_ENCB_IN M2_ENCB_IN M2_ENCB_IN M1_ENCB_IN M1_ENCX_IN M2_ENCX_IN M2_ENCX_IN M2_ENCX_IN M3_ENCX_IN M4_ENCX_IN M4_ENCX_I						

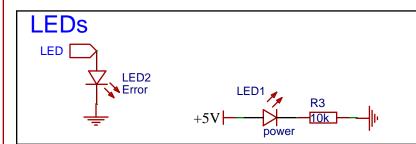
EasyEDA		V0.9	A4	zUrp Industries						
		VER	SIZE		PAGE	1	OF	3		
Reviewed	lordzurp	-	TeenAstro v3							
Drawed	lordzurp	Tart Number								
Page	Teensy				Part Numb	ωr				
Schematic Redux_v3_430				Create Da	ate	2022-07-11				
Och coch i		20duy v2 420			Update Da	ate	2022-12-18			











					1 MOS	I	1 DEC_STEP	M2_STEP	
Schematic	Redux_v3_430			Update	Date	2022-12-18			
	GIIIATTO TOUGHT.				Create	Date	2022-06-27		
Page	Power				Part N	umber			
Drawed	lordzurp								
Reviewed	lordzurp	TeenAstro v3					I		
		VER	SIZE		PAGE	2	0F	3	
EasyEDA		V0.9	V0.9 A4 zUrp Industries						
3		4		5			6	;	

ESD protection

R61

TX_REMOTE

Test Points

1 1 III

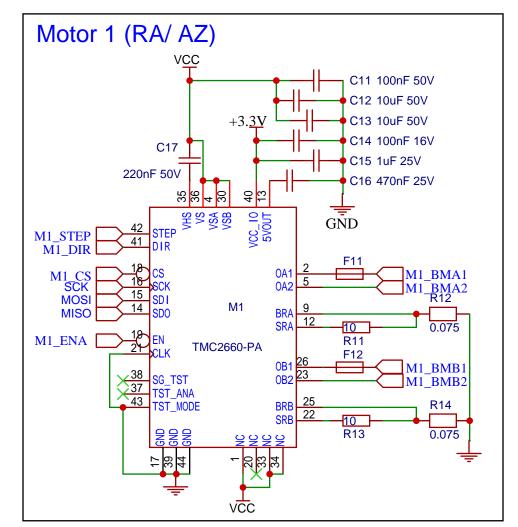
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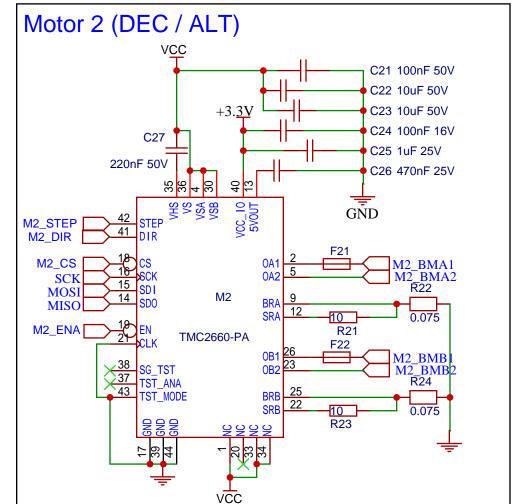
1 TX_DEBUGTX_DEBUG 1 RX_DEBUGRX_DEBUG TV60 SRV05-4-P-T7

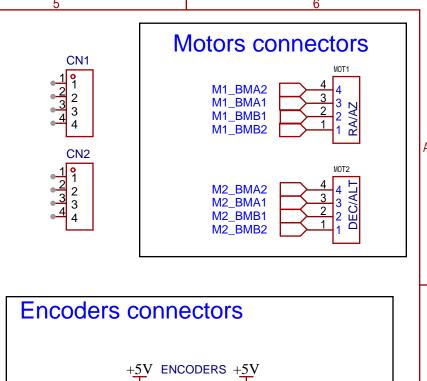
1 DEC_DIR

M2_DIR

RX_REMOTE







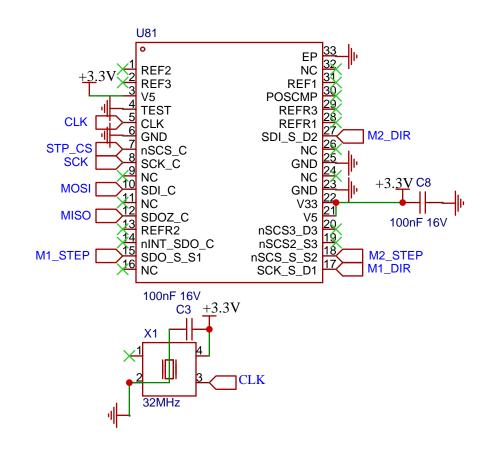
Header-Male-2.54_2x5

M1_ENCA_IN

M1_ENCB_IN M1_ENCX_IN

M2 ENCA IN

M2_ENCB_IN M2_ENCX_IN



€ E	EasyEDA		A4	zl	Jrp In	dustries		
		VER	SIZE	PAGE	3	OF	3	
Drawed Reviewed	lordzurp lordzurp	TeenAstro v3						
Page		Motors		Part N	lumber			
Schematic	Redux_v3_430			Create		2022-06-27		
	D 1 0 100			Update	Date	2022-12-18		