



**POLITECNICO
DI TORINO**



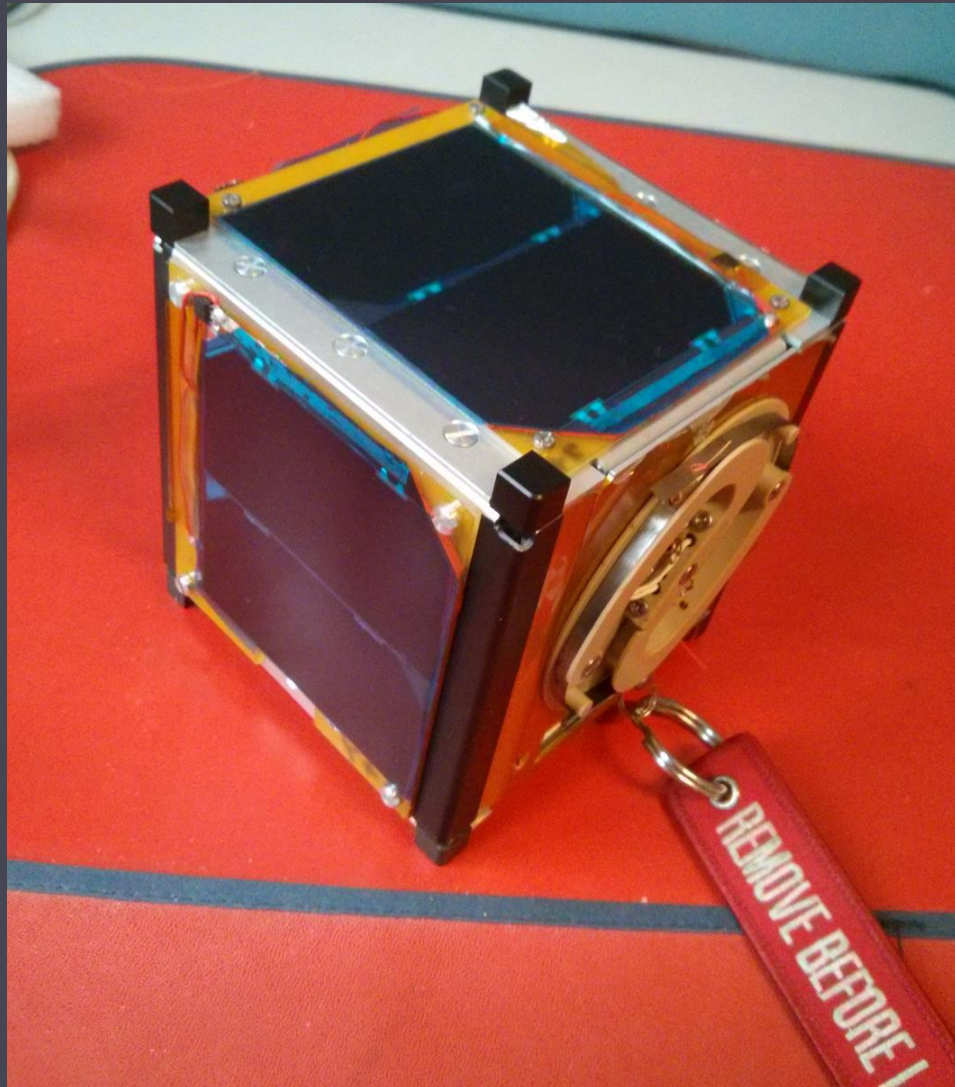
CubeSat
polito team

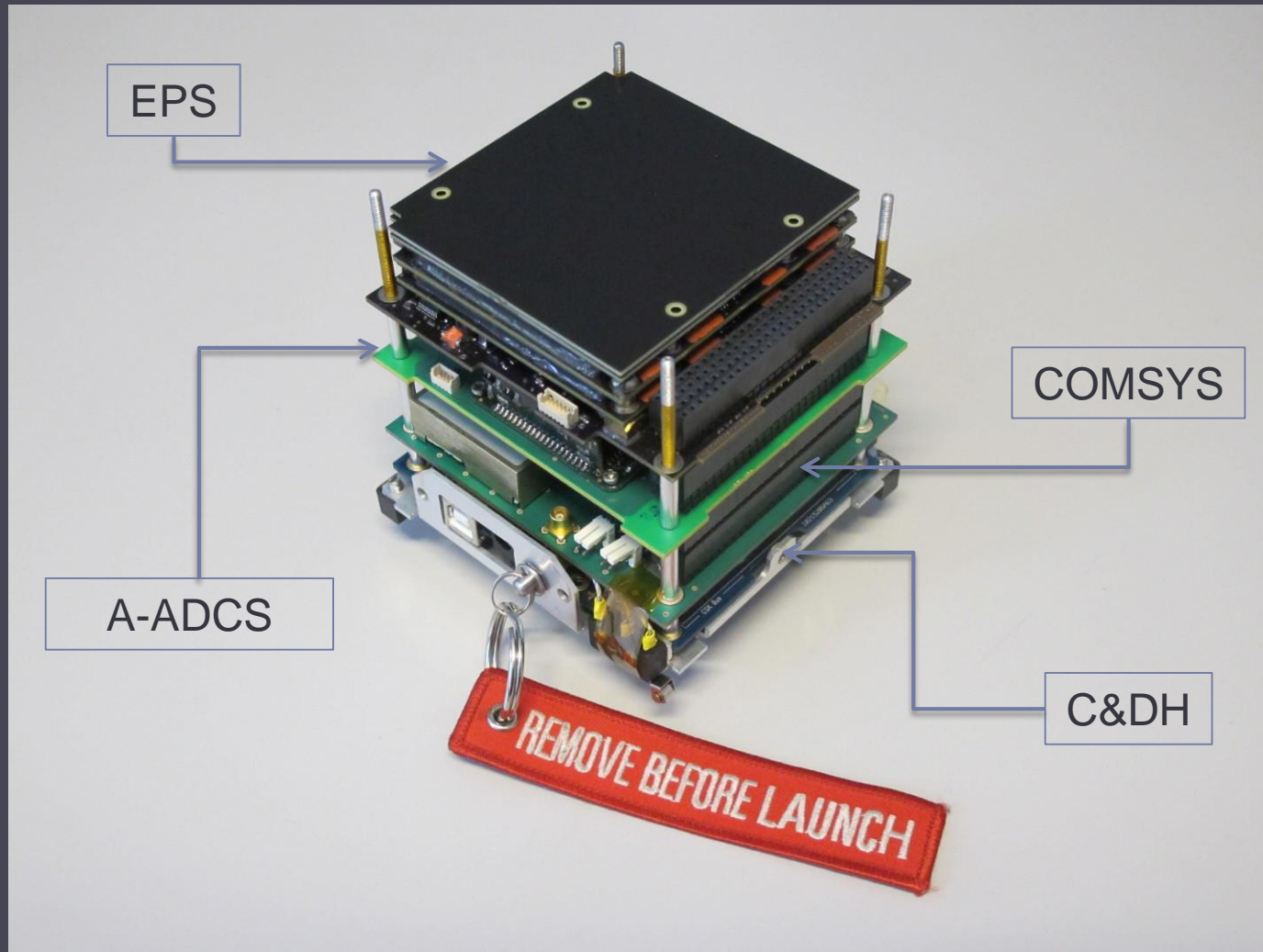
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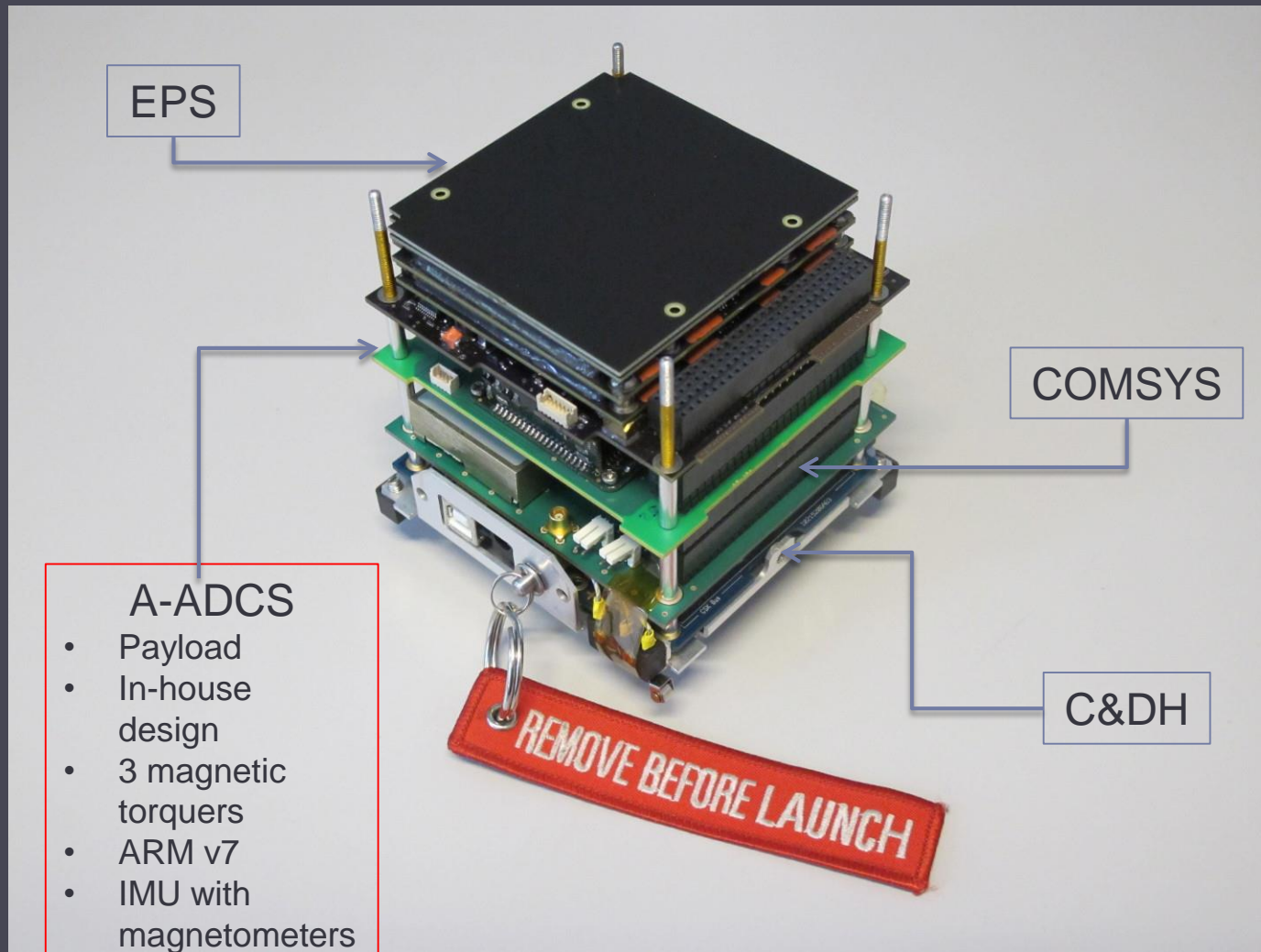
A tool for nanosatellite functional verification: comparison between different in-the-loop simulation configurations

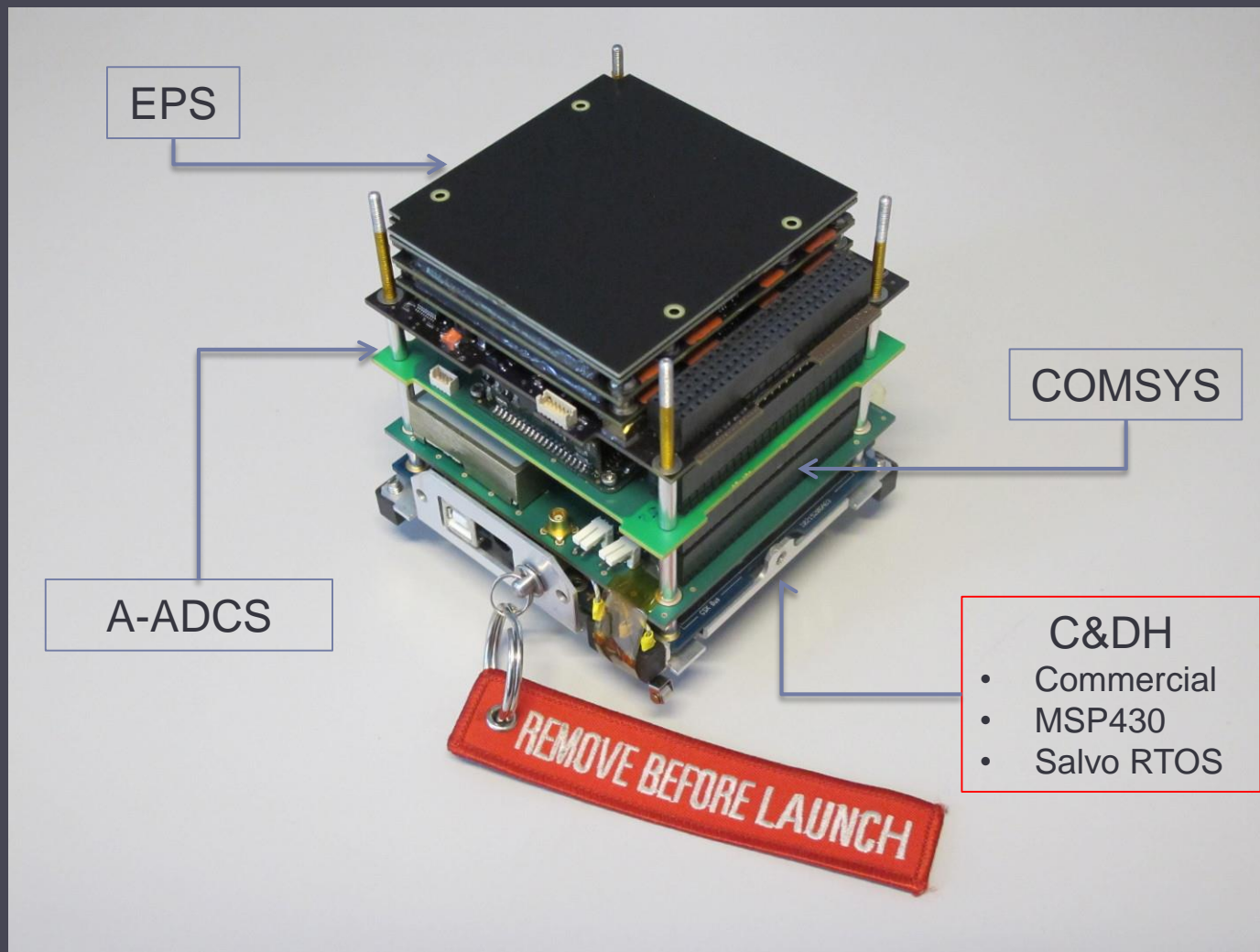
L. Feruglio (speaker), R. Mozzillo, S. Corpino, F. Stesina
Department of Mechanical and Aerospace Engineering

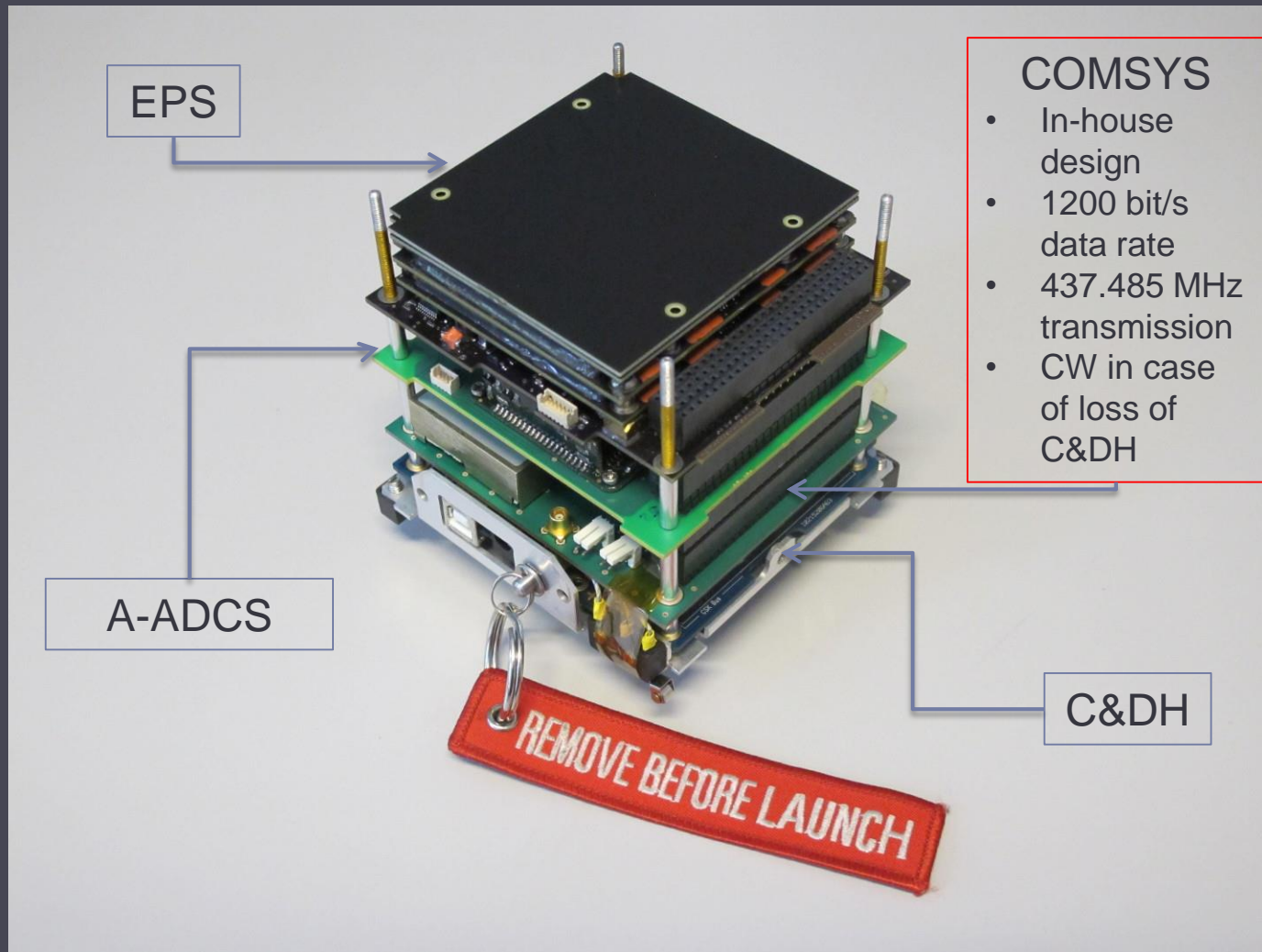
- ▶ e-st@r-II CubeSat
- ▶ StarSim
 - ▶ Key features
 - ▶ GUI brief description
- ▶ AIL, SIL, CIL, HIL
 - ▶ Presentation of a few key examples
- ▶ Comparison
 - ▶ Results of intermediate simulation sessions
 - ▶ Results on Hardware-in-the-Loop simulations

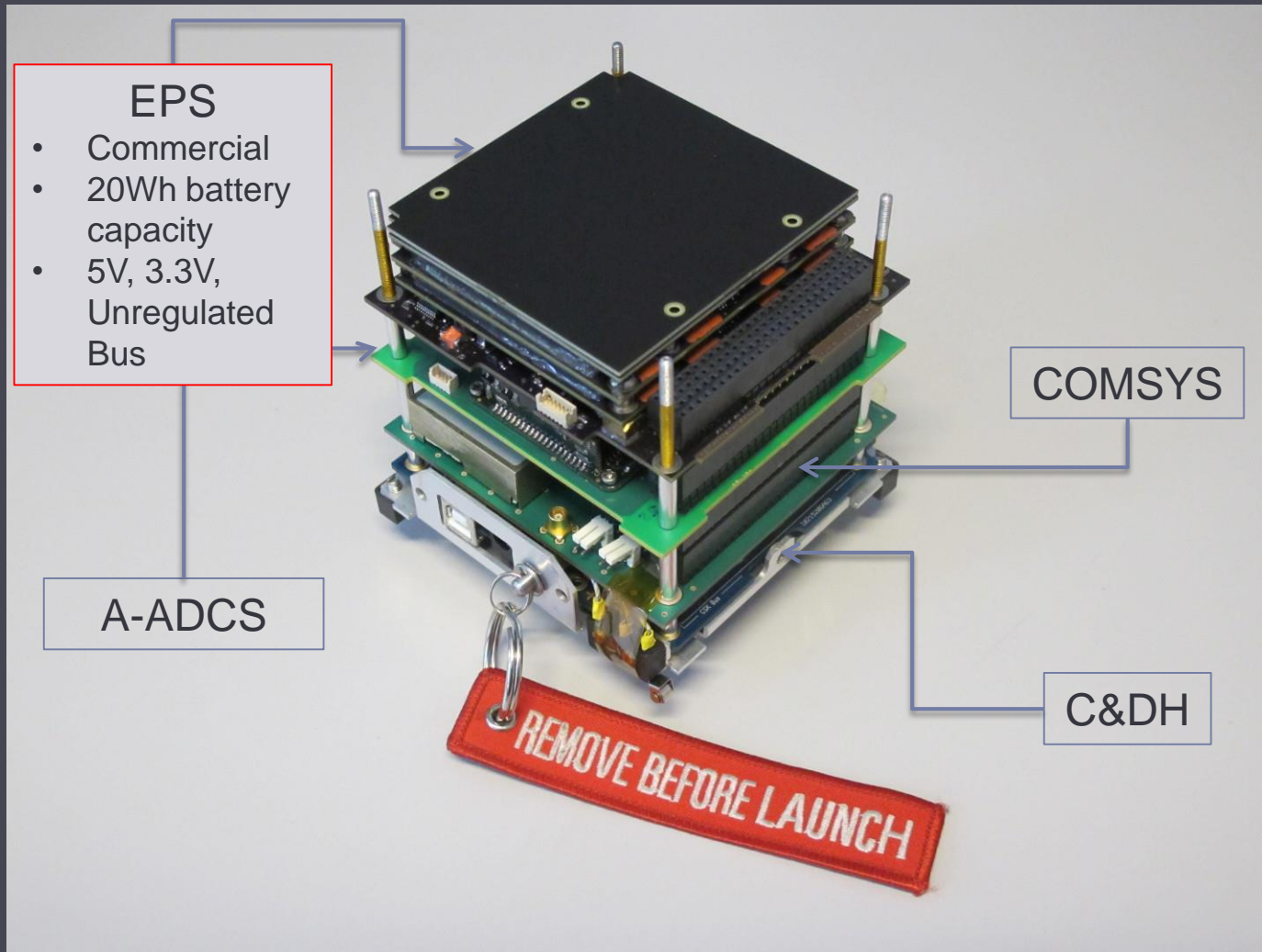


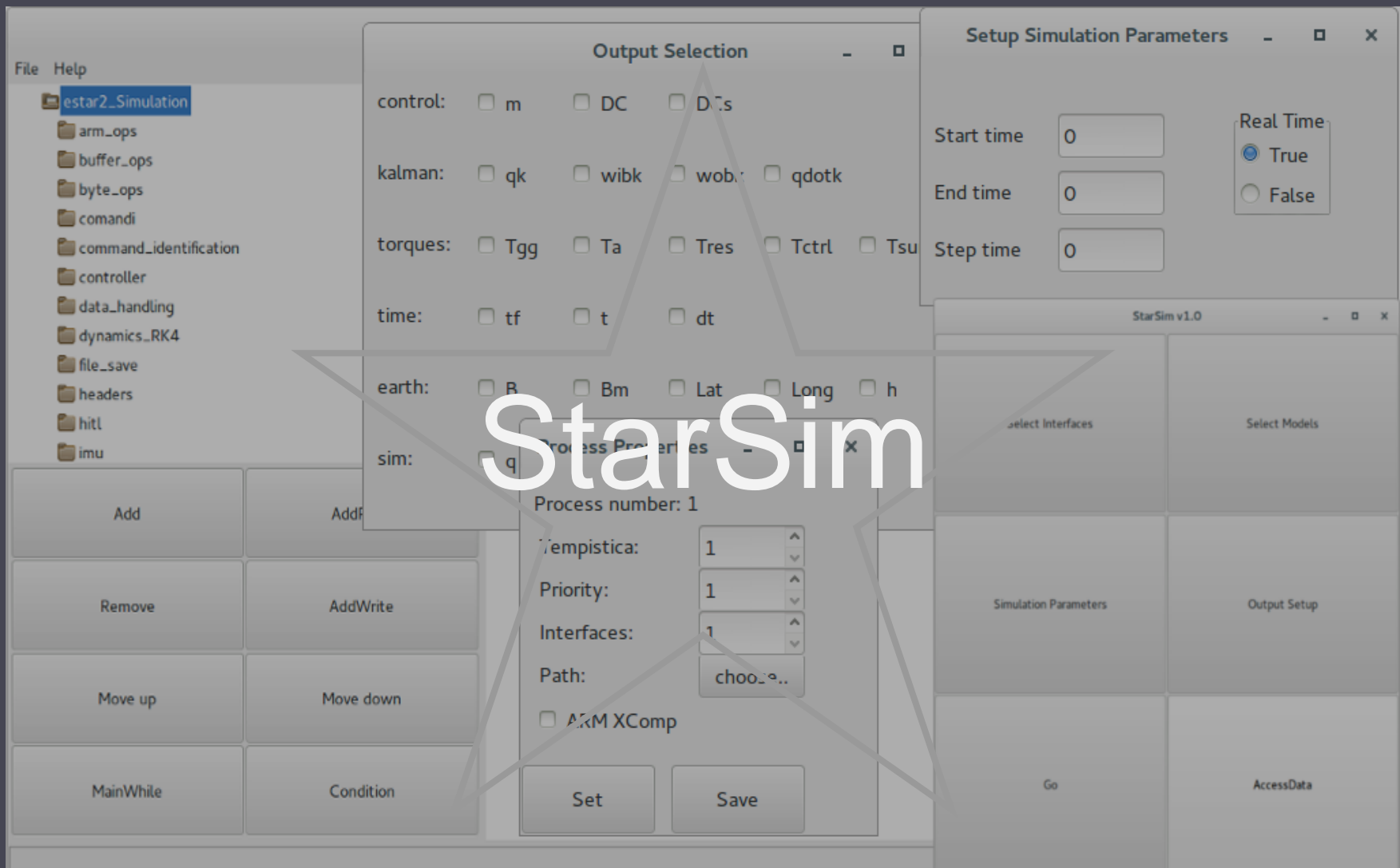




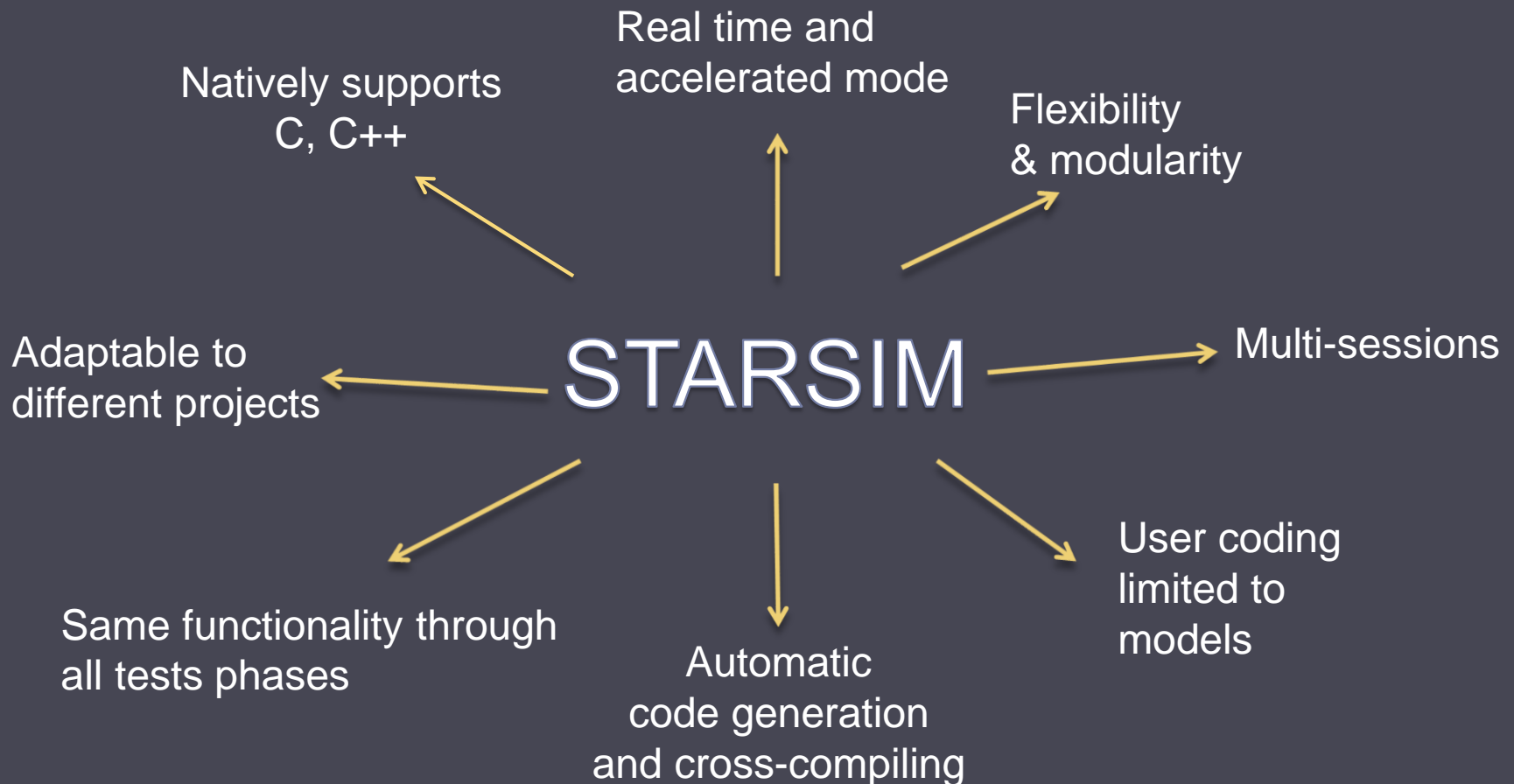


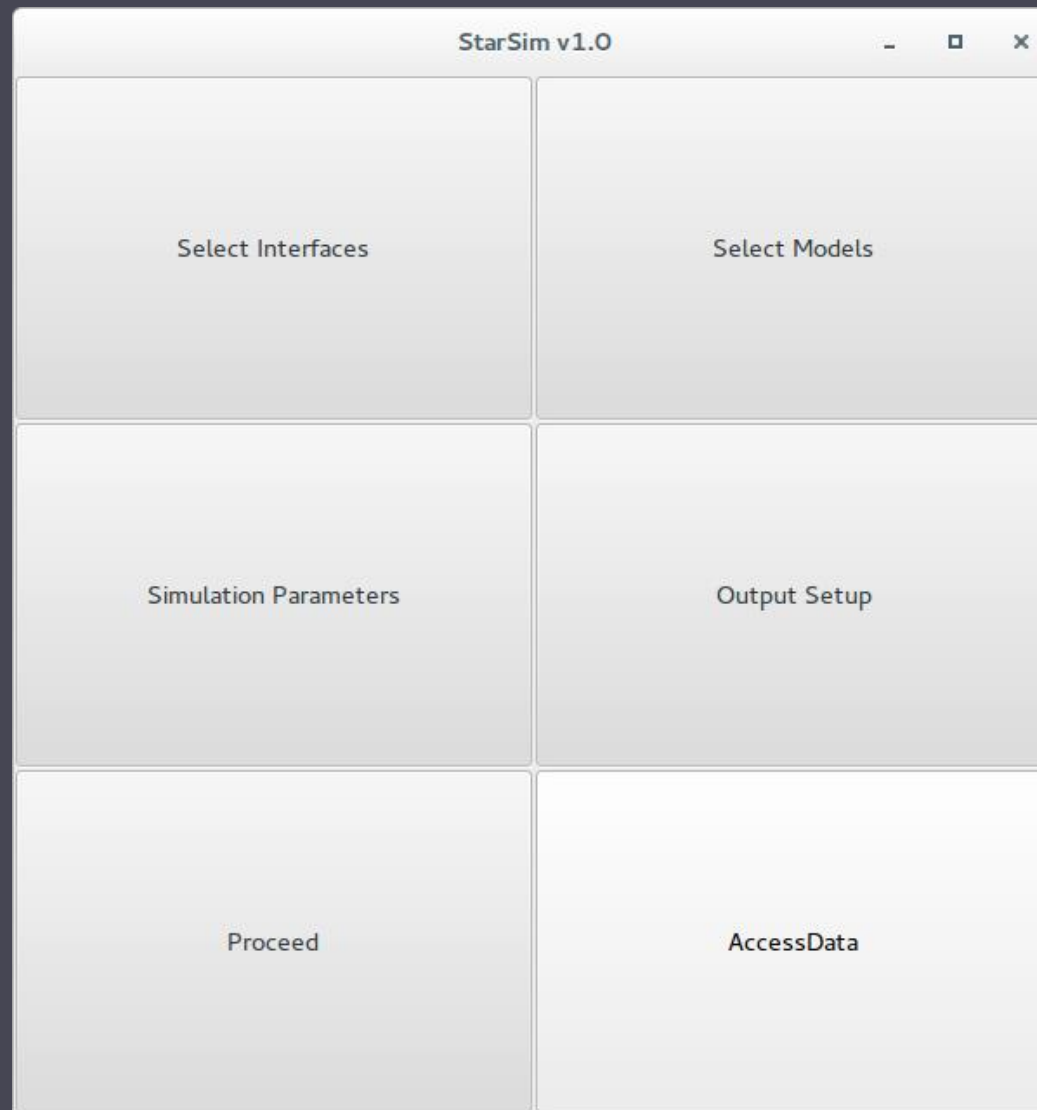






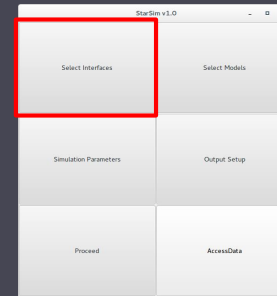
Need of a single “infrastructure” independent of the complexity of the test and from the development stage of the project





Simulation properties setup

- Scheduling and priorities definition
- Interfaces for each process with connection parameters setup
- Cross-compile flag



Process Properties

Process number: 1

Timing: 1

Priority: 1

Interfaces: 2

Path: choose..

☐ ARM XComp

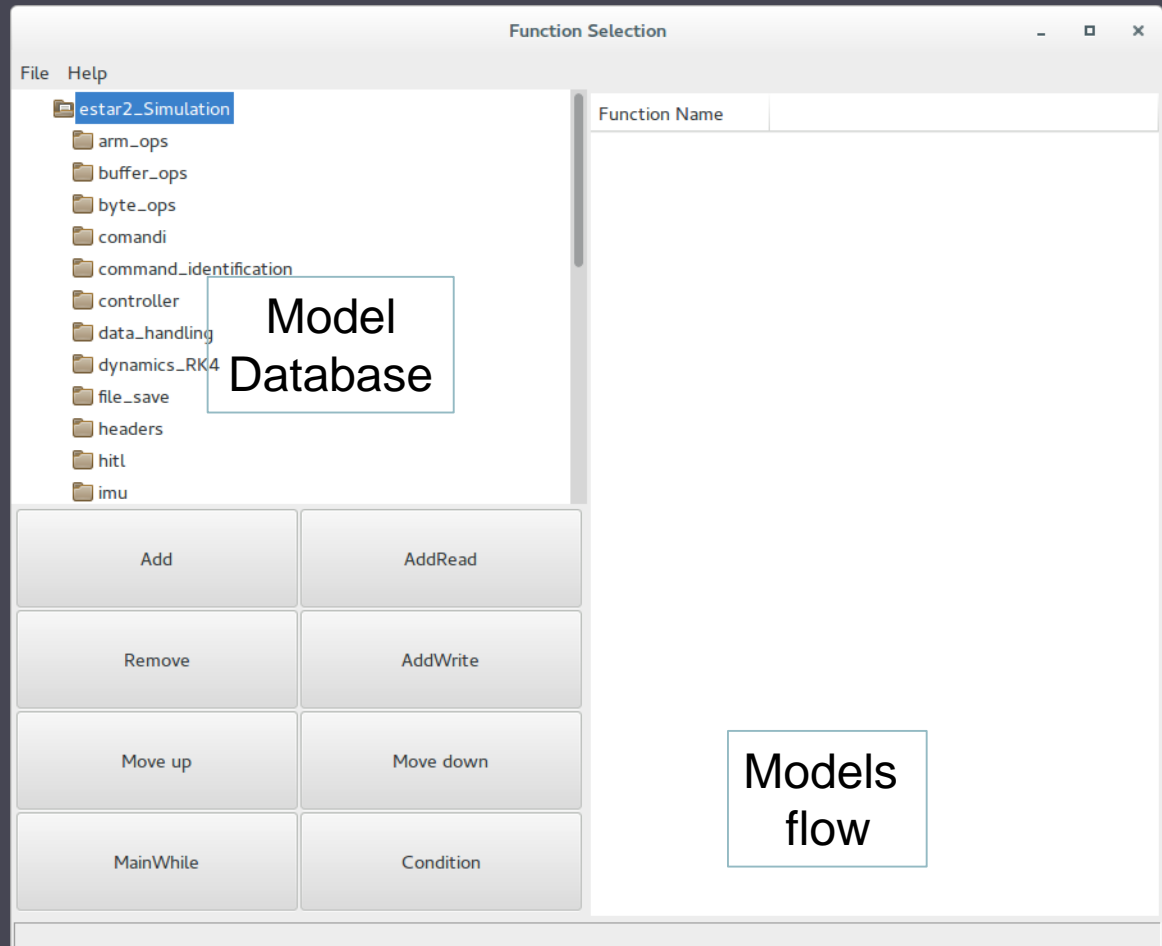
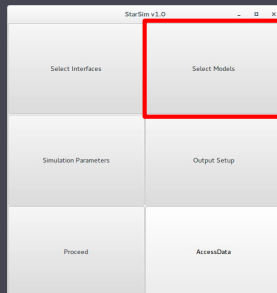
Interfaces:

serial	Name	Path	Speed
pipe	Name	Path	Mode

Set Save

Simulation flow setup – model selection

- Database explorer
- Drag & drop action, or command buttons
- Simulation flow definition
- Open/Save functions



Setup Simulation Parameters

Start time End time Step time

Real Time

☐ True ☒ FalseSet Constants

Output Selection

control: ☒ m ☐ DC ☐ DCskalman: ☒ qk ☒ wibk ☐ wobk ☐ qdotktorques: ☒ Tgg ☒ Ta ☒ Tres ☒ Tctrl ☒ Tsumtime: ☒ tf ☐ t ☐ dtearth: ☐ B ☒ Bm ☒ Lat ☒ Long ☒ hsim: ☒ q ☒ wib ☐ wob ☐ qdot

Simulation Parameters

Output Setup

Go

AccessData

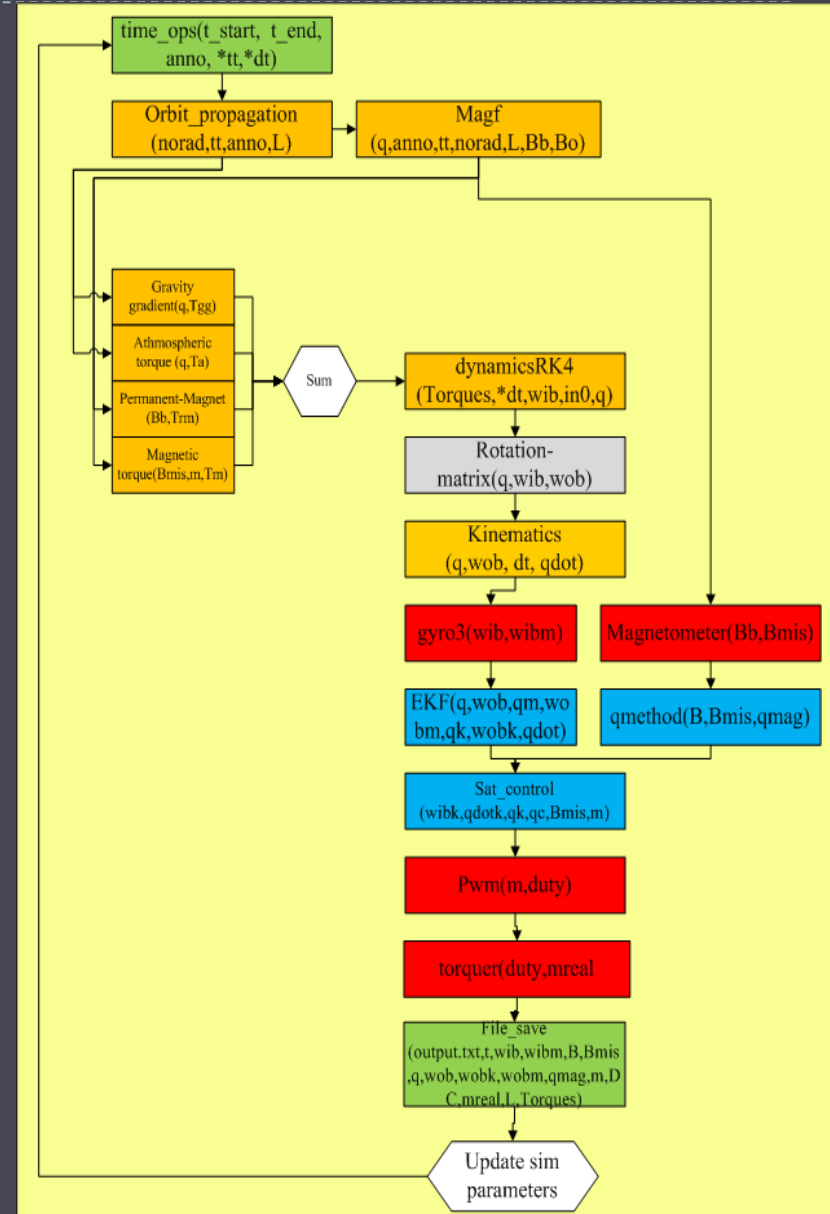
Start/Stop the
simulation

Link to data repository
where the files with
the results/ data/
configurations are
saved

AIL simulations (ADCS)

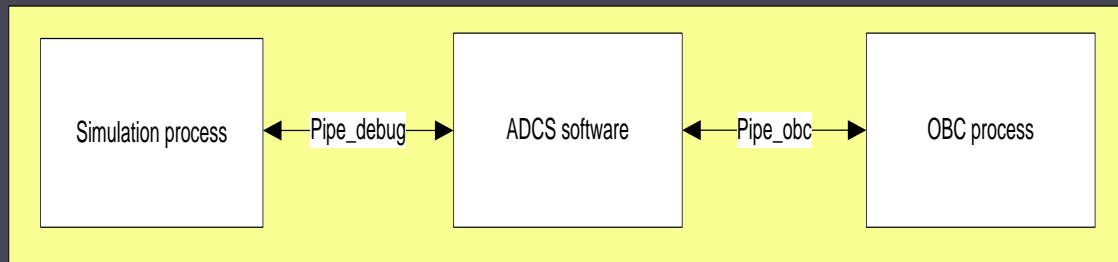
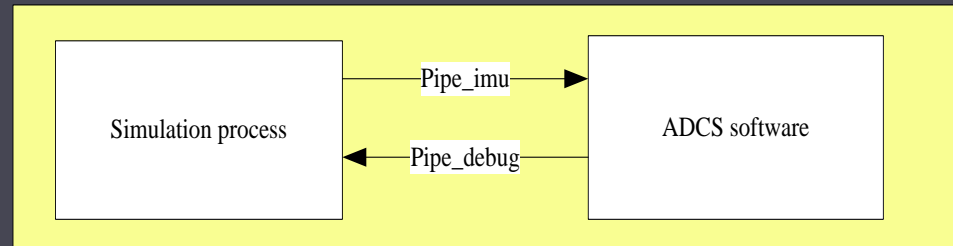
Simulation process

- Only one process runs on the Simulation Unit;
- Neither SW nor HW interfaces are required.



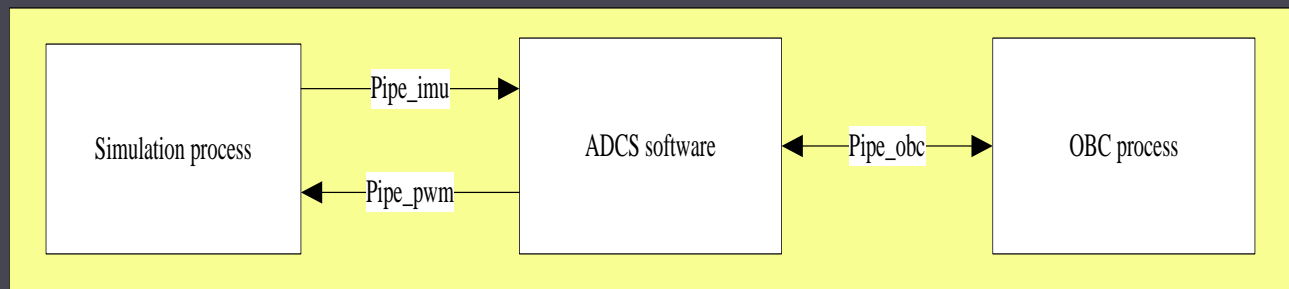
SIL simulations (ADCS)

IMU communication
2 processes, 2 pipes

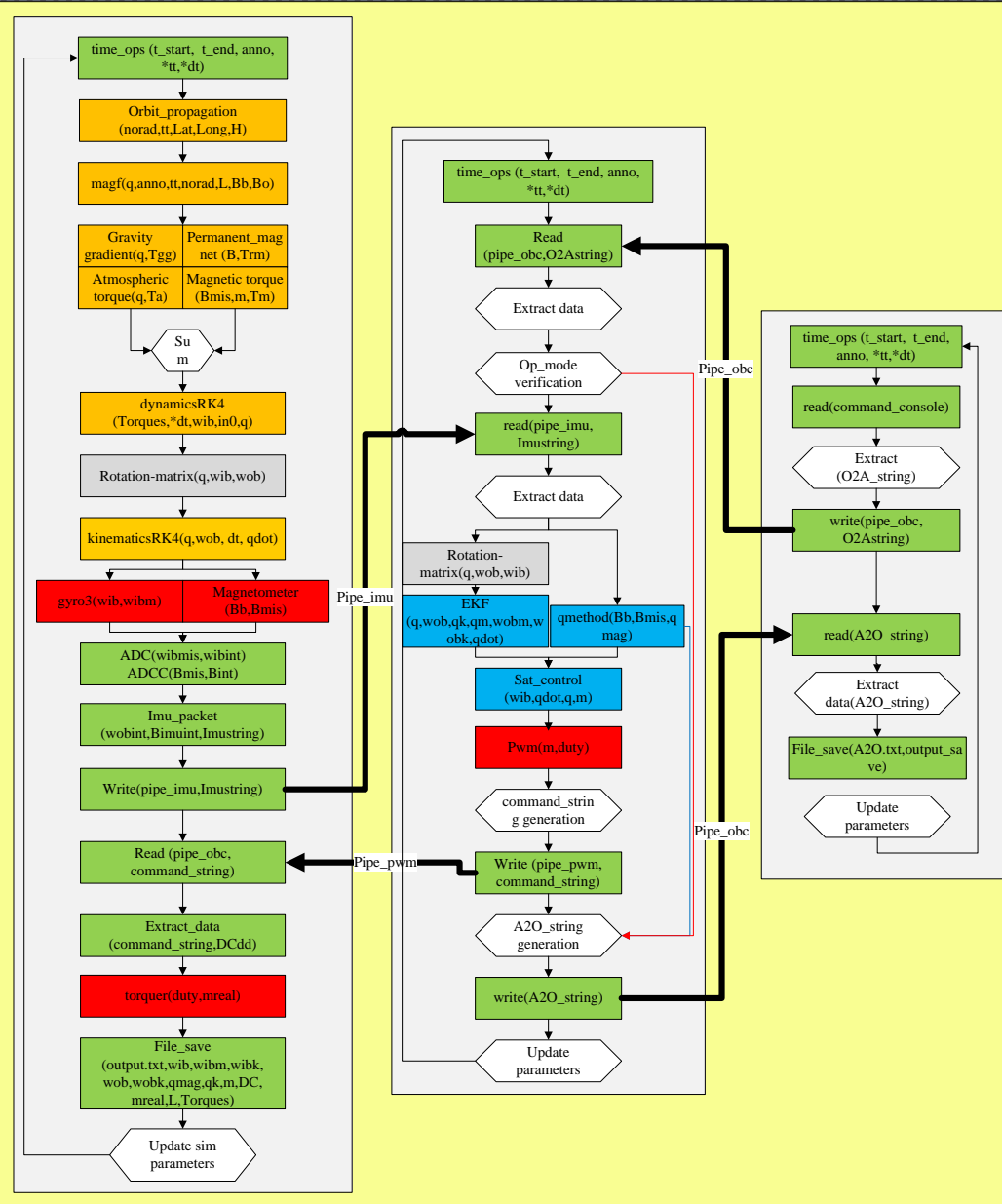


OBC/ADCS communication:
3 processes, 2 pipes

Integrated subsystem verification:
3 processes, 3 pipes



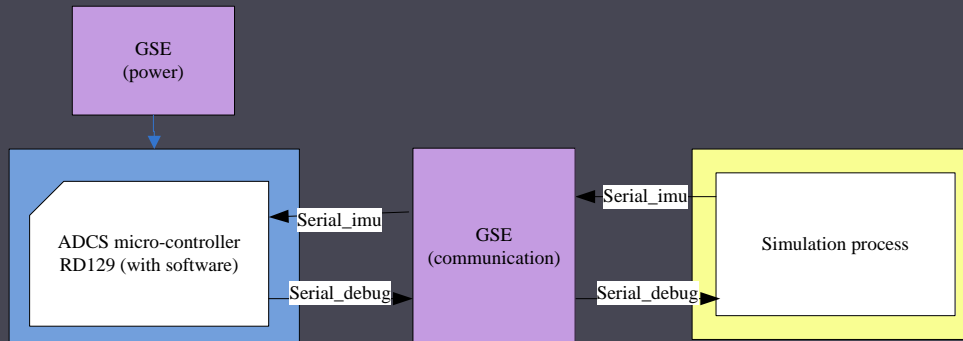
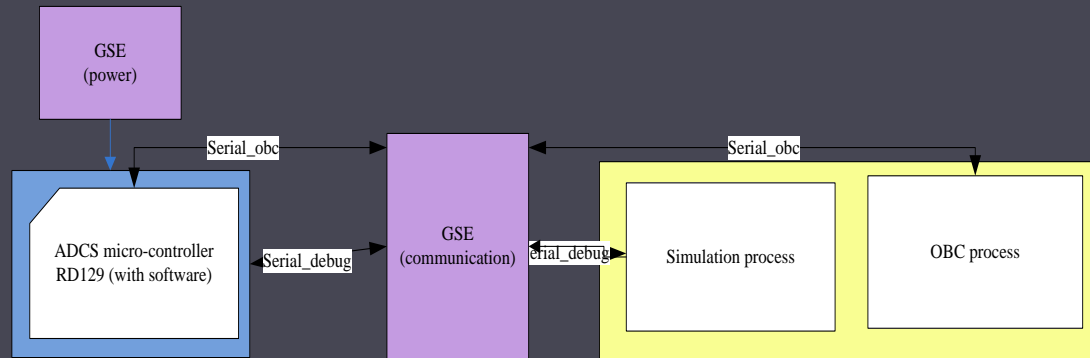
Simulation process



CIL simulations

OBC/ADCS verification:

2 processes, 2 serial communications, 1 process on embedded system, 2 GSE

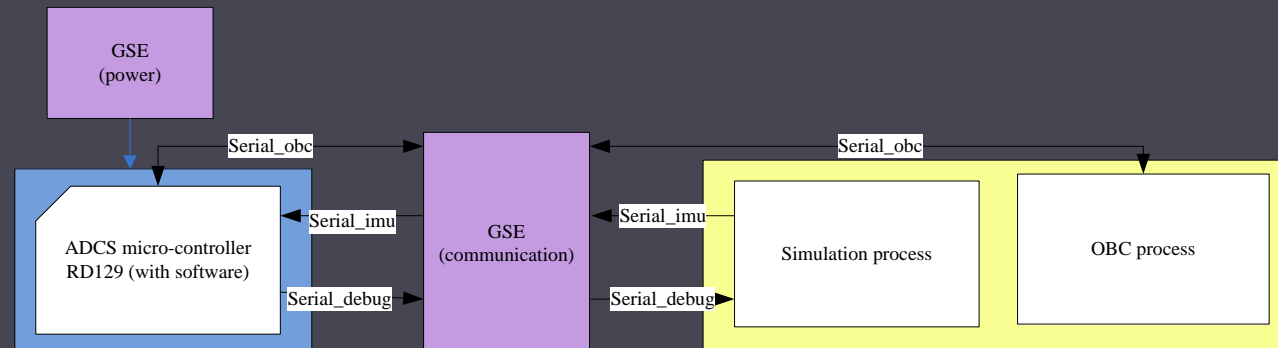


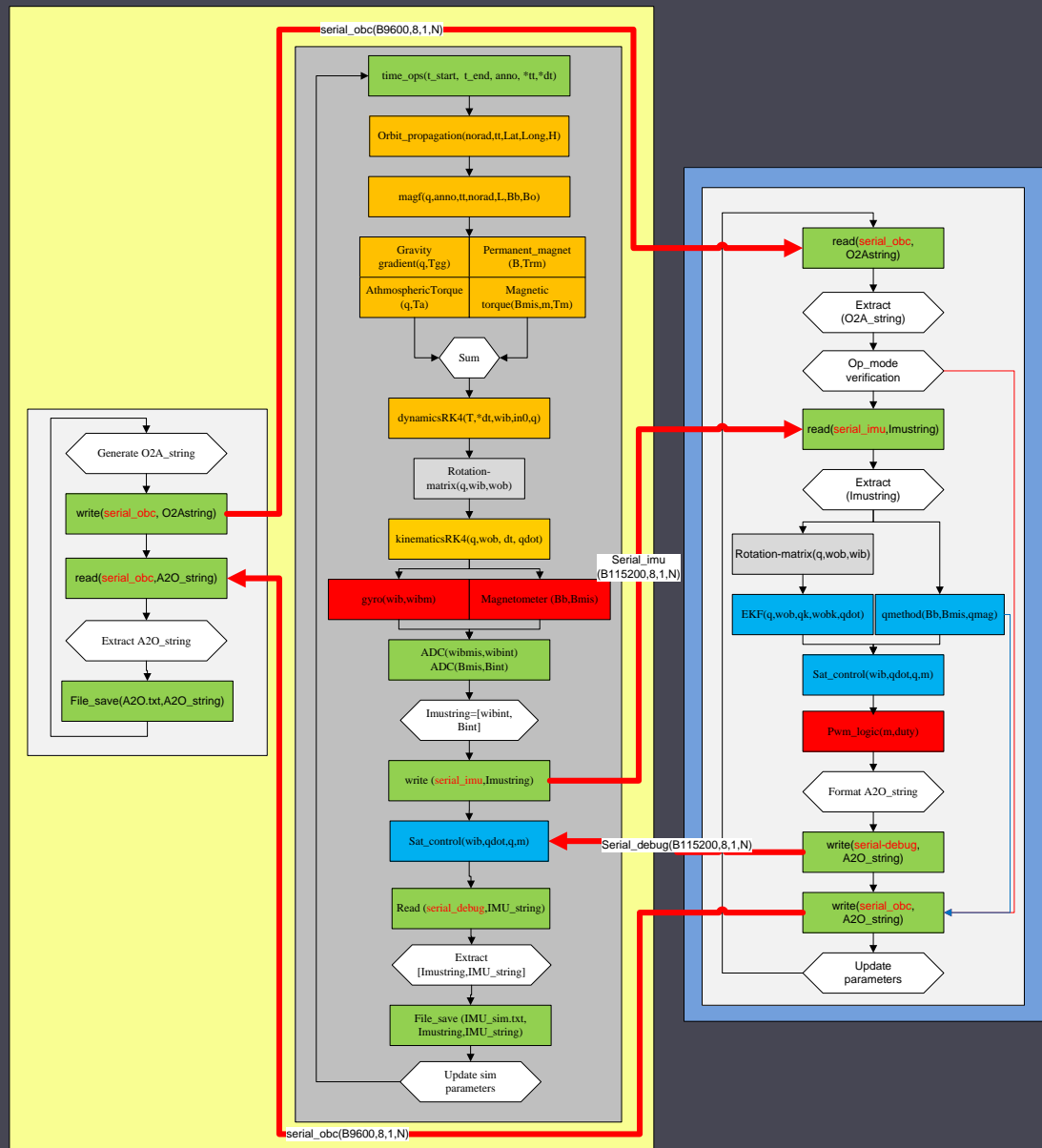
IMU communication verification:

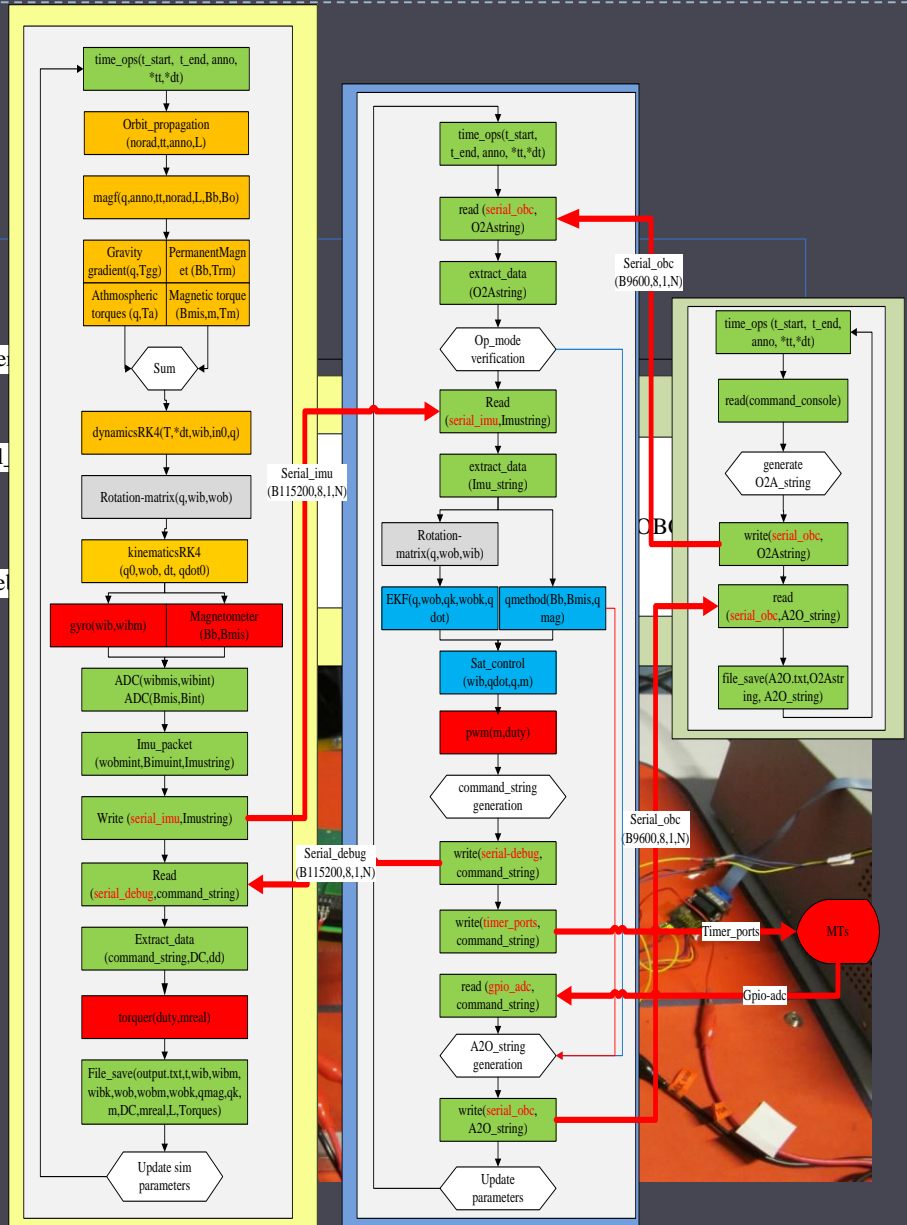
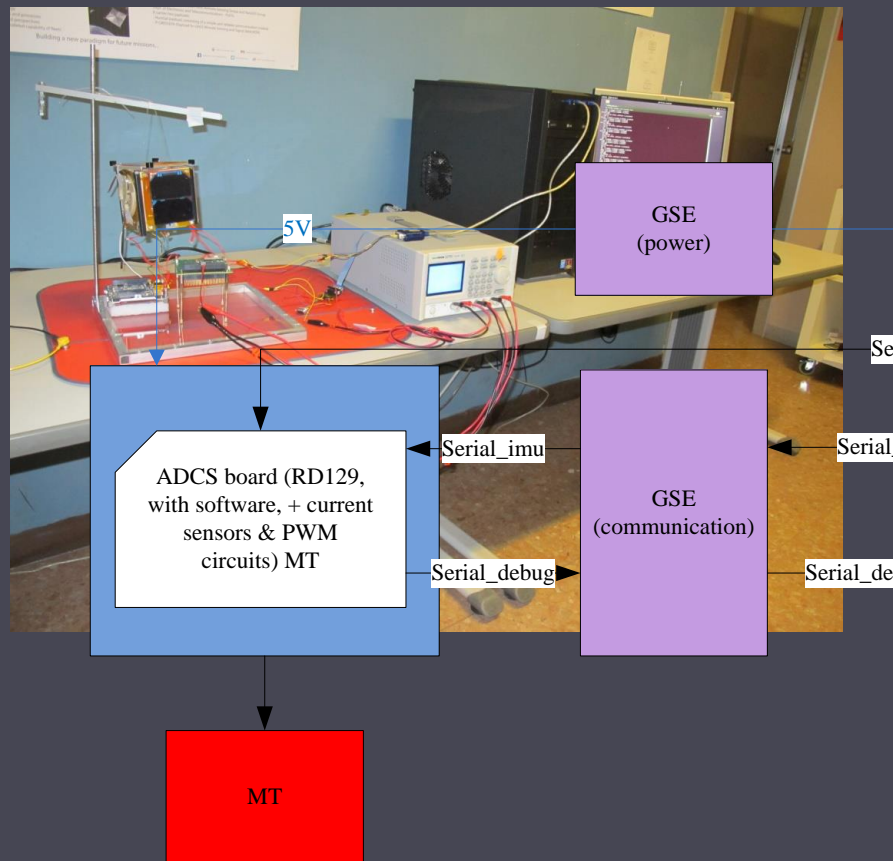
1 process, 1 serial communication, 1 process on embedded system, 2 GSE

RT and synchronization verification:

2 processes, 3 serial communications, 1 process on embedded system, 2 GSE

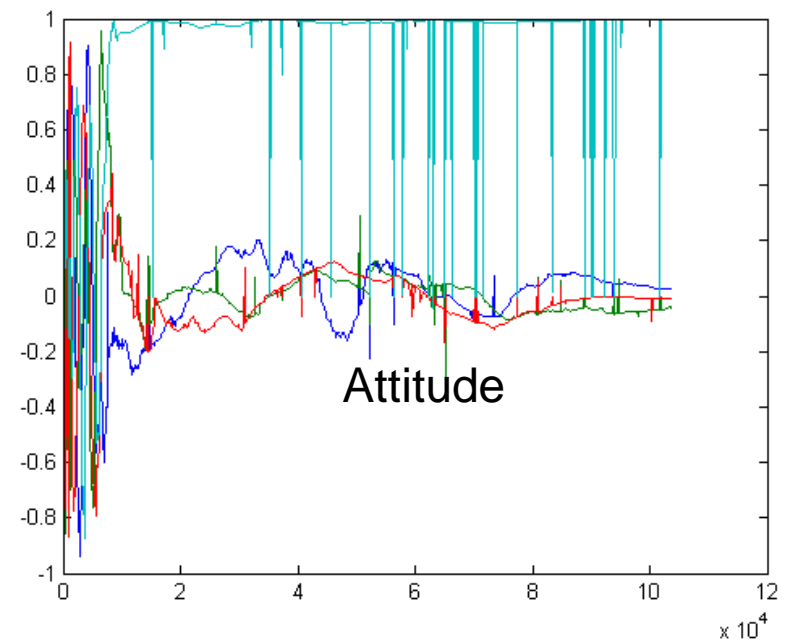
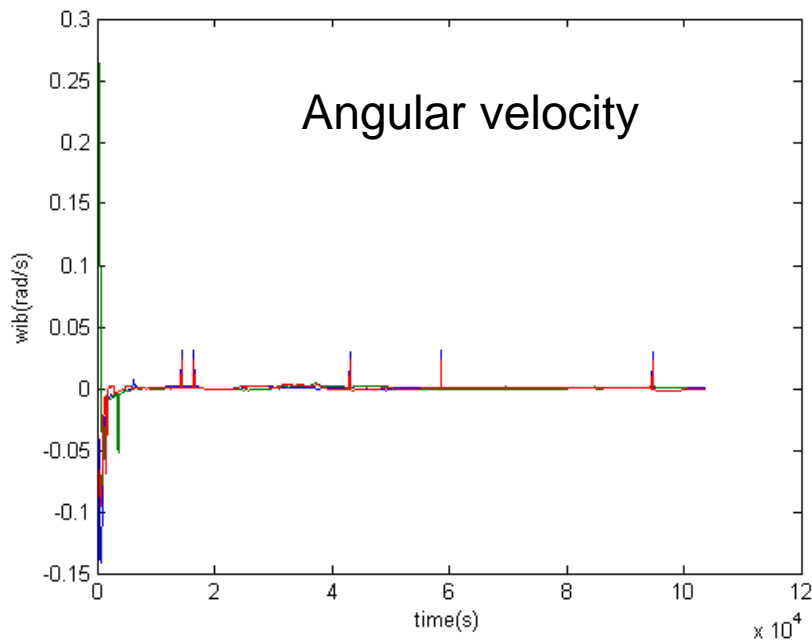




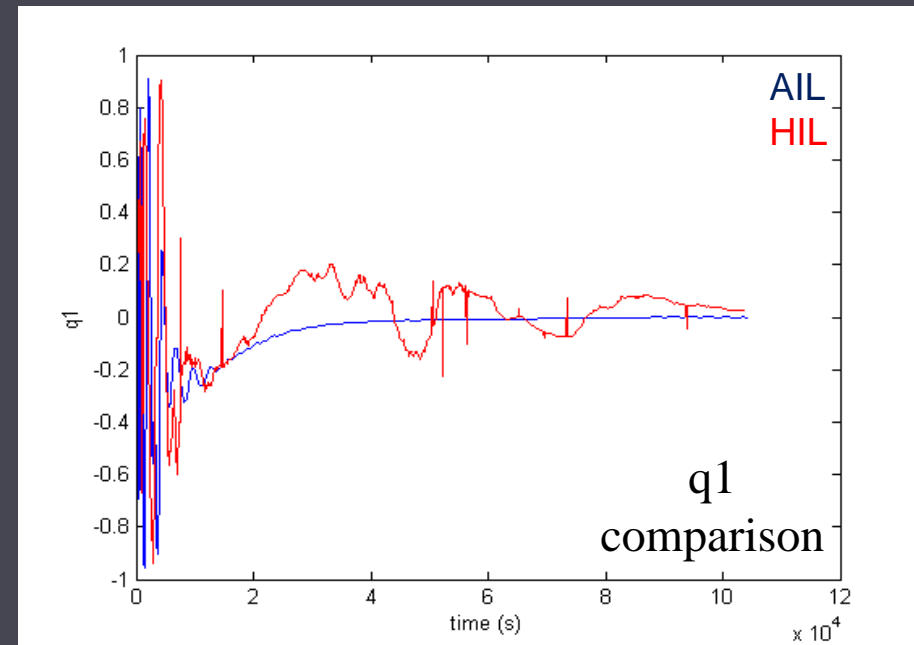
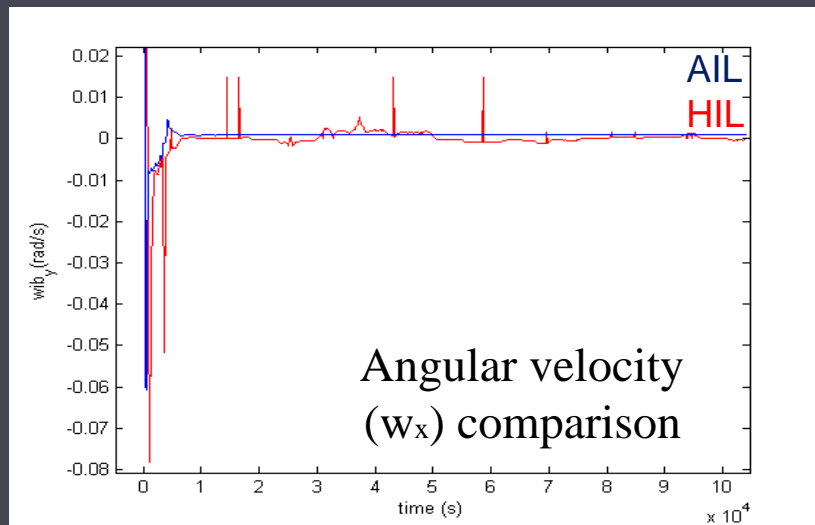


- Software in the Loop
 - No errors in IMU-ADCS board communications
 - No errors in OBC-ADCS communications
- Controller in the Loop
 - No packet is lost in ADCS-OBC but about 1.2% of packets are corrupted
 - No packet is lost in IMU-ADCS board communications but about 2% of packets are corrupted
- 3% of IMU packets are lost. No error arises after the data extraction, thanks to IMU sampling of high enough frequency

Controller in the Loop early results



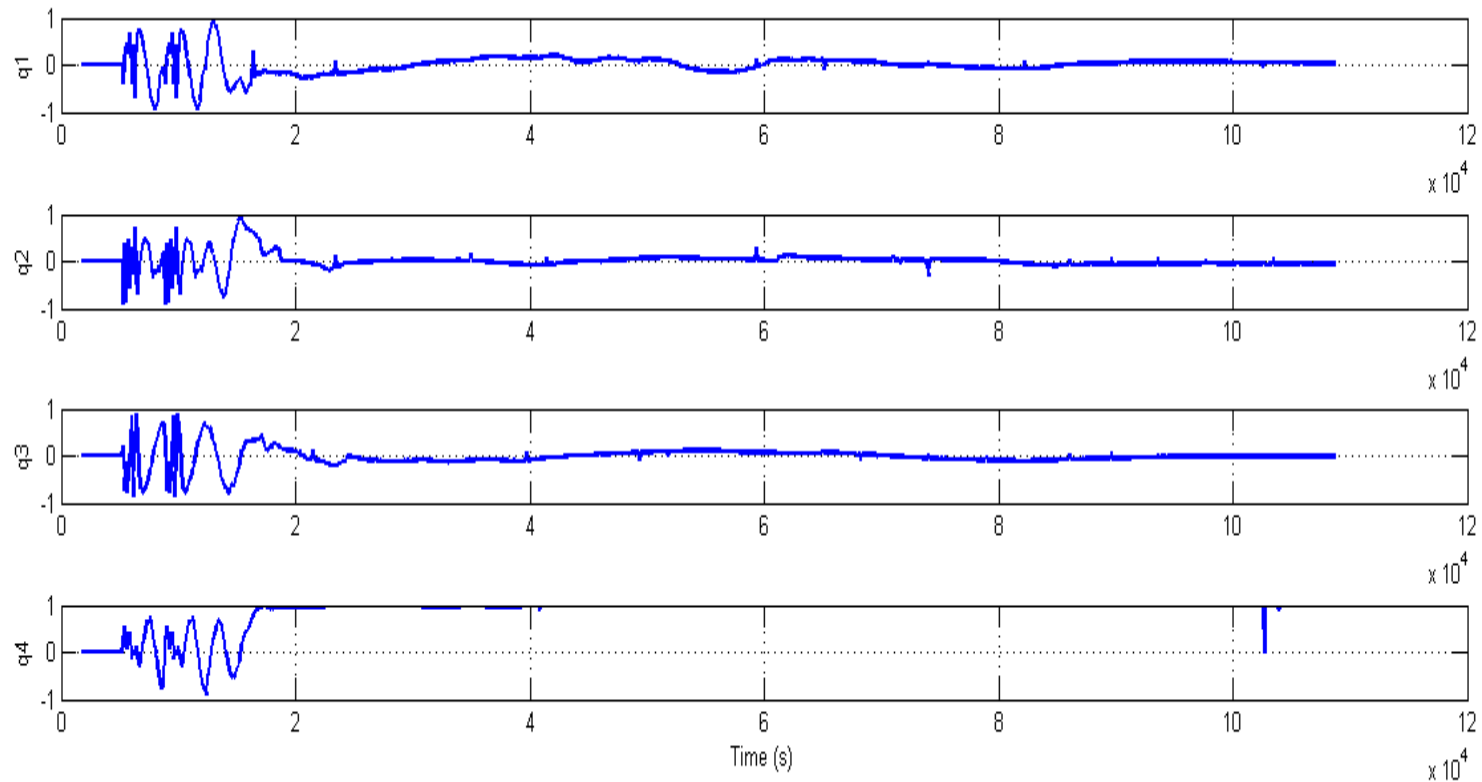
Comparison HIL vs AIL



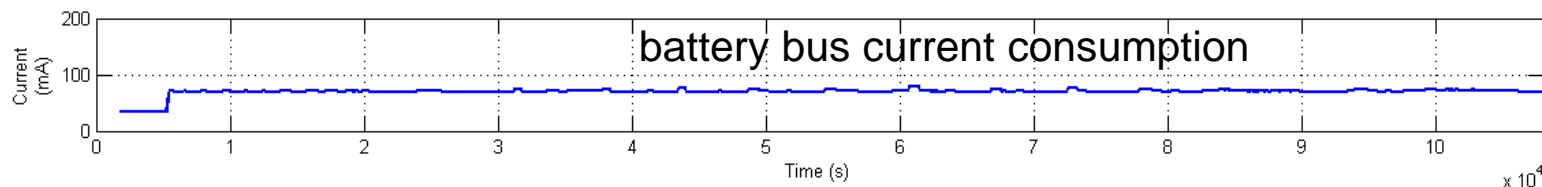
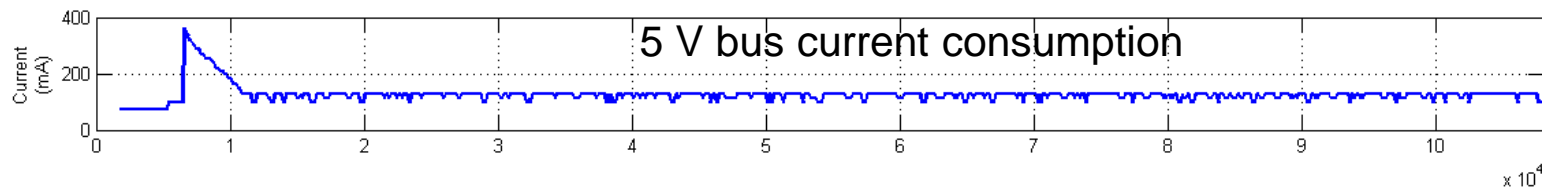
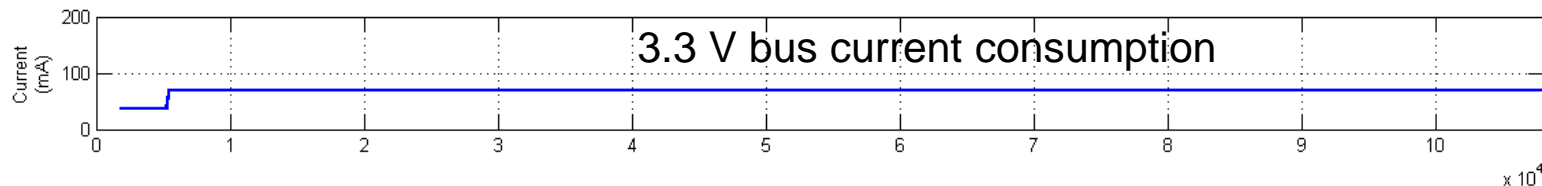
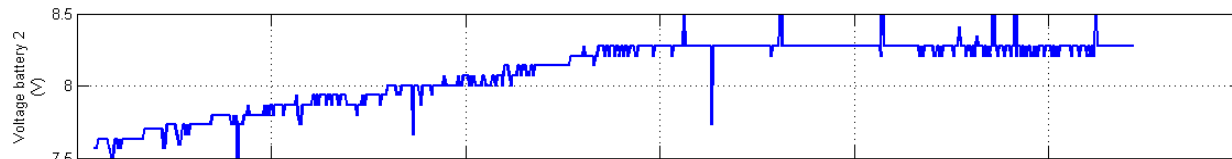
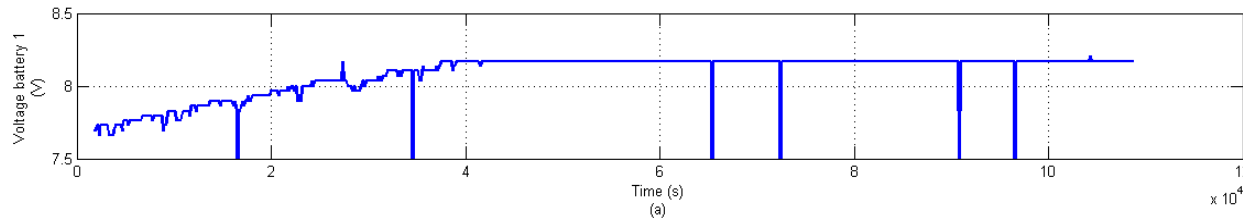
Power consumption for detumbling and acquisition phases:

- AIL: 1750 mW
- HIL: 2540 mW

HIL simulation for integrated system: results (I)



HIL simulation for integrated system: results (II)



THANK YOU FOR YOUR ATTENTION

QUESTIONS AND COMMENTS ARE WELCOME!

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