

Project documentation

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1 Introduction

The aim of this project is the study of Yao's protocol [2] and an useful application of it. More precisely, we will implement Secure multi-party computation; this field has the goal of creating methods for parties to jointly compute a function over their inputs while keeping those inputs private [1]. In this project, the function we decided to implement is the *8 bit sum*.

1.1 Description of the circuit

We will present briefly the 8-bit sum circuit. There are two basic components in this construction:

- *Half Adder*: used to sum the right-most digit;
- *Full adder*: used to sum a generic digit in the number, ranging from position 1 to 8. It receives in input also carry of the previous sum.

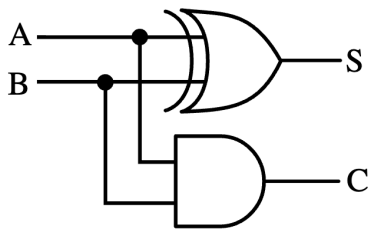


Figure 1: Half Adder

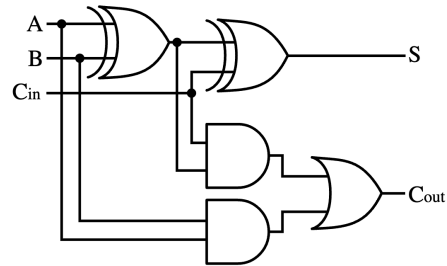


Figure 2: Full Adder

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We then proceeded creating the circuit by wiring 7 full adders and an half adder together, as represented in Figure 3.

¹1 was taken over <https://upload.wikimedia.org/wikipedia/commons/1/14/Half-adder.svg>
2 was taken over <https://upload.wikimedia.org/wikipedia/commons/a/a9/Full-adder.svg>.