

Refinement mapping to show that *Serializability* implements *Sequential*

EXTENDS *Serializability*, *Sequences*, *TLC*

VARIABLES *h*, *henv*, *opBar*, *argBar*, *rvalBar*, *envBar*, *ffBar*, *serialized*

vars \triangleq $\langle h, henv, opBar, argBar, rvalBar, envBar, ffBar, serialized \rangle$

bars \triangleq $\langle opBar, argBar, rvalBar, envBar, ffBar, serialized \rangle$

InitR \triangleq \wedge *Init*
 $\wedge h = \langle \rangle$
 $\wedge henv = \langle \rangle$
 $\wedge opBar = op$
 $\wedge argBar = arg$
 $\wedge rvalBar = rval$
 $\wedge envBar = benv[1]$
 $\wedge ffBar = Flip$
 $\wedge serialized = FALSE$

TypeOkR \triangleq $\wedge h \in Seq([tr : Tr, op : \{ "r", "w" \}, arg : Arg])$
 $\wedge henv \in Seq([Obj \rightarrow Val])$
 $\wedge opBar \in Op$
 $\wedge argBar \in Arg$
 $\wedge rvalBar \in Rval$
 $\wedge envBar \in [Obj \rightarrow Val]$
 $\wedge ffBar \in \{ Flip, Flop \}$
 $\wedge serialized \in BOOLEAN$

Commits(*t*) \triangleq *fate*[*t*] = *Committed*

CommitR(*t*) \triangleq *Commit*(*t*) \wedge UNCHANGED *vars*

AbortR(*t*) \triangleq *Abort*(*t*) \wedge UNCHANGED *vars*

ReadR(*t*, *obj*, *val*) \triangleq $\wedge Read(t, obj, val)$
 $\wedge h' = \text{IF } Commits(t)$
 $\quad \text{THEN } Append(h, [tr \mapsto t, op \mapsto "r",$
 $\quad \quad \quad arg \mapsto arg', rval \mapsto rval'])$
 $\quad \text{ELSE } h$
 $\wedge henv' = \text{IF } Commits(t) \text{ THEN } Append(henv, henv'[t]) \text{ ELSE } henv$
 $\wedge \text{UNCHANGED } bars$

WriteR(*t*, *obj*, *val*) \triangleq $\wedge Write(t, obj, val)$
 $\wedge h' = \text{IF } Commits(t)$
 $\quad \text{THEN } Append(h, [tr \mapsto t, op \mapsto "w",$
 $\quad \quad \quad arg \mapsto arg', rval \mapsto rval'])$
 $\quad \text{ELSE } h$

$$\wedge henv' = \text{IF } \text{Commits}(t) \text{ THEN } \text{Append}(henv, tenv'[t]) \text{ ELSE } henv \\ \wedge \text{UNCHANGED } bars$$

$$\begin{aligned} \text{SerializeHistory} &\triangleq \\ &\wedge \text{Termination} \\ &\wedge \neg \text{serialized} \\ &\wedge \text{LET } N \triangleq \text{Len}(h) \\ &\quad R \triangleq 1 \dots N \\ &\quad perm \triangleq \text{CHOOSE } seq \in [R \rightarrow R] : \\ &\quad \forall i, j \in R : \\ &\quad \quad \text{LET } si \triangleq seq[i] \\ &\quad \quad sj \triangleq seq[j] \\ &\quad \quad hi \triangleq h[si] \\ &\quad \quad hj \triangleq h[sj] \\ &\quad \quad Ti \triangleq hi.tr \\ &\quad \quad Tj \triangleq hj.tr \text{IN} \\ &\quad \quad \text{must be 1:1 mapping} \\ &\quad \quad \wedge si = sj \Rightarrow i = j \\ &\quad \quad \text{preserve order within transaction} \\ &\quad \quad \wedge (Ti = Tj \wedge i < j) \Rightarrow si < sj \\ &\quad \quad \wedge (Ti = Tj \wedge i > j) \Rightarrow si > sj \\ &\quad \quad \text{respect transaction order} \\ &\quad \quad \wedge \text{Ord}(Ti) < \text{Ord}(Tj) \Rightarrow i < j \\ &\quad \quad \wedge \text{Ord}(Ti) > \text{Ord}(Tj) \Rightarrow i > j \\ &\quad \text{IN } \wedge h' = [i \in R \mapsto h[perm[i]]] \\ &\quad \wedge henv' = [i \in R \mapsto henv[perm[i]]] \\ &\wedge \text{serialized}' = \text{TRUE} \\ &\wedge \text{UNCHANGED } \langle opBar, argBar, rvalBar, envBar, ffBar \rangle \end{aligned}$$

Issue the commands to the refinement mapping

$$\begin{aligned} \text{Issue} &\triangleq \text{LET } e \triangleq \text{Head}(h) \text{IN} \\ &\quad \wedge \text{Termination} \\ &\quad \wedge \text{serialized} \\ &\quad \wedge h \neq \langle \rangle \\ &\quad \wedge opBar' = e.op \\ &\quad \wedge argBar' = e.arg \\ &\quad \wedge rvalBar' = e.rval \\ &\quad \wedge envBar' = \text{Head}(henv) \\ &\quad \wedge ffBar' = \text{Toggle}(ffBar) \\ &\quad \wedge h' = \text{Tail}(h) \\ &\quad \wedge henv' = \text{Tail}(henv) \\ &\quad \wedge \text{UNCHANGED } \text{serialized} \\ \\ vr &\triangleq \langle h, henv, opBar, argBar, rvalBar, envBar, ffBar, \text{serialized}, tr, \\ &\quad op, arg, rval, tstate, fate, to, tenv, benv, eval, ff \rangle \end{aligned}$$

$$\begin{aligned}
TerminationR &\triangleq \wedge Termination \\
&\wedge h = \langle \rangle \\
&\wedge UNCHANGED \ vr
\end{aligned}$$

$$\begin{aligned}
NextR &\triangleq \vee \exists t \in Tr : \\
&\vee CommitR(t) \\
&\vee AbortR(t) \\
&\vee \exists obj \in Obj, val \in Val : \\
&\quad \vee ReadR(t, obj, val) \\
&\quad \vee WriteR(t, obj, val) \\
&\vee SerializeHistory \\
&\vee Issue \\
&\vee TerminationR
\end{aligned}$$

$$\begin{aligned}
LR &\triangleq \wedge L \\
&\wedge WF_{vr}(SerializeHistory) \\
&\wedge WF_{vr}(Issue)
\end{aligned}$$

$$SpecR \triangleq InitR \wedge \Box[NextR]_{vr} \wedge LR$$

$$\begin{aligned}
Sequential &\triangleq \text{INSTANCE } Sequential \text{ WITH} \\
&\quad op \leftarrow opBar, \\
&\quad arg \leftarrow argBar, \\
&\quad rval \leftarrow rvalBar, \\
&\quad env \leftarrow envBar, \\
&\quad ff \leftarrow ffBar
\end{aligned}$$

$$SeqSpec \triangleq Sequential!Spec$$

$$\text{THEOREM } Spec \Rightarrow SeqSpec$$
