

MODULE *SSIRefinement*

EXTENDS *SSI, Sequences, FiniteSets*

CONSTANTS *NULL, Flip, Flop*

VARIABLES *h, fateIsSet, canIssue, parity, reads, writes, ord, tenvBar*

Refinement transactions

$TrR \triangleq Tr \setminus \{T0\}$

Committed transactions

$CT \triangleq \{t \in TrR : tstate[t] = Committed\}$

$N \triangleq Cardinality(CT)$

$TypeOkR \triangleq \wedge TypeOkS$
 $\wedge \forall i \in DOMAIN\ h : LET\ e \triangleq h[i] IN$
 $\wedge e.tr \in TrR$
 $\wedge e.op \in \{“r”, “w”, “c”, “a”\}$
 $\wedge e.arg \in CASE\ e.op = “r” \rightarrow Obj$
 $\quad \square\ e.op = “w” \rightarrow Obj \times Val$
 $\quad \square\ OTHER \rightarrow \{\langle \rangle\}$
 $\wedge e.rval \in Val \cup \{Ok, Err\}$
 $\wedge e.tstate \in [Tr \rightarrow \{Unstarted, Open, Committed, Aborted\}]$
 $\wedge e.op \in \{“r”, “w”\} \Rightarrow \wedge DOMAIN\ e.wr \subseteq Obj$
 $\quad \wedge \forall obj \in DOMAIN\ e.wr : e.wr[obj] \in Val$
 $\wedge fateIsSet \in BOOLEAN$
 $\wedge canIssue \in BOOLEAN$
 $\wedge parity \in \{0, 1\}$
 $\wedge reads \in [Tr \rightarrow SUBSET\ Obj]$
 $\wedge writes \in [Tr \rightarrow SUBSET\ Obj]$
 $\wedge tenvBar \in [CT \rightarrow [Obj \rightarrow Val]] \cup \{NULL\}$
 $\wedge ord \in [to : [1 .. N \rightarrow CT]] \cup \{NULL\}, benv : [1 .. N + 1 \rightarrow [Obj \rightarrow Val]] \cup \{NULL\}$

$InitR \triangleq \wedge InitS$
 $\wedge fateIsSet = FALSE$
 $\wedge parity = 0$
 $\wedge h = \langle \rangle$
 $\wedge canIssue = FALSE$
 $\wedge reads = [t \in Tr \mapsto \{\}]$
 $\wedge writes = [t \in Tr \mapsto IF\ t = T0\ THEN\ Obj\ ELSE\ \{\}]$
 $\wedge ord = [to \mapsto NULL, benv \mapsto NULL]$
 $\wedge tenvBar = NULL$

$StartTransactionR(t) \triangleq \wedge StartTransactionS(t)$
 $\wedge UNCHANGED\ \langle h, fateIsSet, canIssue, parity, reads, writes, ord, tenvBar \rangle$

$$\begin{aligned}
& \wedge \text{UNCHANGED } \langle \text{fateIsSet}, \text{canIssue}, \text{parity}, \text{reads}, \text{writes}, \text{ord}, \text{tenvBar} \rangle \\
\text{AbortR}(t) & \triangleq \\
& \wedge \text{AbortS}(t) \\
& \wedge h' = \text{Append}(h, [tr \mapsto t, op \mapsto \text{"a"}, arg \mapsto \langle \rangle, rval \mapsto \text{Ok}, \\
& \quad \quad \quad tstate \mapsto [tstate \text{ EXCEPT } ![t] = \text{Aborted}]]) \\
& \wedge \text{UNCHANGED } \langle \text{fateIsSet}, \text{canIssue}, \text{parity}, \text{reads}, \text{writes}, \text{ord}, \text{tenvBar} \rangle \\
\text{DetectDeadlockR} & \triangleq \wedge \text{DetectDeadlockS} \\
& \wedge \text{UNCHANGED } \langle h, \text{fateIsSet}, \text{canIssue}, \text{parity}, \text{reads}, \text{writes}, \text{ord}, \text{tenvBar} \rangle \\
\text{Get the order in which this transaction runs} \\
\text{Ord}(t) & \triangleq \text{CHOOSE } i \in \text{DOMAIN } \text{ord.to} : \text{ord.to}[i] = t \\
\text{SetFate} & \triangleq \wedge \text{Done} \\
& \wedge \text{fateIsSet} = \text{FALSE} \\
& \wedge \text{fateIsSet}' = \text{TRUE} \\
& \wedge \text{ord}' = \text{CHOOSE } r \in [\text{to} : [1 \dots N \rightarrow CT], \text{benv} : [1 \dots N + 1 \rightarrow [Obj \rightarrow Val]]] : \\
& \quad \text{first environment must be the initialization} \\
& \wedge r.\text{benv}[1] = \text{SnapInit} \\
& \quad \text{to must be a total ordering} \\
& \wedge \forall i, j \in 1 \dots N : r.\text{to}[i] = r.\text{to}[j] \Rightarrow i = j \\
& \wedge \forall i \in 1 \dots N : \text{LET } t \triangleq r.\text{to}[i] \text{ IN} \\
& \quad \text{all non-written reads have to be consistent with transaction's snapshot} \\
& \wedge \forall obj \in \text{reads}[t] : r.\text{benv}[i][obj] = \text{GetVer}(obj, \text{vis}[t] \setminus \{t\}).\text{val} \\
& \quad \text{all writes have to be consistent with transaction's environment} \\
& \wedge \forall obj \in \text{writes}[t] : r.\text{benv}[i + 1][obj] = \text{Get}(t, obj) \\
& \quad \text{if a variable changed, there must be a corresponding write} \\
& \wedge \forall obj \in Obj : (r.\text{benv}[i + 1][obj] \neq r.\text{benv}[i][obj]) \Rightarrow obj \in \text{writes}[t] \\
& \wedge \text{tenvBar}' = \text{LET } \text{ordp} \triangleq \text{ord}' \\
& \quad \quad \text{benv} \triangleq \text{ordp}.\text{benv} \\
& \quad \quad \text{to} \triangleq \text{ordp}.\text{to} \text{ IN} \\
& \quad [t \in CT \mapsto \text{LET } i \triangleq \text{CHOOSE } i \in \text{DOMAIN } \text{to} : \text{to}[i] = t \text{ IN } \text{benv}[i]] \\
& \wedge \text{UNCHANGED } \langle op, arg, rval, tr, db, vis, tstate, tid, \text{deadlocked}, \\
& \quad \quad h, \text{canIssue}, \text{parity}, \text{reads}, \text{writes}, \text{rds}, \text{inc}, \text{outc} \rangle \\
\text{Issue} & \triangleq \wedge h \neq \langle \rangle \\
& \wedge \text{fateIsSet} \\
& \wedge \text{canIssue}' = \text{TRUE} \\
& \wedge h' = \text{IF } \text{canIssue} \text{ THEN } \text{Tail}(h) \text{ ELSE } h \\
& \wedge h' \neq \langle \rangle \\
& \quad \text{tenvBar}' \text{ needs to reflect the state of the *next* head in the history, not the current head} \\
& \wedge \text{tenvBar}' = \text{LET } e \triangleq \text{Head}(h') \\
& \quad \quad obj \triangleq e.\text{arg}[1] \\
& \quad \quad val \triangleq e.\text{arg}[2] \\
& \quad \quad t \triangleq e.\text{tr}
\end{aligned}$$

$$\begin{aligned}
& \text{IN IF } tstate[e.tr] = Committed \wedge e.op = \text{"w"} \\
& \quad \text{THEN } [tenvBar \text{ EXCEPT } ![t][obj] = val] \\
& \quad \text{ELSE } tenvBar \\
& \wedge \text{UNCHANGED } \langle op, arg, rval, tr, db, vis, tstate, tid, deadlocked, \\
& \quad \quad \quad fateIsSet, parity, reads, writes, ord, rds, inc, outc \rangle \\
vv & \triangleq \langle op, arg, rval, tr, db, vis, tstate, tid, deadlocked, h, fateIsSet, canIssue, \\
& \quad \quad \quad parity, reads, writes, ord, tenvBar, rds, inc, outc \rangle \\
TerminationR & \triangleq \wedge Done \\
& \quad \wedge Tail(h) = \langle \rangle \\
& \quad \wedge \text{UNCHANGED } vv \\
NextR & \triangleq \vee \exists t \in Tr, obj \in Obj, val \in Val : \\
& \quad \vee StartTransactionR(t) \\
& \quad \vee BeginRdR(t, obj) \\
& \quad \vee EndRdR(t, obj, val) \\
& \quad \vee AbortRdR(t, obj) \\
& \quad \vee BeginWrR(t, obj, val) \\
& \quad \vee EndWrR(t, obj, val) \\
& \quad \vee AbortWrR(t, obj) \\
& \quad \vee BeginCommitR(t) \\
& \quad \vee AbortCommitR(t) \\
& \quad \vee EndCommitR(t) \\
& \quad \vee AbortR(t) \\
& \quad \vee DetectDeadlockR \\
& \quad \vee Issue \\
& \quad \vee SetFate \\
& \quad \vee TerminationR \\
SpecR & \triangleq InitR \wedge \square [NextR]_{vv} \\
trBar & \triangleq \text{IF } canIssue \text{ THEN } Head(h).tr \text{ ELSE } T0 \\
opBar & \triangleq \text{IF } canIssue \text{ THEN } Head(h).op \text{ ELSE } \text{"r"} \\
argBar & \triangleq \text{CASE } canIssue \wedge Head(h).arg = \langle \rangle \rightarrow None \\
& \quad \square \quad canIssue \wedge Head(h).arg \neq \langle \rangle \rightarrow Head(h).arg \\
& \quad \square \quad \text{OTHER} \rightarrow \text{CHOOSE } obj \in Obj : \text{TRUE} \\
rvalBar & \triangleq \text{CASE } canIssue \wedge Head(h).rval \neq Err \rightarrow Head(h).rval \\
& \quad \square \quad canIssue \wedge Head(h).rval = Err \rightarrow Ok \\
& \quad \square \quad \text{OTHER} \rightarrow V0 \\
tstateBar & \triangleq [t \in TrR \mapsto \\
& \quad \text{LET } s \triangleq Head(h).tstate[t] \text{ IN} \\
& \quad \text{CASE } \neg canIssue & \rightarrow Open \\
& \quad \square \quad canIssue \wedge s = Unstarted & \rightarrow Open \\
& \quad \square \quad canIssue \wedge s = Open & \rightarrow Open \\
& \quad \square \quad canIssue \wedge s = Committed & \rightarrow Committed
\end{aligned}$$

$$\square \quad canIssue \wedge s = Aborted \quad \rightarrow Aborted]$$

$$ffBar \triangleq \text{LET } Parity(hh) \triangleq Len(SelectSeq(hh, \text{LAMBDA } e : e.op \in \{“r”, “w”\})) \% 2$$

$$p \triangleq Parity(h)$$

$$opp \triangleq Head(h).opIN$$

$$\text{CASE } \neg canIssue \quad \rightarrow Flip$$

$$\square \quad canIssue \wedge opp \in \{“r”, “w”\} \wedge parity = p \rightarrow Flop$$

$$\square \quad canIssue \wedge opp \notin \{“r”, “w”\} \wedge parity = p \rightarrow Flip$$

$$\square \quad canIssue \wedge opp \in \{“r”, “w”\} \wedge parity \neq p \rightarrow Flip$$

$$\square \quad canIssue \wedge opp \notin \{“r”, “w”\} \wedge parity \neq p \rightarrow Flop$$

$$fateBar \triangleq \text{IF } \neg fateIsSet \text{ THEN } NULL$$

$$\text{ELSE } [t \in TrR \mapsto tstate[t]]$$

$$Ser \triangleq \text{INSTANCE } SerializabilityD \text{ WITH}$$

$$Tr \leftarrow TrR,$$

$$tr \leftarrow trBar,$$

$$op \leftarrow opBar,$$

$$arg \leftarrow argBar,$$

$$rval \leftarrow rvalBar,$$

$$tstate \leftarrow tstateBar,$$

$$fate \leftarrow fateBar,$$

$$to \leftarrow ord.to,$$

$$tenv \leftarrow tenvBar,$$

$$benv \leftarrow ord.benv,$$

$$ff \leftarrow ffBar,$$

$$Vinit \leftarrow V0$$

$$SerSpec \triangleq Ser!SpecD$$

$$\text{THEOREM } SpecR \Rightarrow SerSpec$$
