

```

#include "reg51.h"
sfr P1M0=0x92; //p71
sfr P1M1=0x91; //p71
sfr P4M0=0xB4; //P71
sfr P4M1=0xB3; //p71
//sfr P1=0x90;    //p72
sfr P4=0xc0;    //p72
sbit P1_6=P1^6;
sbit P1_7=P1^7;
sbit P4_6=P4^6;
sbit P4_7=P4^7;
void main()
{
    volatile bit a=1,b=0;
    P1M0=0;
    P1M1=0;
    P4M0=0;
    P4M1=0;
    P1_6=a & b;
    P1_7=a | b;
    P4_6=a ^ b;
    P4_7=~a;
}

```