

## Hardware/Software Codesign SystemC Exercise One

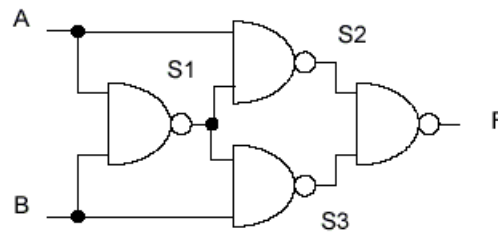


Fig. 1. EXOR Gate Diagram

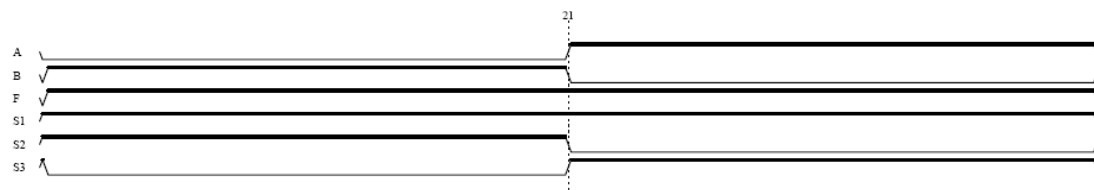


Fig. 2. Output Traces of the EXOR-2 SystemC Program.

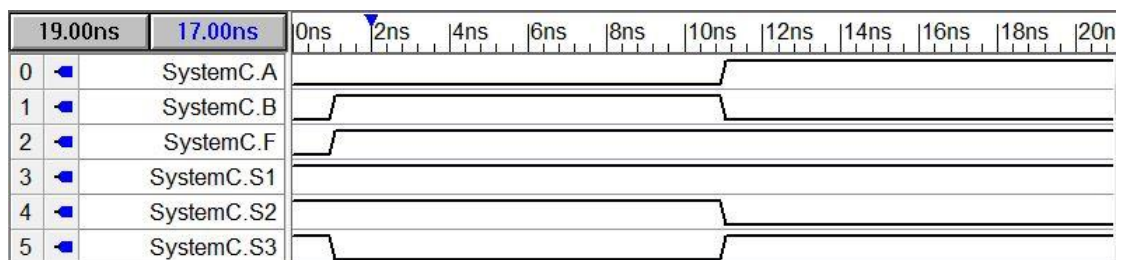


Fig. 3. My Output Traces of the EXOR-2 SystemC Program using waveview software

系統環境：Visual Studio 2012

vcd 讀取軟體：waveview