Hardware/Software Codesign SystemC Exercise One

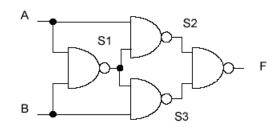


Fig. 1. EXOR Gate Diagram

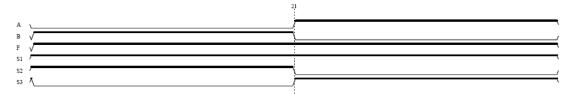


Fig. 2. Output Traces of the EXOR-2 SystemC Program.

19.00ns		Ons 17.00ns	0ns 2ns 4ns 6ns 8ns 10ns 12ns 14ns 16ns 18ns 20n
0	-	SystemC.A	
1	-	SystemC.B	
2	-	SystemC.F	
3	-	SystemC.S1	
4	-	SystemC.S2	
5	-	SystemC.S3	<u></u>

Fig. 3. My Output Traces of the EXOR-2 SystemC Program using waveview software

系統環境: Visual Studio 2012

vcd 讀取軟體: waveview