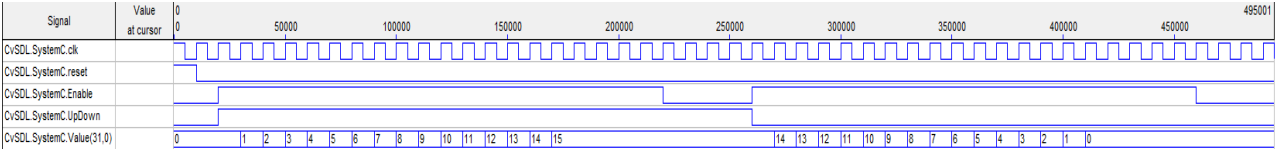


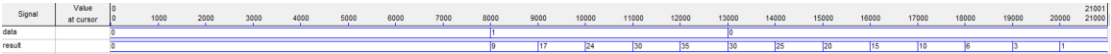
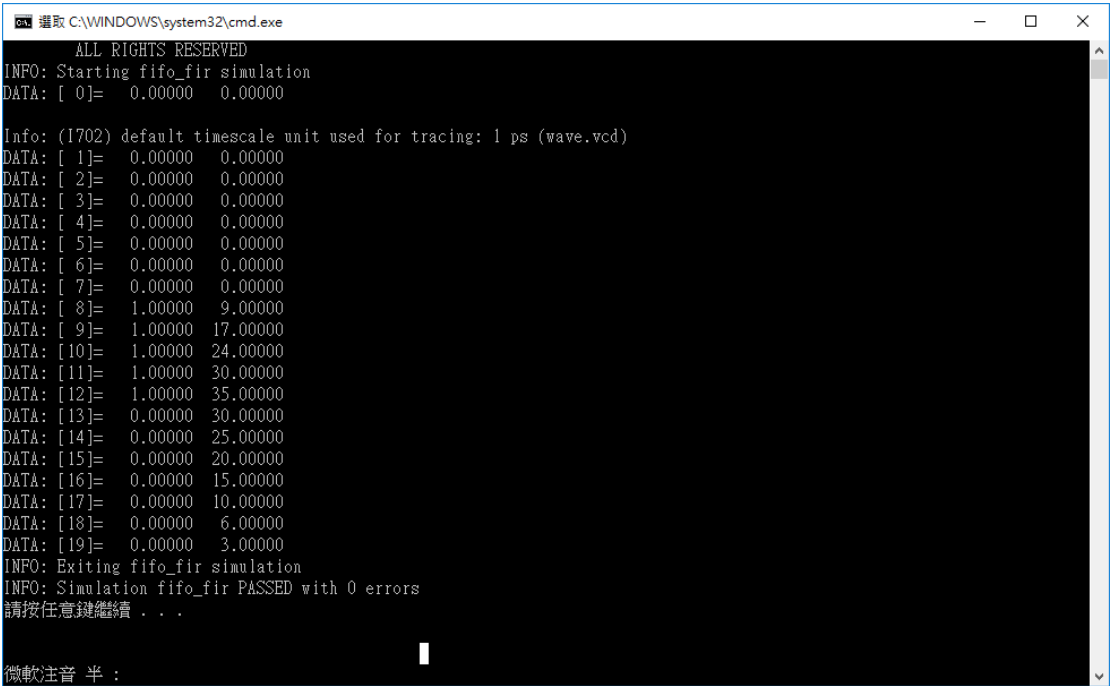
# MSOC HW1 Report

## 1. Counter example waveform

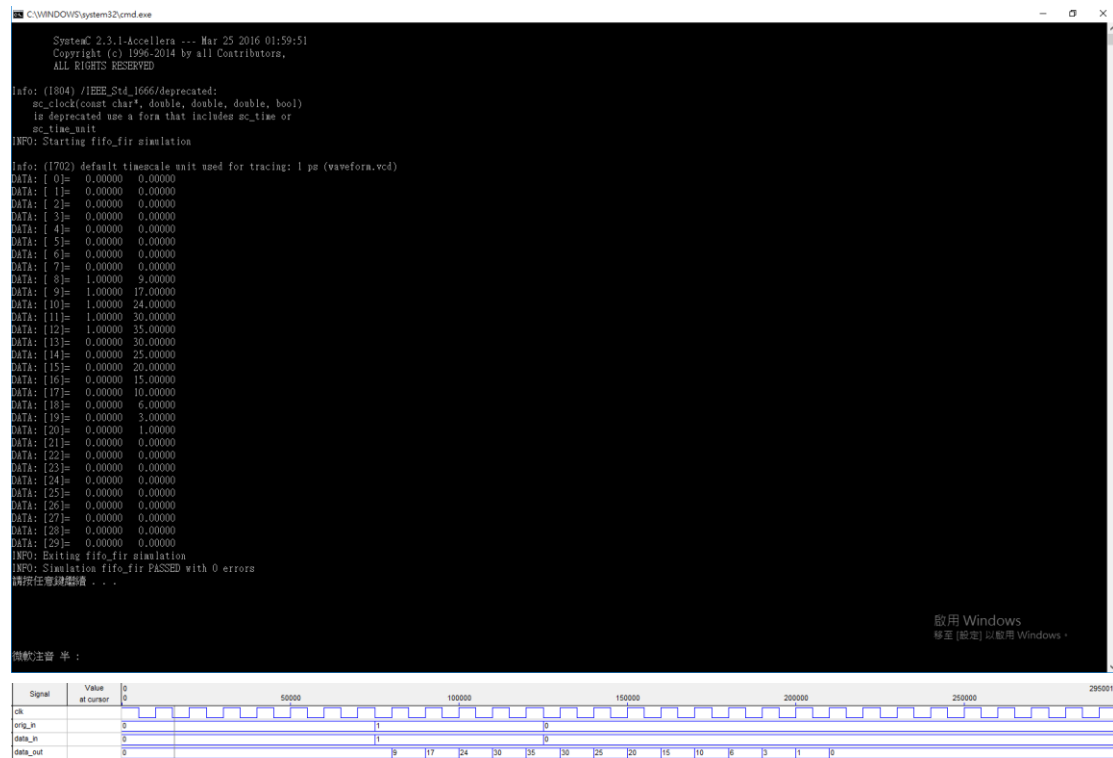


## 2.

### (1)



(2)



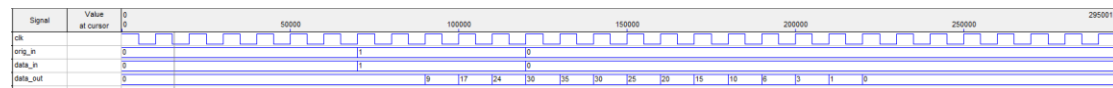
(3)

```
C:\WINDOWS\system32\cmd.exe
sc_time_unit
INFO: Starting fifo_fir simulation
DATA: [ 0]= 0.00000 0.00000

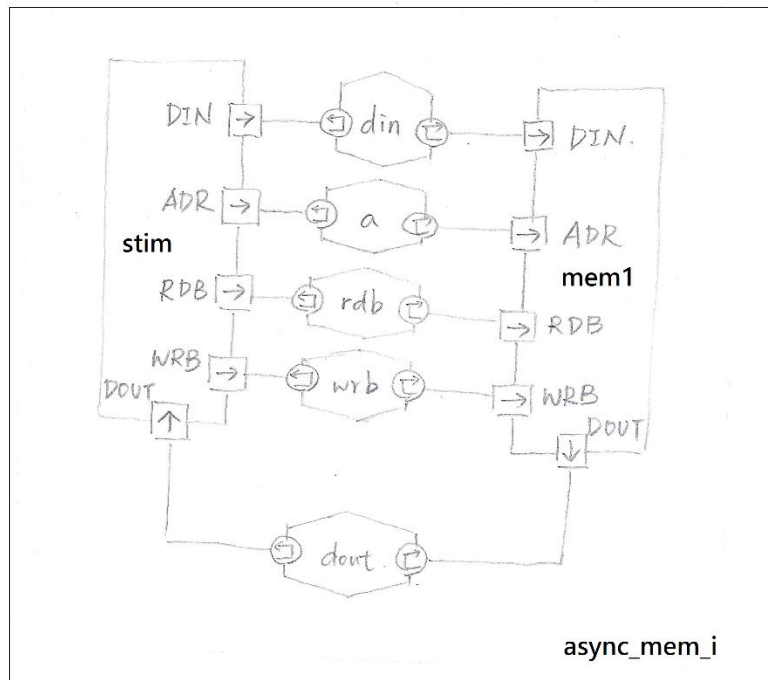
Info: (1702) default timescale unit used for tracing: 1 ps (waveform.vcd)
DATA: [ 1]= 0.00000 0.00000
DATA: [ 2]= 0.00000 0.00000
DATA: [ 3]= 0.00000 0.00000
DATA: [ 4]= 0.00000 0.00000
DATA: [ 5]= 0.00000 0.00000
DATA: [ 6]= 0.00000 0.00000
DATA: [ 7]= 0.00000 0.00000
DATA: [ 8]= 1.00000 0.00000
DATA: [ 9]= 1.00000 0.00000
DATA: [10]= 1.00000 9.00000
DATA: [11]= 1.00000 17.00000
DATA: [12]= 1.00000 24.00000
DATA: [13]= 0.00000 30.00000
DATA: [14]= 0.00000 35.00000
DATA: [15]= 0.00000 30.00000
DATA: [16]= 0.00000 25.00000
DATA: [17]= 0.00000 20.00000
DATA: [18]= 0.00000 15.00000
DATA: [19]= 0.00000 10.00000
DATA: [20]= 0.00000 6.00000
DATA: [21]= 0.00000 3.00000
DATA: [22]= 0.00000 1.00000
DATA: [23]= 0.00000 0.00000
DATA: [24]= 0.00000 0.00000
DATA: [25]= 0.00000 0.00000
DATA: [26]= 0.00000 0.00000
DATA: [27]= 0.00000 0.00000
DATA: [28]= 0.00000 0.00000
DATA: [29]= 0.00000 0.00000
DATA: [30]= 0.00000 0.00000
INFO: Exiting fifo_fir simulation
INFO: Simulation fifo_fir PASSED with 0 errors
线程正在等待结束...
```

使用 Windows  
移至 [确定] 以使用 Windows

错误的注册 半 :



2. The structure of the example *async\_mem*



Main