

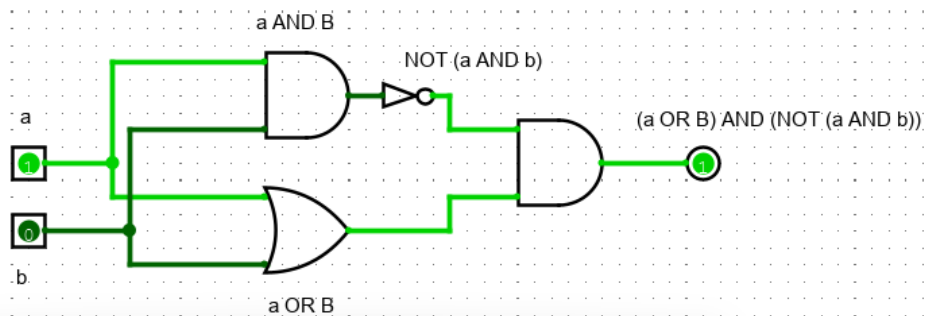
Reading assigned January 9, 2023

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I did not have any questions or confusions about the reading, but I also found the section on switching circuits to be very interesting since I've seen them show up in Computer Architecture. Will we be reviewing that section later, or is not relevant to our study of logic?

Just for fun I built a simple circuit implementing exclusive-or from basic AND and OR gates, but the modeling program also supports exclusive-or as a built-in gate (and much more complicated logic and computing circuits)

If classmates are interested I would be happy to demo how to model logic as with logisim.



Combinational Analysis

File Edit Project Simulate FPGA Window Help

Inputs & Outputs Table Expression Minimized

x 1 0 Collapse Duplicate Rows Show All Rows

4 of 4 rows shown

a	b	x
0	0	0
0	1	1
1	0	1
1	1	0

Import Table Build Circuit Export Table

Exercises

1. Give an example of a set S of wffs such that every two-element subset of S is satisfiable but S is not satisfiable.
(It is possible to do this with S having exactly 3 elements, but it is okay if your set is bigger.)

Let S be a set of 3 wffs such that two wffs are independent of each other but the third wff places a condition on the first two wffs, such as:

$$S = \{\alpha, \beta, \neg(\alpha \wedge \beta)\}.$$

with the two-element subsets

$$S_1 = \{\alpha, \beta\}$$

$$S_2 = \{\alpha, \neg(\alpha \wedge \beta)\}$$

$$S_3 = \{\beta, \neg(\alpha \wedge \beta)\}$$

Then, S is not satisfiable (since setting $\alpha = T$ and $\beta = T$ violates the third wff).

However, each of the subsets S_1, S_2, S_3 is satisfiable.

Another example:

$$S = \{\neg\alpha, \neg\beta, (\alpha \vee \beta)\}$$

$$S_1 = \{\neg\alpha, \neg\beta\}$$

$$S_2 = \{\neg\alpha, (\alpha \vee \beta)\}$$

$$S_3 = \{\neg\beta, (\alpha \vee \beta)\}$$