QINGLIN LI

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EDUCATION

Northwestern University

SEP 2015 - DEC 2016 (Expected)

M.S. in Computer Science

Coursework: Machine Learning, Biometrics, Data Science

Shanghai Jiao Tong University, China GPA: 86.8/100

SEP 2011 - JUN 2015

B.S. in Computer Science & Engineering

Coursework: Statistical Learning, Scientific Computing, Algorithms, Operating System, Computer Network, Database, Computer Architecture, etc.

SKILLS

Programming Languages C/C++, Java, Ruby, Python, MATLAB

Application Development HTML, CSS, Bootstrap, Ruby on Rails, Java Swing

Machine Learning & Vision scikit-learn, scikit-image, OpenCV

Tools & Others Vim, LATEX, Linux/Mac OS X, MySQL, Git

EXPERIENCE

Research Intern Microsoft Research Asia, Beijing

AUG 2014 - FEB 2015

Team: Natural Language Computing, Mentor: Dr. Mu Li

- Collected text data from Internet and analyzed the English-Chinese relationship with rules, using Python and Mechanize.
- Implemented a graphical model to solve entities translation problem between English and Chinese knowledge bases by utilizing the relations between entities. Outperformed traditional machine translation algorithms. Using Python, NumPy and scikit-learn.

Research Student BCMI Lab, Shanghai Jiao Tong University

JUL 2013 - AUG 2014

Research Area: Computer Vision, Mentor: Prof. Liqing Zhang

• Worked on mining rich low-level computer vision features and testing different regression models to solve crowd density estimation problem, using Python, scikit-learn and OpenCV.

COURSE PROJECTS

GitHub: https://github.com/lostleaf

Database System Lab Shanghai Jiao Tong University

FALL 2014

• Developed a relational database system supporting JDBC interface including SQL parser, query engine and file storage in Java.

Nachos Operating System Lab Shanghai Jiao Tong University

FALL 2013

• Implemented threading and multiprogramming, virtual memory, and a self-designed file system within the Nachos operating system in Java.

Compiler Lab Shanghai Jiao Tong University

SPRING 2013

• Developed a compiler supporting most features of C Language and targeting the MIPS architecture including parser, syntax checking and linear scan algorithm for register allocation in Java.

Computer Architecture Lab Shanghai Jiao Tong University

SPRING 2013

• Simulated a CPU with Tomasula algorithm for dynamic instruction scheduling in Verilog.