

# PRTR5V0U4D

# Ultra low capacitance quadruple rail-to-rail ESD protection

Rev. 2 — 5 March 2012

**Product data sheet** 

### 1. Product profile

### 1.1 General description

Ultra low capacitance quadruple rail-to-rail ElectroStatic Discharge (ESD) protection device in an SOT457 (SC-74) small Surface-Mounted Device (SMD) plastic package.

The device is designed to protect four high-speed data lines or high-frequency signal lines from the damage caused by ESD and other transients.

PRTR5V0U4D integrates four ultra low capacitance rail-to-rail ESD protection channels and one additional ESD protection diode to ensure signal line protection even if no supply voltage is available.

#### 1.2 Features and benefits

- ESD protection of four high-speed data lines or high-frequency signal lines
- Ultra low input/output to ground capacitance: C<sub>(I/O-GND)</sub> = 1 pF
- ESD protection up to 8 kV
- IEC 61000-4-2, level 4 (ESD)
- Very low clamping voltage due to an integrated additional ESD protection diode
- Very low reverse current
- AEC-Q101 qualified
- Small SMD plastic package

### 1.3 Applications

- USB 2.0 interfaces
- Digital Video Interface (DVI)
- High-Definition Multimedia Interface (HDMI)
- Mobile phones
- Digital cameras
- WAN/LAN systems
- PC, notebooks, printers and other PC peripherals



### 1.4 Quick reference data

Table 1. Quick reference data

 $T_{amb} = 25$  °C unless otherwise specified.

· aiiib — -						
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per chann	nel					
C <sub>(I/O-GND)</sub>	input/output to ground capacitance	$V_{(I/O-GND)} = 0 V;$ $V_{CC} = 3 V;$ f = 1 MHz	<u>[1]</u> -	1.0	-	pF
Zener dio	de					
VI	input voltage		0	-	5.5	V
C <sub>sup</sub>	supply pin to ground capacitance	$V_{(I/O-GND)} = 0 V;$ $V_{CC} = 3 V;$ f = 1 MHz	[2] -	40	-	pF

<sup>[1]</sup> Measured from pins 1, 3, 4 and 6 to pin 2.

## 2. Pinning information

Table 2. Pinning

I GOIO Z.	9			
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I/O1	input/output 1	D. D. D.	
2	GND	ground	<u> </u>	6 5 4
3	I/O2	input/output 2	0	
4	I/O3	input/output 3	1 1 2 13	
5	$V_{CC}$	supply voltage		
6	I/O4	input/output 4		
				001aag273

## 3. Ordering information

Table 3. Ordering information

Type number	Package	Package				
	Name	Description	Version			
PRTR5V0U4D	SC-74	plastic surface-mounted package (TSOP6); 6 leads	SOT457			

## 4. Marking

Table 4. Marking code

Type number	Marking code
PRTR5V0U4D	4D

PRTR5V0U4D

<sup>[2]</sup> Measured from pin 5 to pin 2.

### 5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Per device					
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-55	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

Table 6. ESD maximum ratings

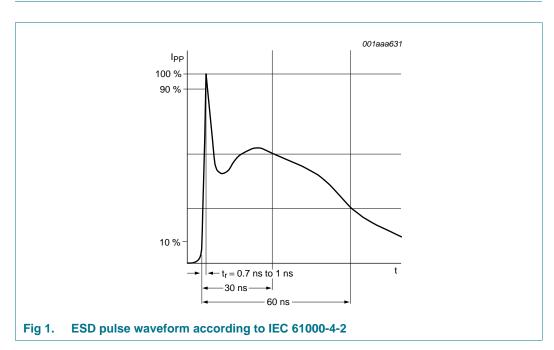
 $T_{amb} = 25$  °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Max	Unit
Per channel	I				
V <sub>ESD</sub>	electrostatic discharge voltage	IEC 61000-4-2 (contact discharge)	[1][2] _	8	kV
		MIL-STD-883 (human body model)	-	8	kV

<sup>[1]</sup> Device stressed with ten non-repetitive ESD pulses.

Table 7. ESD standards compliance

Standard	Conditions
Per channel	
IEC 61000-4-2; level 4 (ESD)	> 8 kV (contact)
MIL-STD-883; class 3B (human body model)	> 8 kV



<sup>[2]</sup> Measured from pin 1, 3, 4 or 6 to pin 2 or 5.

### 6. Characteristics

Table 8. Characteristics

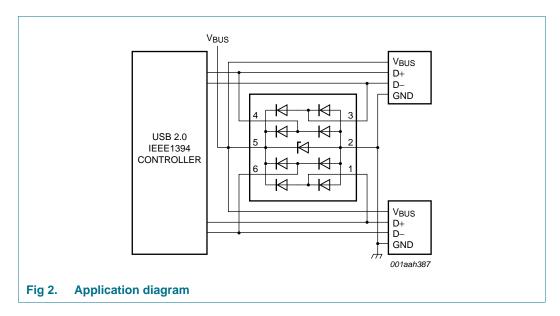
 $T_{amb} = 25$  °C unless otherwise specified.

	<b>a</b>		_		
Parameter	Conditions	Min	Тур	Max	Unit
el					
reverse leakage current	V <sub>R</sub> = 3 V	<u>[1]</u> -	-	100	nA
input/output to ground capacitance	$V_{(I/O-GND)} = 0 \text{ V};$ $V_{CC} = 3 \text{ V}; f = 1 \text{ MHz}$	[1] -	1.0	-	pF
forward voltage		-	0.7	-	V
le					
input voltage		0	-	5.5	V
breakdown voltage	$I_I = 1 \text{ mA}$	6	-	9	V
supply pin to ground capacitance	$V_{(I/O-GND)} = 0 \text{ V};$ $V_{CC} = 3 \text{ V}; f = 1 \text{ MHz}$	[2] _	40	-	pF
	reverse leakage current input/output to ground capacitance forward voltage le input voltage breakdown voltage supply pin to ground	reverse leakage current $V_R = 3 \text{ V}$ input/output to ground capacitance $V_{(I/O\text{-}GND)} = 0 \text{ V};$ forward voltage  input voltage  breakdown voltage $I_I = 1 \text{ mA}$ supply pin to ground $V_{(I/O\text{-}GND)} = 0 \text{ V};$	reverse leakage current $V_R = 3 \text{ V}$ [1] - input/output to ground capacitance $V_{(I/O\text{-}GND)} = 0 \text{ V}$ ; $V_{CC} = 3 \text{ V}$ ; $f = 1 \text{ MHz}$ forward voltage - input voltage $V_{II} = 1 \text{ mA}$ $V_{II$	reverse leakage current $V_R = 3 \text{ V}$ [1] input/output to ground capacitance $V_{(I/O\text{-}GND)} = 0 \text{ V}$ ; $V_{CC} = 3 $	reverse leakage current $V_R = 3 \text{ V}$

<sup>[1]</sup> Measured from pins 1, 3, 4 and 6 to pin 2.

### 7. Application information

The device is designed for the protection of for example, two USB 2.0 ports against ESD. Each device is capable to protect both, USB data lines and the  $V_{BUS}$  supply.



### 8. Test information

### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

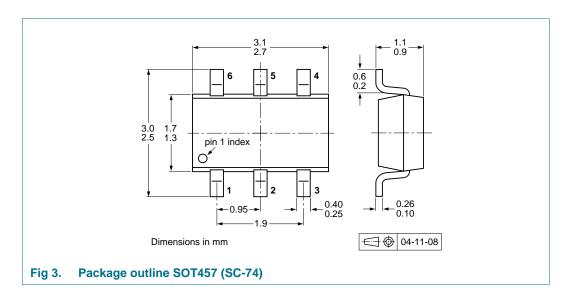
PRTR5V0U4D

All information provided in this document is subject to legal disclaimers.

© NXP B.V. 2012. All rights reserved.

<sup>[2]</sup> Measured from pin 5 to pin 2.

### 9. Package outline



## 10. Packing information

Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

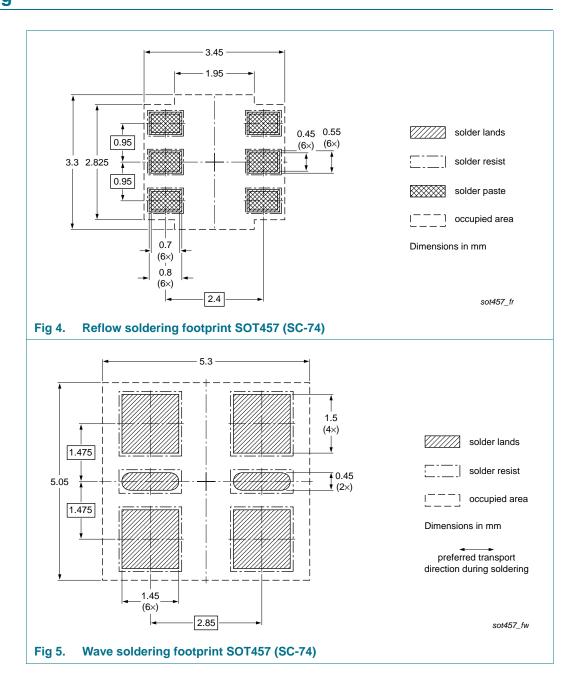
Type number	Package	Description		Packing quantity	
				3000	10000
PRTR5V0U4D SOT457		4 mm pitch, 8 mm tape and reel; T1	[2]	-115	-135
		4 mm pitch, 8 mm tape and reel; T2	[3]	-125	-165

<sup>[1]</sup> For further information and the availability of packing methods, see  $\underline{\text{Section 14}}$ .

[2] T1: normal taping

[3] T2: reverse taping

## 11. Soldering





## 12. Revision history

### Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
PRTR5V0U4D v.2	20120305	Product data sheet	-	PRTR5V0U4D v.1		
Modifications:	Section 1 "P	roduct profile": reshaped				
	<ul> <li>Section 1.4 "Quick reference data": added</li> </ul>					
<ul> <li><u>Section 2</u>: updated</li> </ul>						
<ul> <li><u>Section 4 "Marking"</u>: added</li> <li><u>Section 5 "Limiting values"</u>: reshaped and updated; junction temperate Table 7 and Figure 1 added</li> </ul>						
				nperature $T_j$ added; <u>Table 6</u> ,		
	<ul> <li>Section 6 "C</li> </ul>	<ul> <li>Section 6 "Characteristics": reshaped; I<sub>LR</sub> redefined to I<sub>RM</sub></li> </ul>				
	<ul> <li><u>Section 8 "Test information"</u>: added</li> <li><u>Figure 3</u>: replaced by minimized outline drawing</li> <li><u>Section 10 "Packing information"</u>: added</li> <li><u>Section 11 "Soldering"</u>: added</li> </ul>					
	<ul> <li>Section 13 "I</li> </ul>	Legal information": updated				
PRTR5V0U4D v.1	20080111	Product data sheet	-	-		

### 13. Legal information

#### 13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <a href="http://www.nxp.com">http://www.nxp.com</a>.

#### 13.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

#### 13.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use in automotive applications — This NXP Semiconductors product has been qualified for use in automotive applications. Unless otherwise agreed in writing, the product is not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <a href="http://www.nxp.com/profile/terms">http://www.nxp.com/profile/terms</a>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.



No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

#### 13.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

### 14. Contact information

For more information, please visit: <a href="http://www.nxp.com">http://www.nxp.com</a>

For sales office addresses, please send an email to: salesaddresses@nxp.com

# PRTR5V0U4D

### Ultra low capacitance quadruple rail-to-rail ESD protection

## 15. Contents

1	Product profile
1.1	General description 1
1.2	Features and benefits1
1.3	Applications
1.4	Quick reference data 2
2	Pinning information 2
3	Ordering information 2
4	Marking 2
5	Limiting values 3
6	Characteristics 4
7	Application information 4
8	Test information 4
8.1	Quality information 4
9	Package outline 5
10	Packing information 5
11	Soldering 6
12	Revision history 7
13	Legal information 8
13.1	Data sheet status 8
13.2	Definitions 8
13.3	Disclaimers
13.4	Trademarks 9
14	Contact information 9
15	Contents

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.