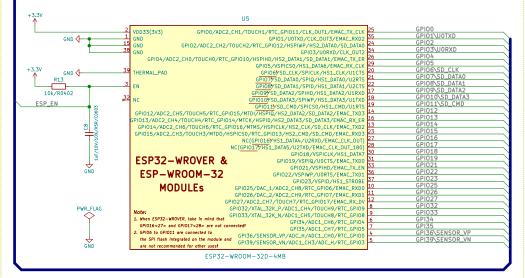


ESP32-WROOM-32x-xxx MODULE

S ESPRESSIF



Fiducials:





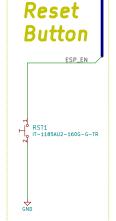




internat bootstra	pping kesistors
MTDI/GPI012:	Pull-Down
GPI00:	Pull-Up
GPI02:	Pull-Down
GP104:	Pull-Down
MTDO/GPI015:	Pull-Up
GP105:	Pull-Up

Internal Restatranging Resistors

	Ext	ension	S		
F3.3V at lout(max)=600mA, Power Outl CH	EXT1 2 3 4 5 6 7 8 9 10 11 12 12 14 14 15 16 17 18 17 18 19 HNIX19		EXT2 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 HN1x19	GPI023 GPI022 GPI01VUTXD GPI03VUDRXD GPI021 GPI019 GPI018 GPI016 GPI016 GPI016 GPI00 GPI02 GPI015 GPI016 GPI016 GPI02 GPI015 GPI016 GPI02 GPI015 GPI016 GPI02 GPI015 GPI016 GPI02 GPI015 GPI016 GPI02 GPI015 GPI016 GPI02 GPI015 GPI015 GPI015 GPI016 GPI017 GPI017 GPI017 GPI017 GPI017 GPI018 G	→ GND



		Voltage of Inter	nat LDO(VDD_SDIO)				
Pin	Default	3.3V	1.8V		Bootstrapping Pins		
GPI012/MTDI	Pull-Down	0	1	The state of			
		Bootin	g Mode				
Pin	Default	SPI Flash Boot	Download Boot	Settings			
GPI00	Pull-Up	1	0				
GPI02	Pull-Down	Don't-care	0				
		Debugging Log on I	JOTXD During Booting				
Pin Default UOTXD Toggling UOTXD Silent							
GPI015/MTD0	Pull-Up	1	0				
		Timing of	SDIO Slave				
Pîn	Default	Falling-edge:Output	Falling-edge:Input Rising-edge:Output	Rising—edge:Input Falling—edge:Output	Rising-edge:Input Rising-edge:Output		
GPI015/MTD0	Pull-Up	0	0	1	1		
GPI05	Pull-Up	0	1	0	1		





Sheet: / File: ESP32-DevKit-Lipo Rev C.sch Title: ESP32-DevKit-Lipo

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