


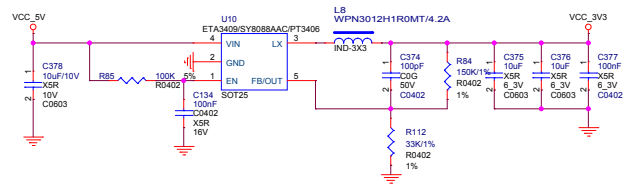
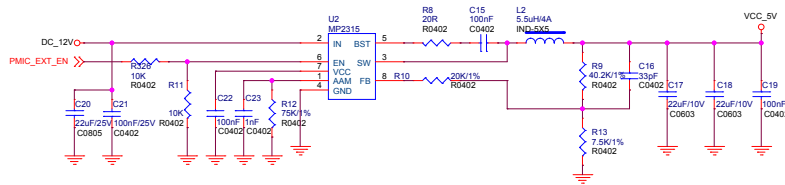
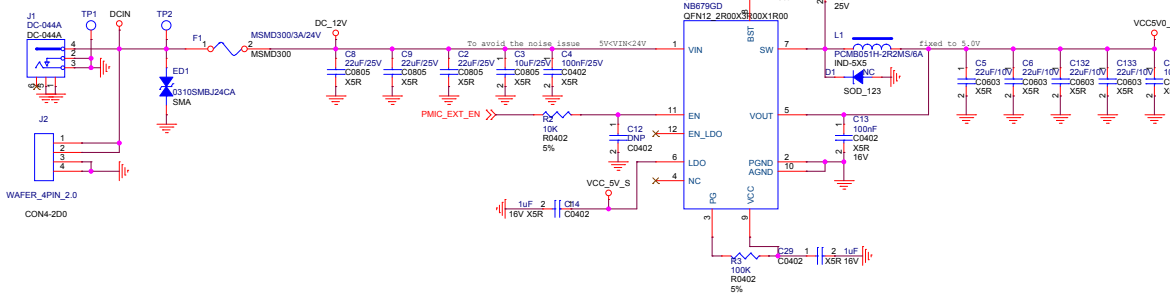
Modify note

Version	Date	Author	Change Note	Approved
V00	20190118	WXZ	First edictor	

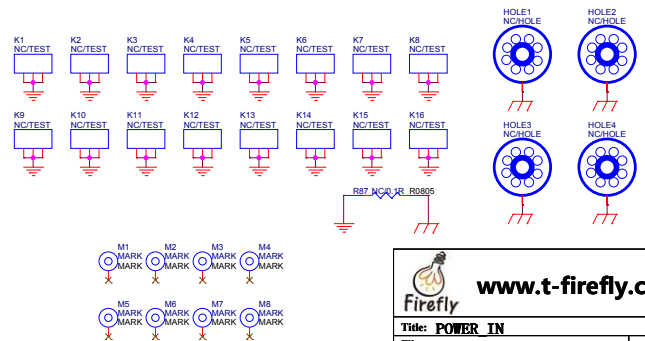
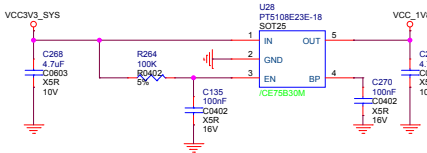
**www.t-firefly.com**

Title: Modify note	
File: MB-JD4-RK3399&3399PRO	REV: V0.0
Create Date: Thursday, October 18, 2018	Page Num: 1
Modify Date: Tuesday, April 30, 2019	Page Total: 21

DC_IN



VCC_1V8



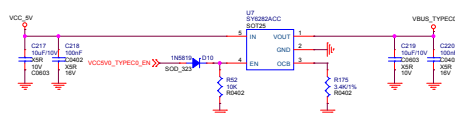
Firefly www.t-firefly.com

Title: **POWER IN**

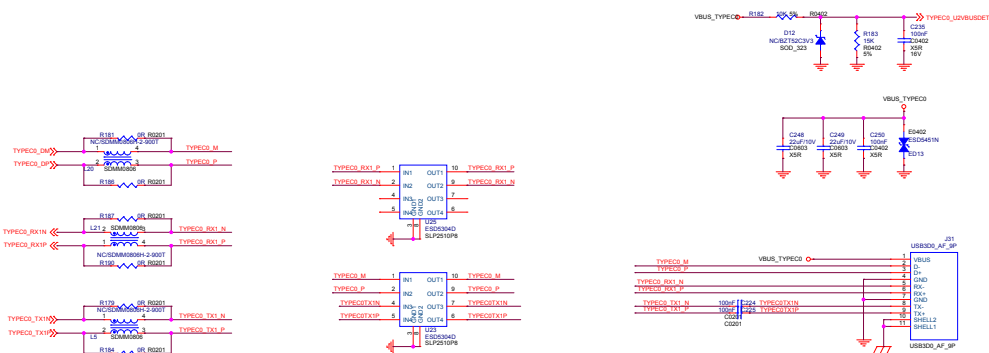
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Create Date: Thursday, October 18, 2018 Page Num: 2

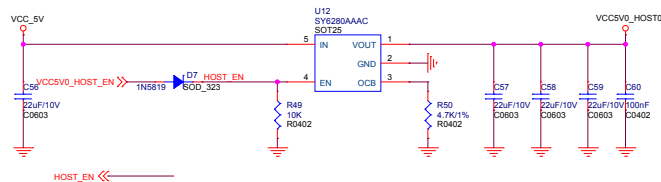
Modify Date: Tuesday, April 30, 2019 Page Total: 21



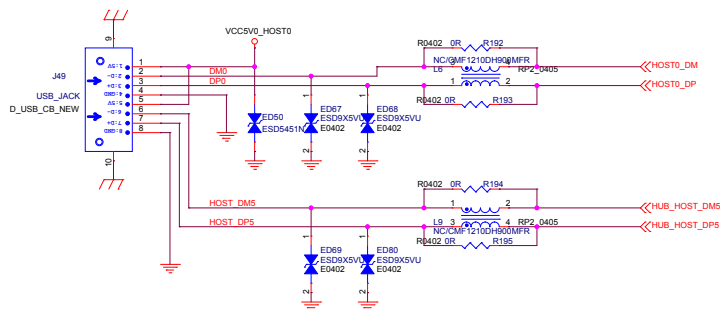
TYPEC0



HOST POWER



USB2.0

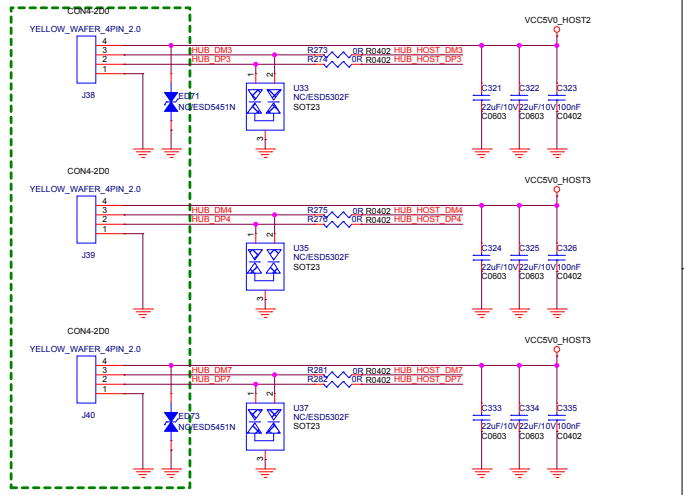


The schematic diagram illustrates the USB2.0 HOST PORT circuit. It features three USB ports (J21, J35, J37) connected to a common host controller (U6) and power supply (VCC5V0_HOST1, VCC5V0_HOST2).

Port J21 (USB3_DM, USB3_DP): Connected to U6 (NCESD5302F) via R48 (RST), R49 (R0402), and R50 (R0402). Power is supplied by VCC5V0_HOST1 through C226 (22uF/10V), C227 (22uF/10V), and C228 (100nF).

Port J35 (HUB_DM1, HUB_DP1): Connected to U6 (NCESD5302F) via R180 (RST), R181 (R0402), and R182 (R0402). Power is supplied by VCC5V0_HOST1 through C240 (22uF/10V), C247 (22uF/10V), and C251 (100nF).

Port J37 (HUB_DM2, HUB_DP2): Connected to U6 (NCESD5302F) via R271 (RST), R272 (R0402), and R273 (R0402). Power is supplied by VCC5V0_HOST2 through C318 (22uF/10V), C319 (22uF/10V), and C320 (100nF).



U33 SY6280AAAC S0125

VCC_5V

C313 10uF/10V XSR 10V C0603

C314 100nF C0402

HOST1_DP

IN 5

VOUT 4

GND 3

EN 2

OCB 1

R235 2K/1% R4042

VCC5V0_HOST1

C312 10uF/10V XSR 10V C0603

U36 SY6280AAAC S0125

VCC_5V

C331 10uF/10V XSR 10V C0603

C332 100nF C0402

HOST1_DM

IN 5

VOUT 4

GND 3

EN 2

OCB 1

R233 2K/1% R4042

VCC5V0_HOST2

C330 10uF/10V XSR 10V C0603

U38 SY6280AAAC S0125

VCC_5V

C337 10uF/10V XSR 10V C0603

C338 100nF C0402

HOST1_DP

IN 5

VOUT 4

GND 3

EN 2

OCB 1

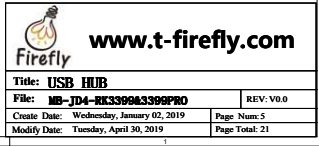
R234 2K/1% R4042

VCC5V0_HOST3

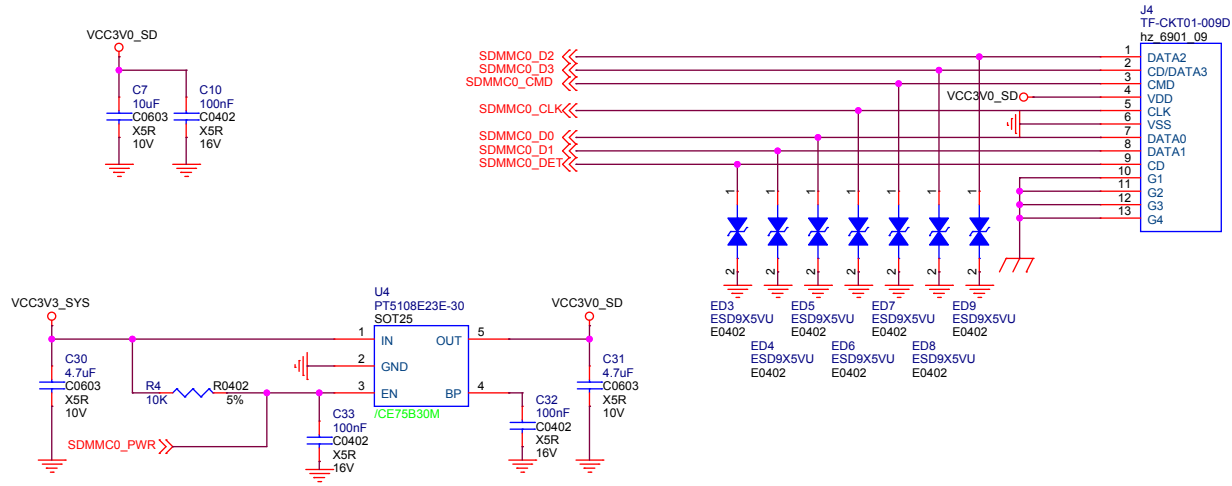
C336 10uF/10V XSR 10V C0603

Legend:

HUB HOST DP5	R287	NC/0R	R4042	HOST1_DP
HUB HOST DM5	R288	NC/0R	R4042	HOST1_DM
HUB DP5	R289	0R	R4042	HUB HOST DP5
HUB DM5	R290	0R	R4042	HUB HOST DM5
HUB DP	R291	0R	R4042	HOST1_DP
HUB DM	R307	0R	R4042	HOST1_DM



TF CARD



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Title: TF Card/EFUSE

File: MB-JD4-RK3399&3399PRO

REV: V0.0

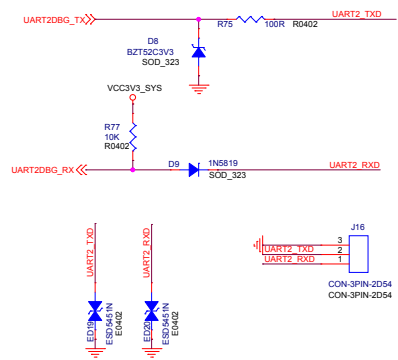
Create Date: Thursday, October 18, 2018

Page Num: 6

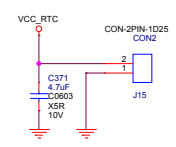
Modify Date: Tuesday, April 30, 2019

Page Total: 21

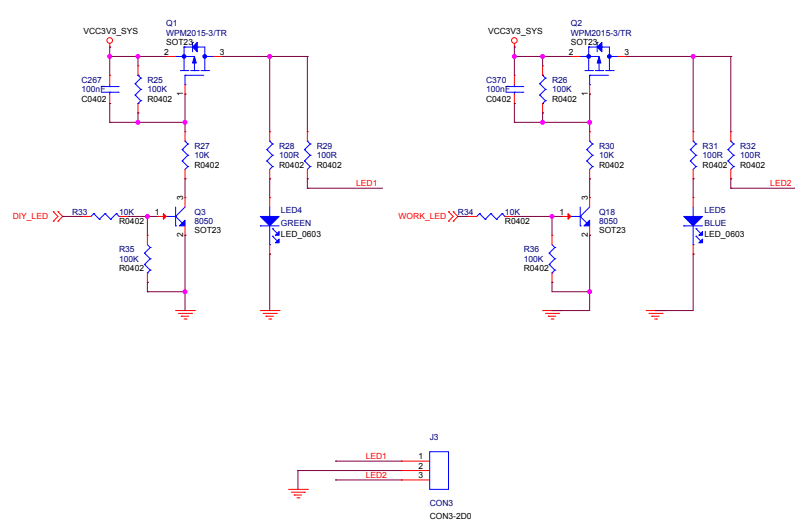
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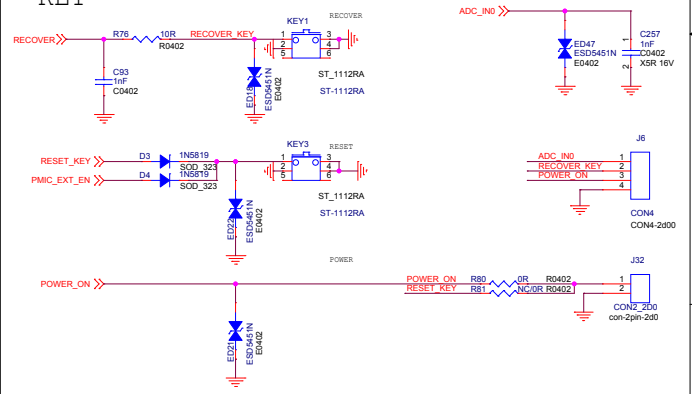
RTC_BAT




LED



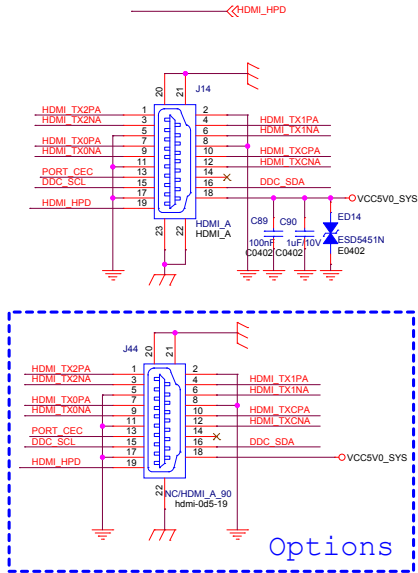
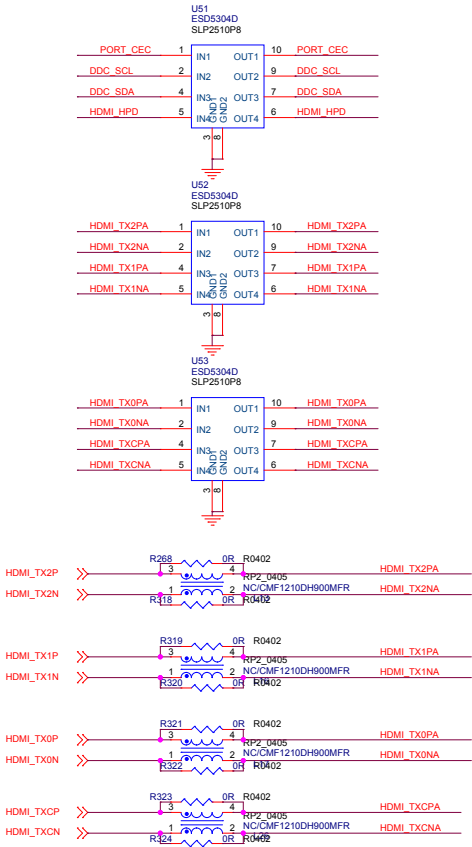
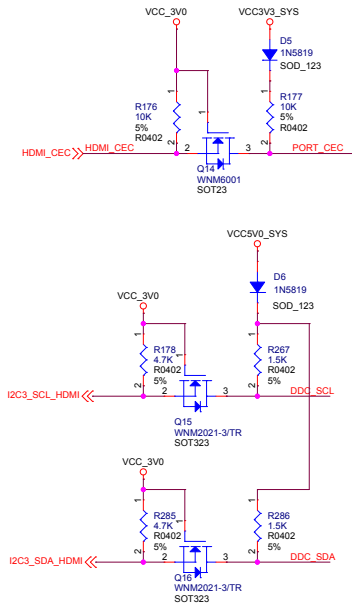
KEY




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Title: KEY/LED/DEBUG	
File: MB-JD4-RK3399&3399PRO	REV: V0.0
Create Date: Friday, October 19, 2018	Page Num: 7
Modify Date: Tuesday, April 30, 2019	Page Total: 21

HDMI OUT



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Title: **HDMI OUT**

File: **MB-JD4-RC3399L3399PRO**

Create Date: Thursday, January 03, 2019

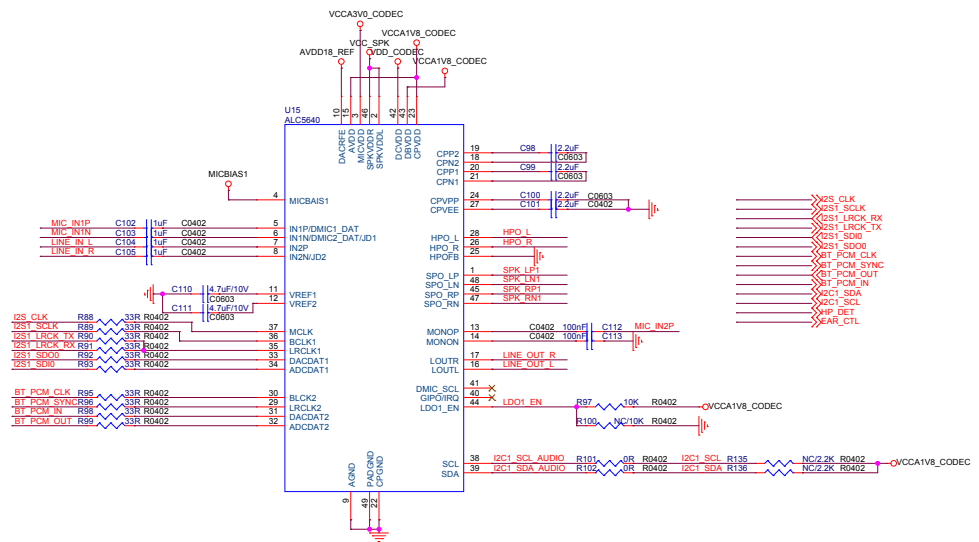
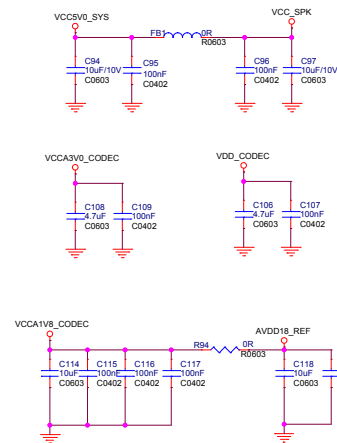
Modify Date: Tuesday, April 30, 2019

REV: V0.0

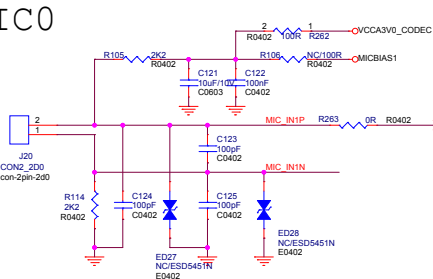
Page Num: 8

Page Total: 21

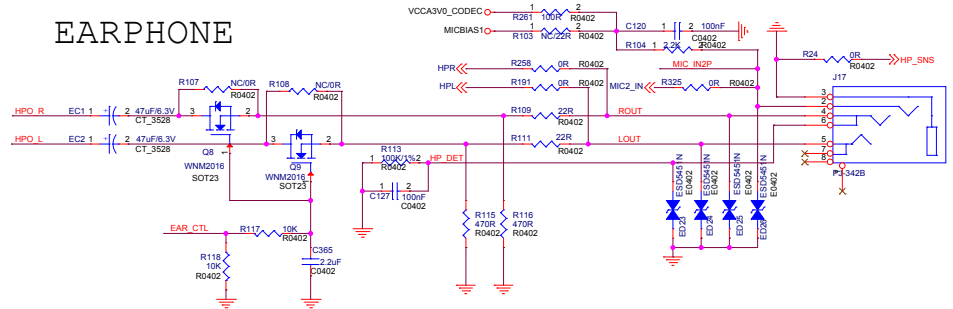
CODEC



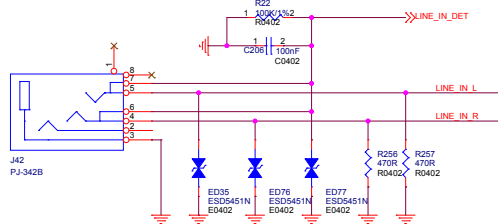
MIC0



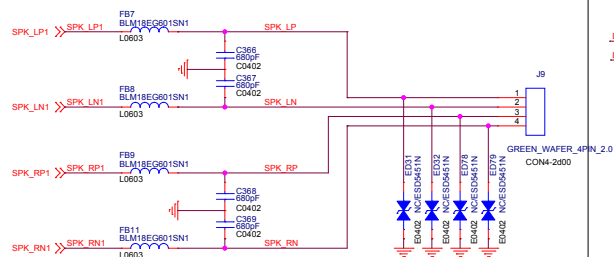
EARPHONE



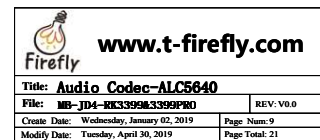
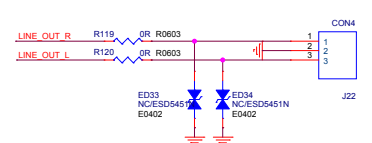
LINEIN



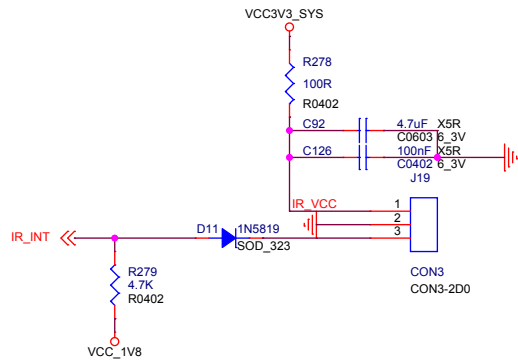
SPEAKER



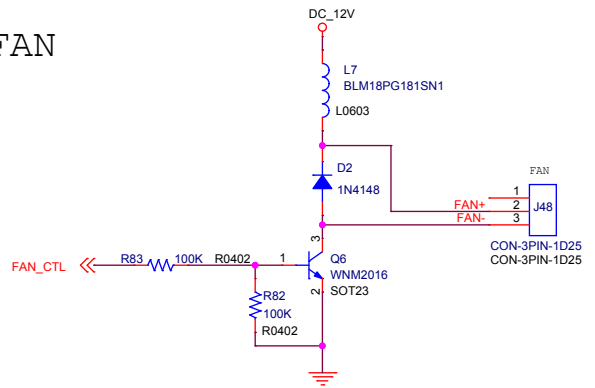
LINE OUT



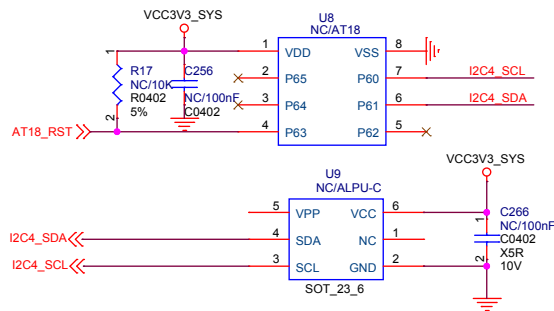
IR Receiver




FAN

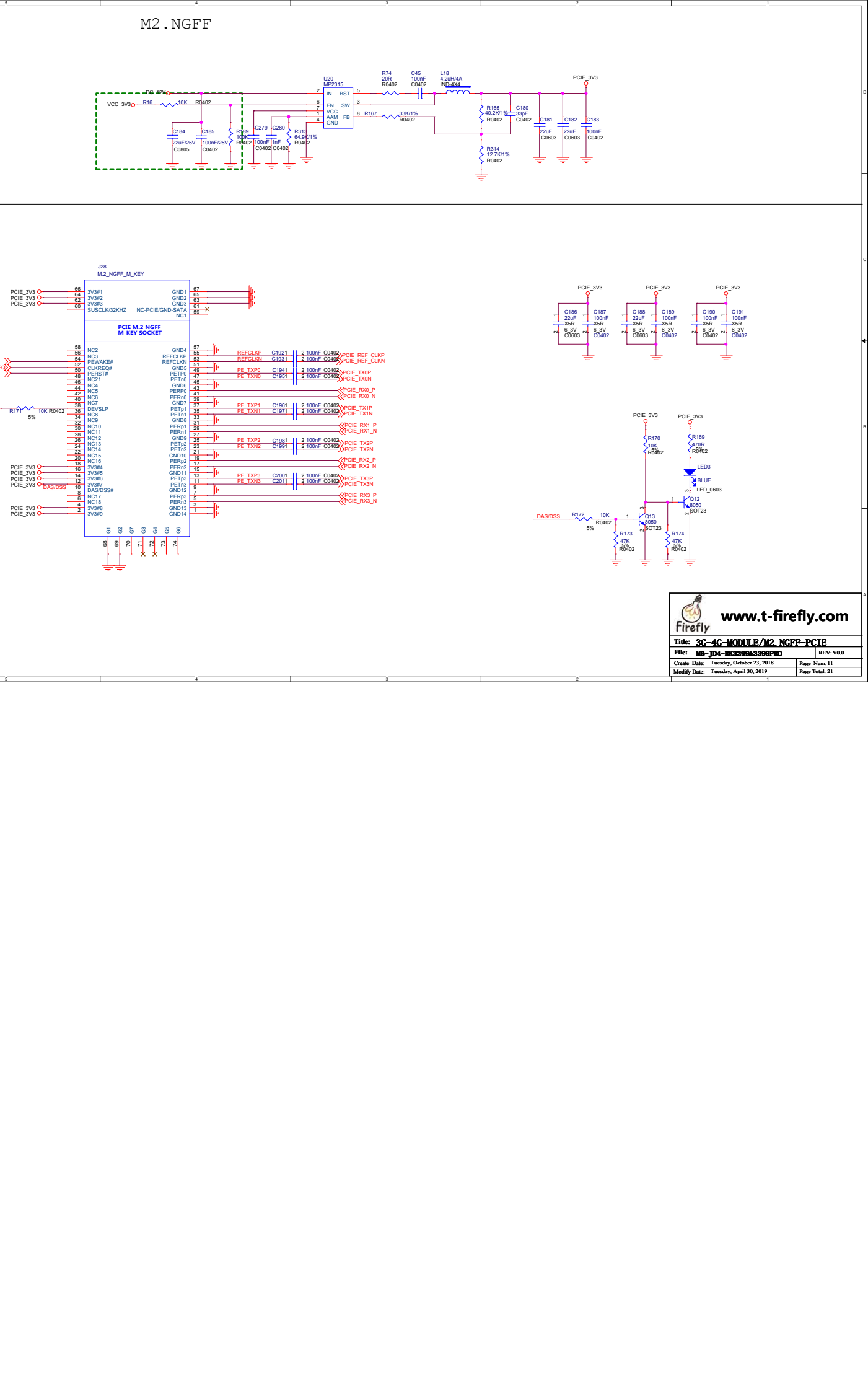
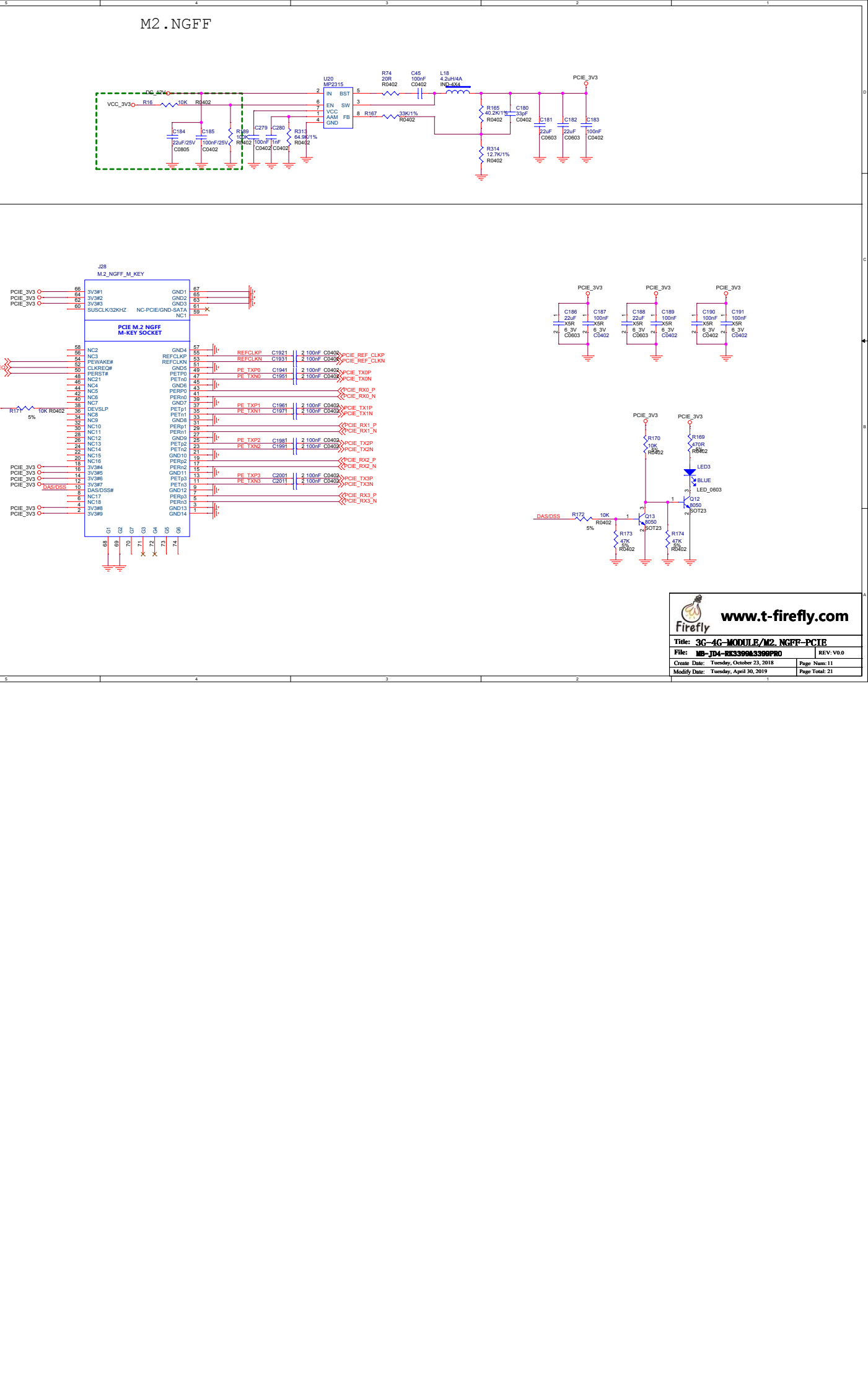
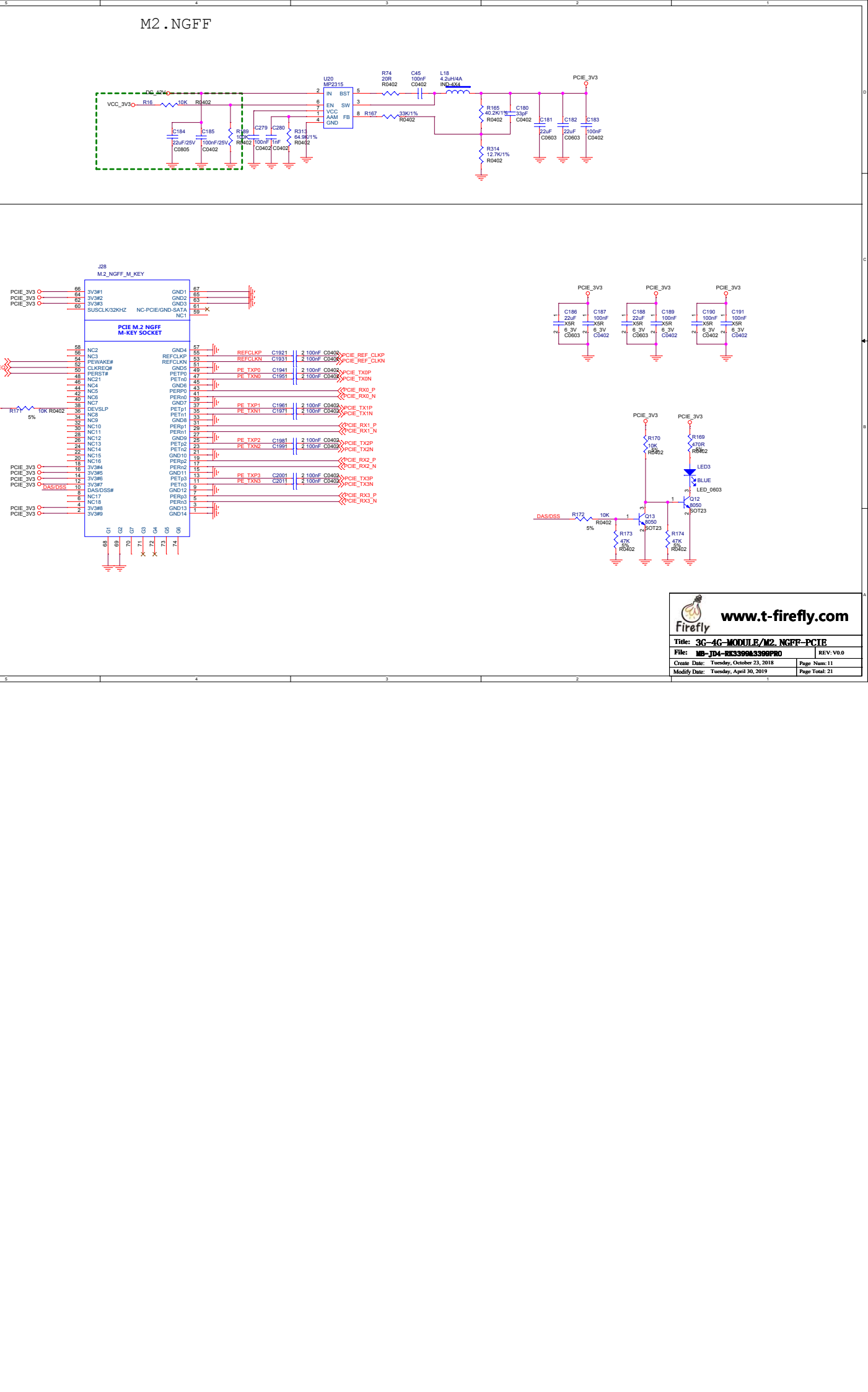
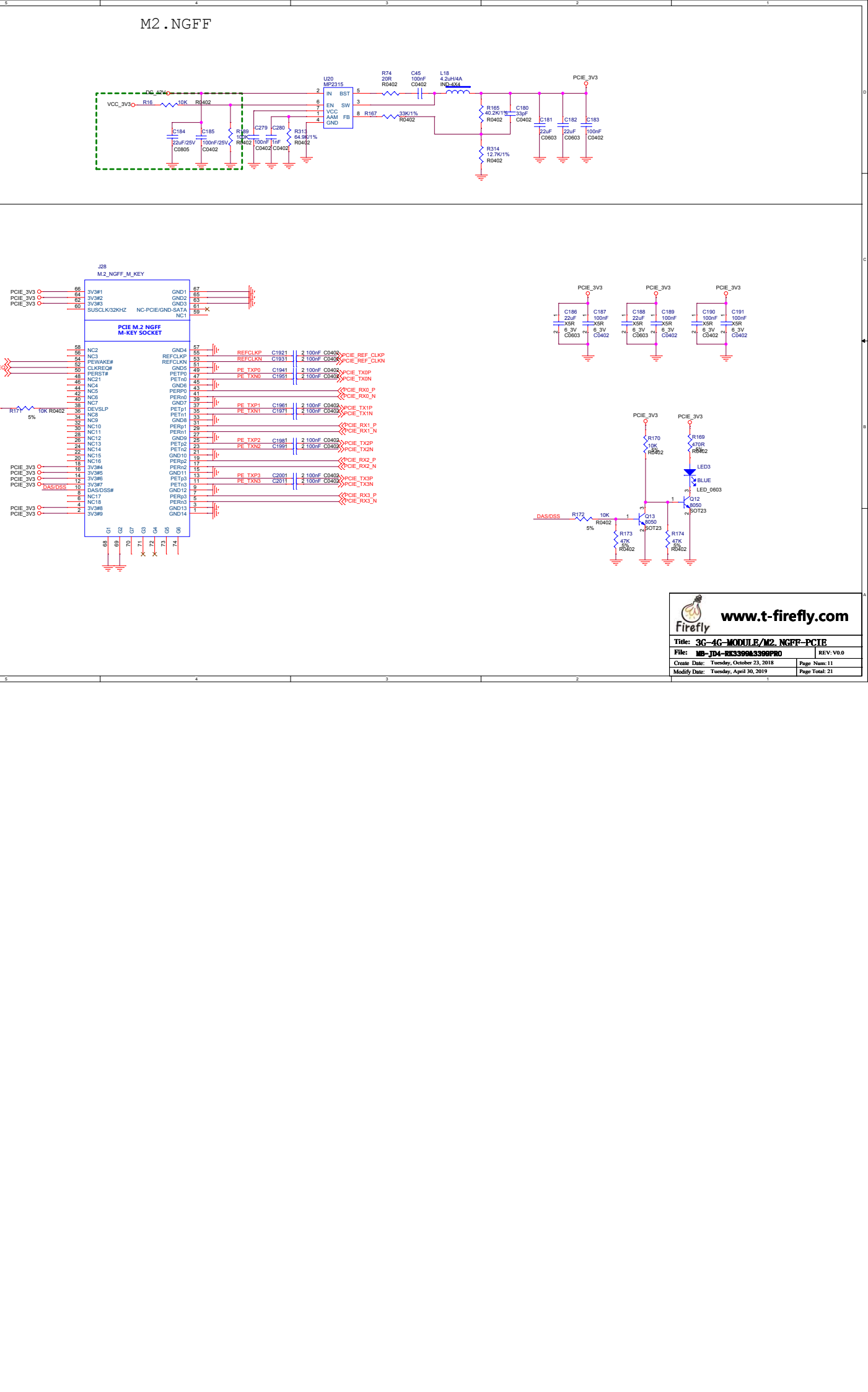


Encryption



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Title: IR/Encryption/FAN	
File: MB-JD4-RK3399&3399PRO	REV: V0.0
Create Date: Friday, October 19, 2018	Page Num: 10
Modify Date: Tuesday, April 30, 2019	Page Total: 21



WIFI/BT MODULE

NOTE: Default is AP6354/AP6356S/AP6359S, If using AP62X2, module pin12 is connected.

NOTE: L5, rated current 1A expectation specification.

NOTE: Default is AP6354/AP6356S/AP6359S, If using AP62X2, block A components is needed.

NOTE: The output current limit must be larger than 700mA for internal PA, if using AP62X2.

Block A

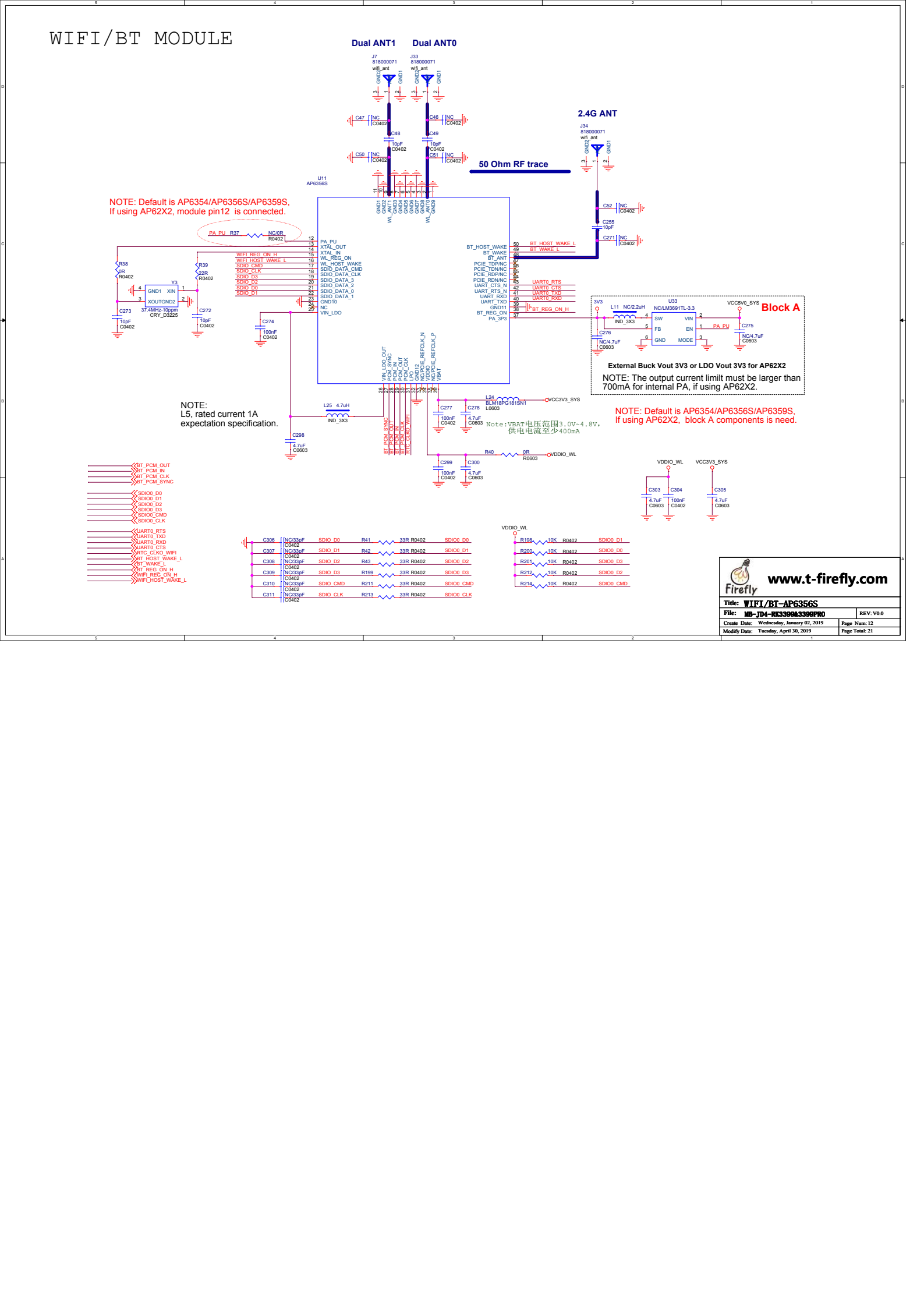
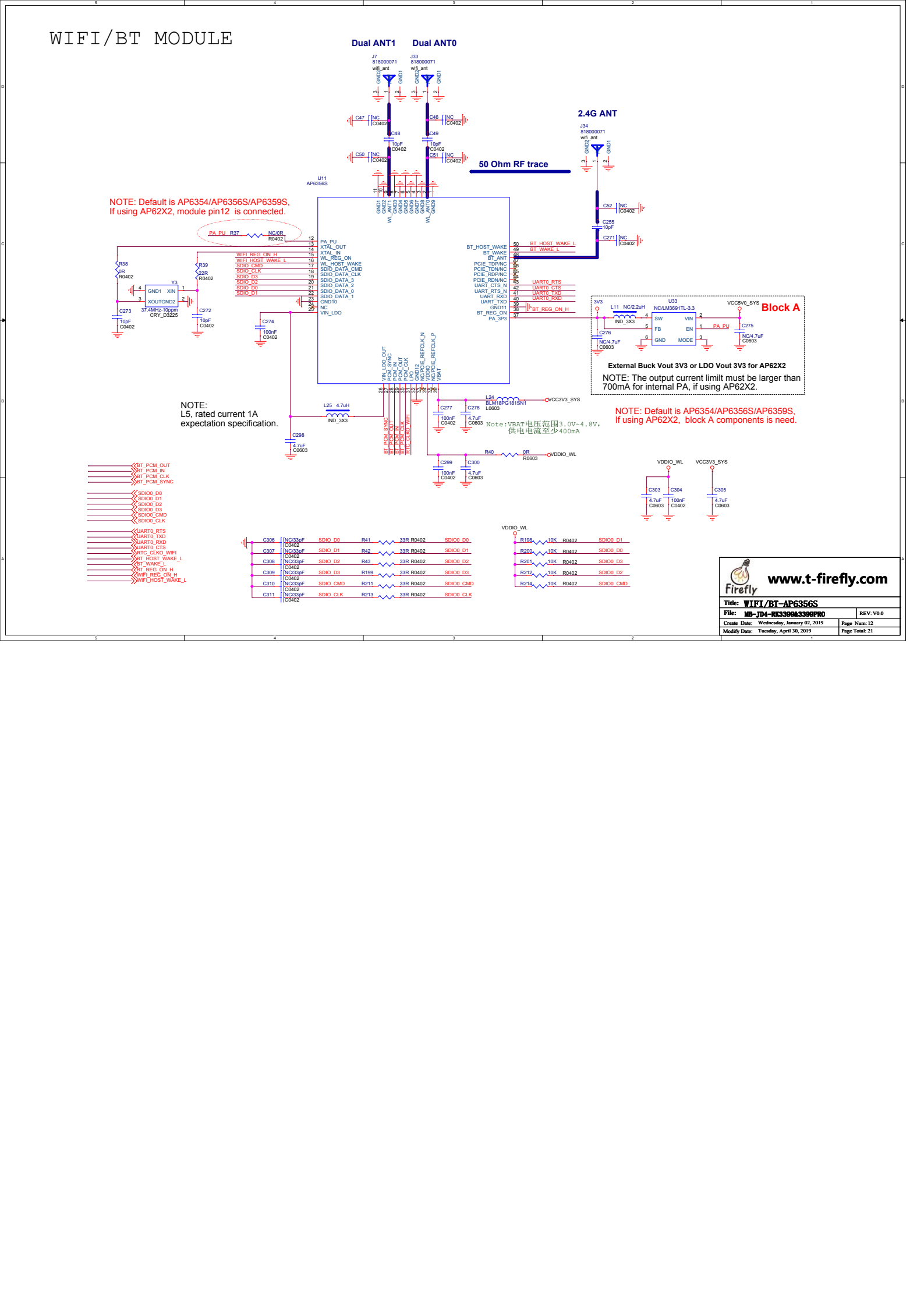
External Buck Vout 3V3 or LDO Vout 3V3 for AP62X2

NOTE: The output current limit must be larger than 700mA for internal PA, if using AP62X2.

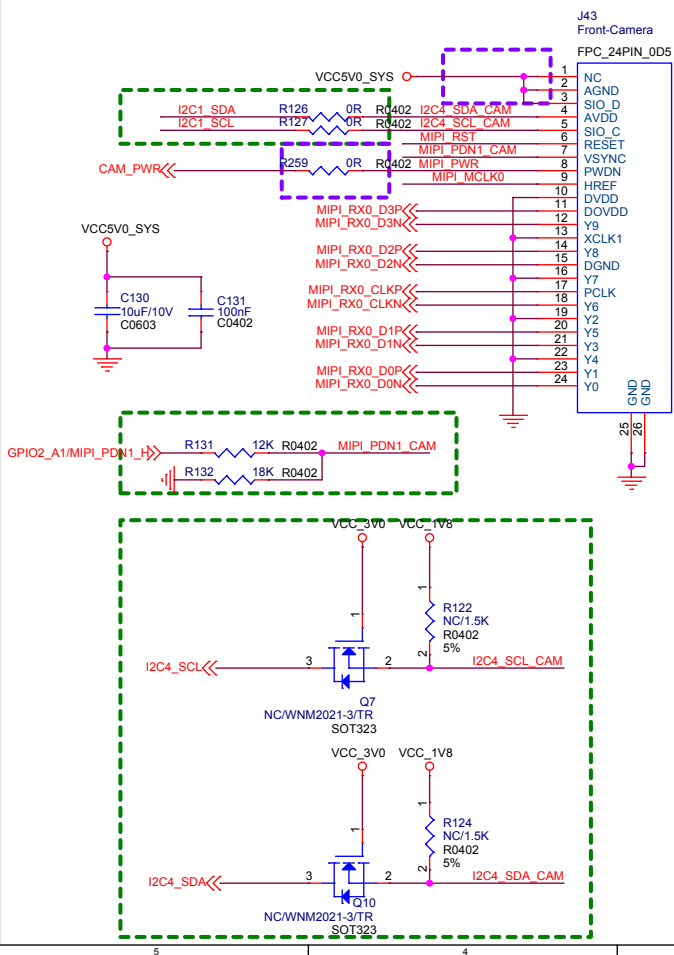
NOTE: VBAT电压范围3.0V~4.8V, 供电电流至少400mA.

Component List:

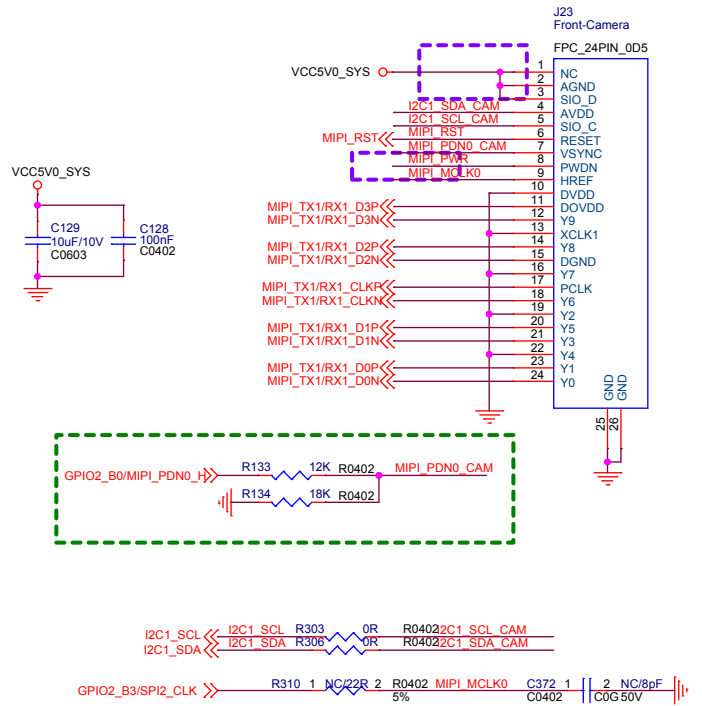
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- C278 4.7uF C0603
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- C300 4.7uF C0603
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MIPI CAMERA 0



MIPI CAMERA 1



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Title: MIPI Interface

File: MB-JD4-RK3399&3399PRO

REV: V0.0

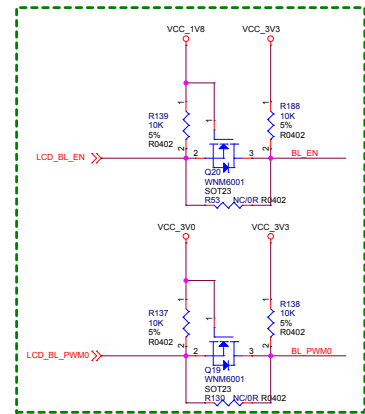
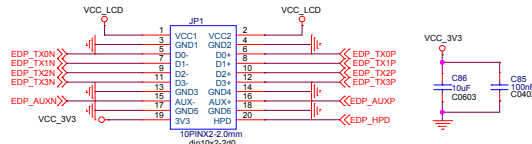
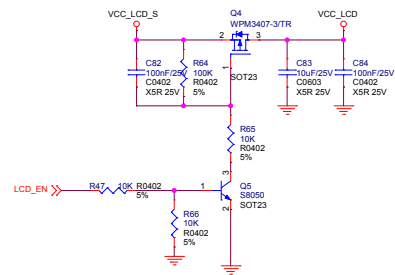
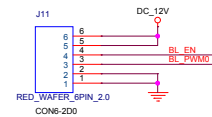
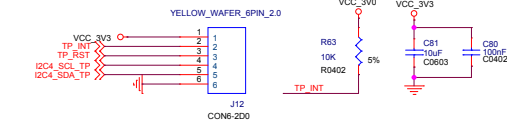
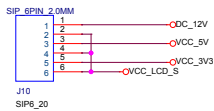
Create Date: Thursday, January 03, 2019

Page Num: 14

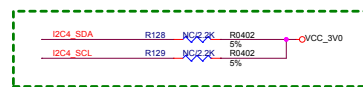
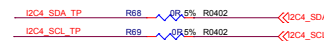
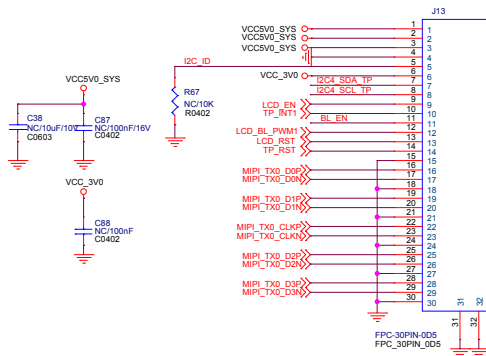
Modify Date: Tuesday, April 30, 2019

Page Total: 21

EDP Display Interface

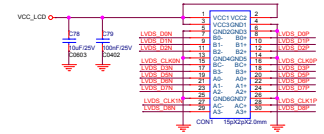


MIPI_PANEL

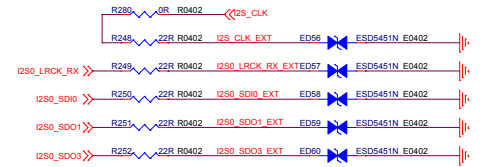
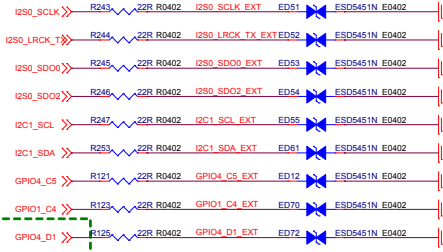
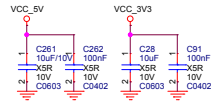
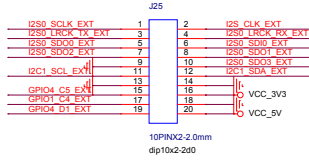


Firefly www.t-firefly.com

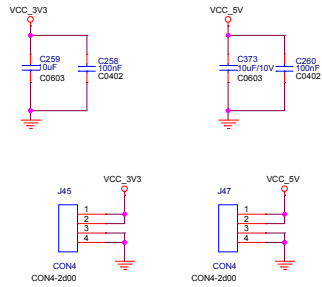
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File: MB-JD4-RK3399A3399PRO	REV: V0.0
Create Date: Friday, October 19, 2018	Page Num: 15
Modify Date: Tuesday, April 30, 2019	Page Total: 21



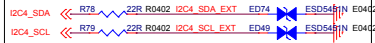
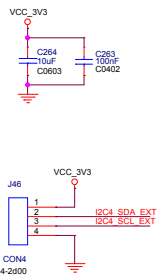
I2S/GPIO



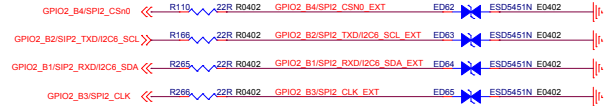
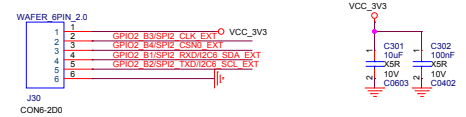
POWER OUT



I2C4



SPI/GPIO



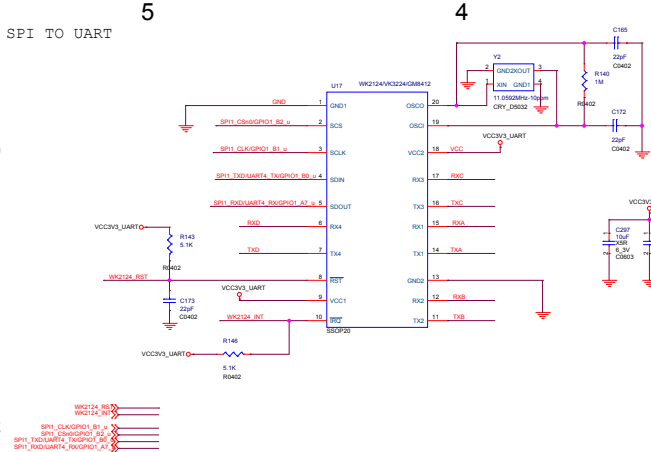
Firefly www.t-firefly.com

Title: **Extension Interface**

File: **MB-JD4-RK3399A3399PBO** REV: V0.0

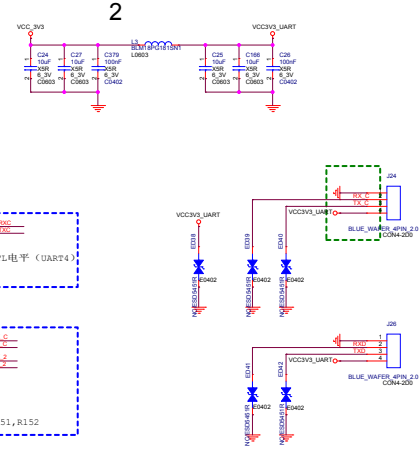
Create Date: Tuesday, October 23, 2018 Page Num: 17

Modify Date: Tuesday, April 30, 2019 Page Total: 21



貼R141,R142時J24直接由主芯片輸出TTL電平

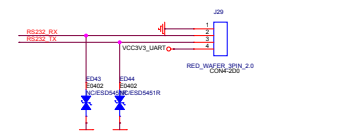
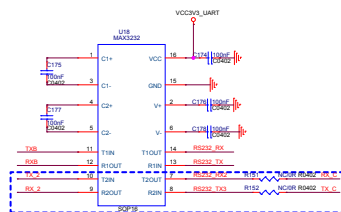
默认J24输出TTL串口电平
J24输出TTL电平，贴R144,R145
输出323电平，贴R147,R148,R151,R152



UART TO RS232

贴片R149,R150.时主芯片输出TTL电平(UAR)到U34转成485电平

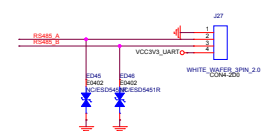
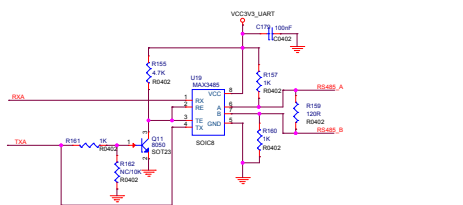
J29输出232电平与TTL电平切换电阻
默认J29输出232电平：R153,R154不贴

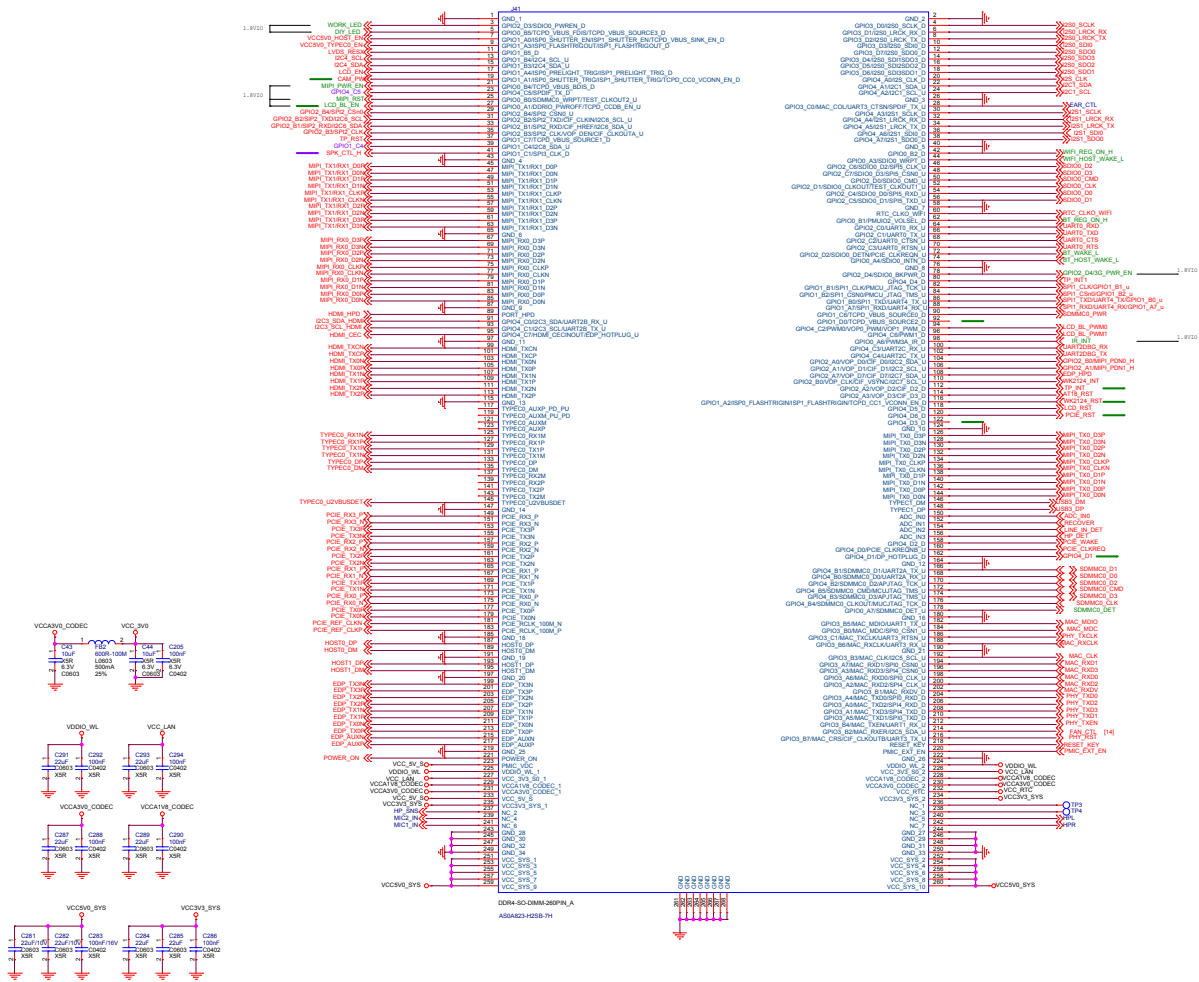


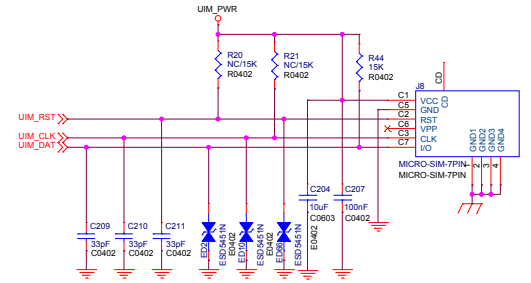
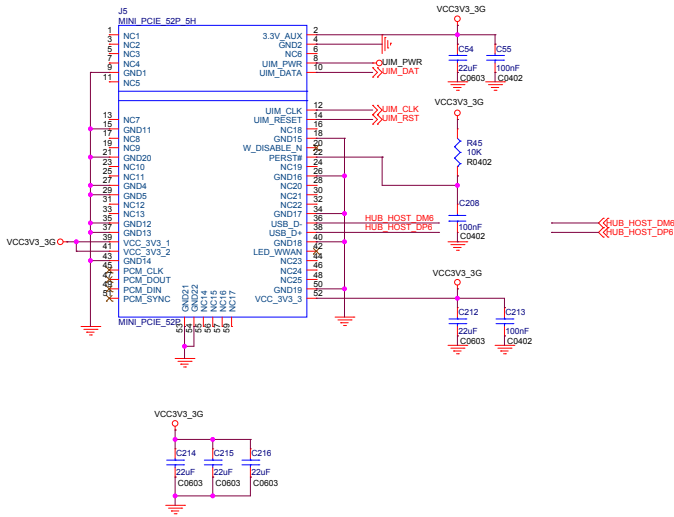
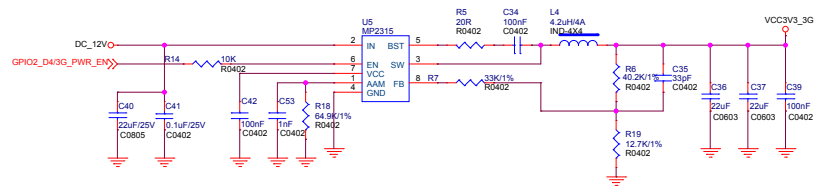
UART TO RS485

貼R156, R158時主芯片輸出TTL電平 (UART4
到U34變成485電平

J27输出485电平与TTL电平切换电阻
默认J27输出485电平: R163,R164不贴







[illegible]

Title: Amplifier/Watch dog	
File: MB-JD4-RK3399E3399PRO	REV: <Revcode>
Create Date: Thursday, April 18, 2019	Page Num: 21
Modify Date: Tuesday, April 30, 2019	Page Total: 21