

created : Wed Apr 9 17:17:49 CST 2025

date : .

這裡筆者要說明需要先列出清單

item	page	check
get device information	FPGA Information	V
get output count	FPGA output	V
all pins	FPGA output	V
Get Data	FPGA output	V
Set Data	FPGA output	V
get input count	FPGA input	V
All pins	FPGA input	V
get counter	IrqPIEnable	V
set all pin to 0/1	IrqPIEnable	V
check target (1>0or0>1)	IrqPISetting	V
auto timer	timer	V
timer1/2/3/4	timer	V
random	FPGA Random	V
general Test	Memory	V
BIOS Data test	Memory	V
Message	other	V