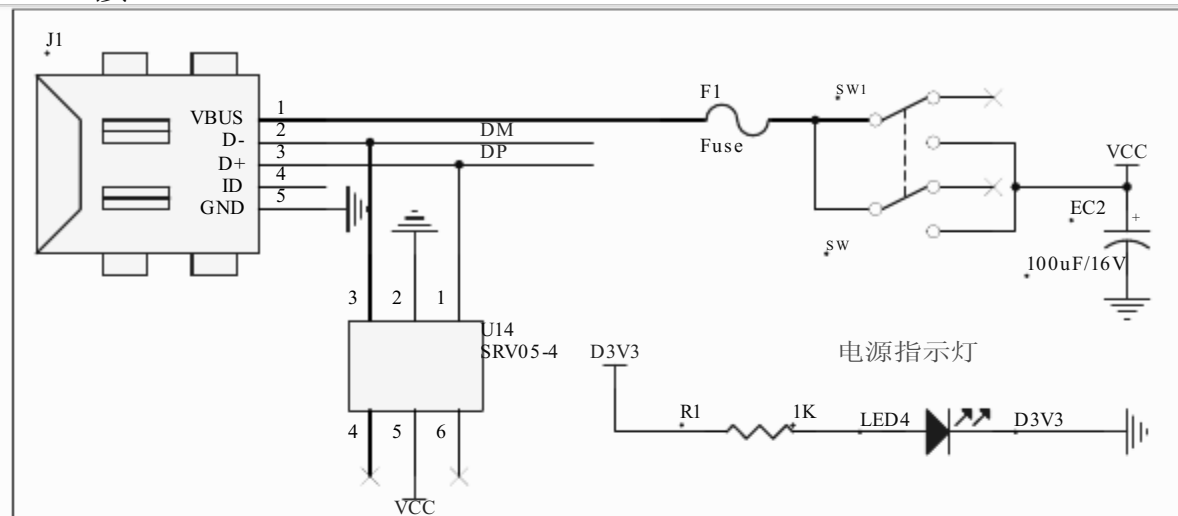
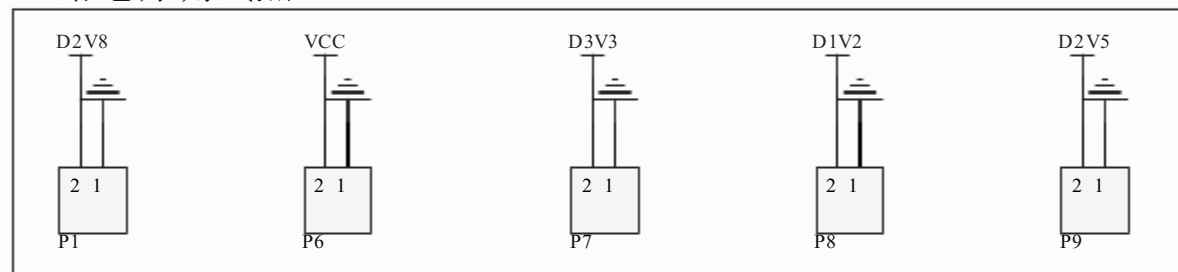


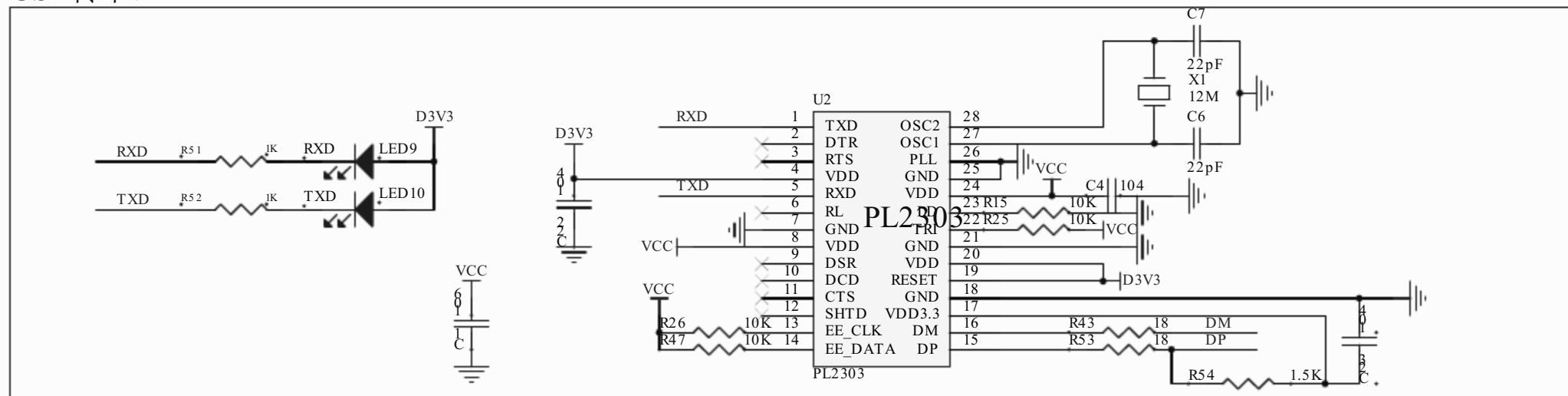
USB接口



五路电源测试点

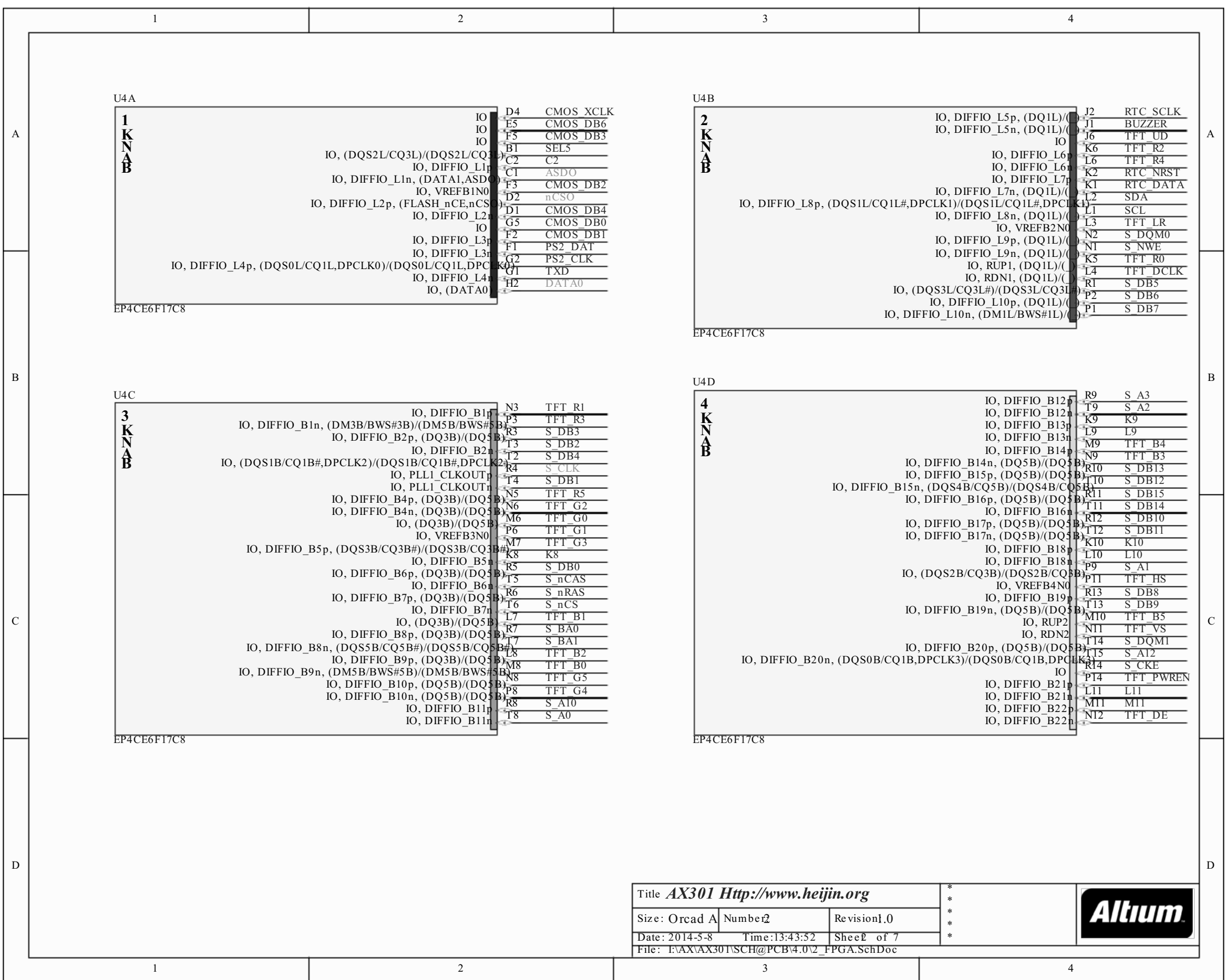


USB转串口



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Size: Orcad A	Number1	Revision1.0
Date: 2014-5-8	Time:13:43:40	Sheet of 7
File: F:\AX301\SCH@PCB4.0\ POWER.SchDoc		





U4E

**5
K
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	IO, N13	TFT_MODE
	IO, M12	M12
	IO, L12	L12
	IO, K12	K12
	IO, N14	N14
	IO, P15	S_A9
	IO, RDN3, (DQ1R)/(DQ1R#)	P16 S_A8
IO, DIFFIO_R11n, (DQS3R/CQ3R#)/(DQS3R/CQ3R#)	R16	S_A11
IO, DIFFIO_R11p, (DQ1R)/(DQ1R#)	K11	K11
	IO, N16	S_A6
IO, DIFFIO_R10n, (DQ1R)/(DQ1R#)	N15	S_A7
IO, DIFFIO_R10p, (DQ1R)/(DQ1R#)	L14	L14
IO, VREFB5N0	L13	L13
IO, (DQ1R)/(DQ1R#)	L16	S_A4
IO, DIFFIO_R9n, (DQ1R)/(DQ1R#)	L15	S_A5
IO, DIFFIO_R9p, (DQ1R)/(DQ1R#)	J11	J11
	IO, K16	SD_DIN
IO, DIFFIO_R8n, (DQ1R)/(DQ1R#)	K15	SD_NCS
IO, DIFFIO_R8p, (DQS1R/CQ1R#,DPCLK4)/(DQS1R/CQ1R#,DPCLK4)	J16	SD_DOUT
IO, DIFFIO_R7n, (DEV_OB#)/(DEV_OB#)	J15	SD_CLK
IO, DIFFIO_R7p, (DEV_CLRN#)/(DEV_CLRN#)	J14	J14
IO, DIFFIO_R6n, (DQ1R)/(DQ1R#)	J12	J12
IO, DIFFIO_R6p, (DQ1R)/(DQ1R#)	J13	J13
IO, (DQ1R)/(DQ1R#)		

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U4F

**6
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	IO, DIFFIO_R4n, (INIT_DONE#)/(INIT_DONE#)	G16	G16
	IO, DIFFIO_R4p, (CRC_ERROR#)/(CRC_ERROR#)	G15	LED0
	IO, F13	F13	F13
	IO, F16	F16	LED1
	IO, DIFFIO_R3n, (nCEQ#)/(nCEQ#)	F15	LED2
	IO, DIFFIO_R3p, (CLKUSR#)/(CLKUSR#)	B16	VGAD1
IO, (DQS0R/CQ1R,DPCLK5)/(DQS0R/CQ1R,DPCLK5)	F14	F14	F14
	IO, VREFB6N0	D16	LED3
	IO, DIFFIO_R2n, (DQ1R)/(DQ1R#)	D15	VGA_VSYNC
	IO, DIFFIO_R2p, (DQ1R)/(DQ1R#)	G11	G11
	IO, C16	C16	VGA_HSYNC
IO, DIFFIO_R1n, (DQS2R/CQ3R)/(DQS2R/CQ3R)	C15	VGAD0	VGAD0
IO, DIFFIO_R1p, (DQ1R)/(DQ1R#)			

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U4G

**7
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	IO, DIFFIO_T21n, (DQ5T)/(DQ5T#)	C14	C14
	IO, DIFFIO_T21p, (DQ5T)/(DQ5T#)	D14	D14
	IO, DIFFIO_T20n, (DQ5T)/(DQ5T#)	D11	D11
	IO, DIFFIO_T20p, (DQ5T)/(DQ5T#)	D12	D12
IO, DIFFIO_T20p, (DQS0T/CQ1T,DPCLK6)/(DQS0T/CQ1T,DPCLK6)	A13	VGAD6	VGAD6
IO, DIFFIO_T19n, (DQ5T)/(DQ5T#)	B13	VGAD5	VGAD5
IO, DIFFIO_T19p, (DQ5T)/(DQ5T#)	A14	VGAD4	VGAD4
IO, PLL2_CLKOUT#	B14	VGAD3	VGAD3
IO, PLL2_CLKOUT#	E11	E11	E11
IO, RUP4	E10	E10	E10
IO, RDN4	A12	VGAD8	VGAD8
IO, DIFFIO_T18n, (DQ5T)/(DQ5T#)	B12	VGAD7	VGAD7
IO, DIFFIO_T18p, (DQ5T)/(DQ5T#)	A11	VGAD10	VGAD10
IO, DIFFIO_T17n, (DQ5T)/(DQ5T#)	B11	VGAD9	VGAD9
IO, DIFFIO_T17p, (DQ5T)/(DQ5T#)	C11	C11	C11
IO, VREFB7N0	F10	F10	F10
IO, DIFFIO_T16n, (DQ5T)/(DQ5T#)	E9	F9	F9
IO, DIFFIO_T16p, (DQS2T/CQ3T)/(DQS2T/CQ3T)	F11	F11	F11
IO, DIFFIO_T15n, (DQ5T)/(DQ5T#)	A15	VGAD2	VGAD2
IO, DIFFIO_T15p, (DQ5T)/(DQ5T#)	A10	VGAD12	VGAD12
IO, DIFFIO_T14n, (DQ5T)/(DQ5T#)	B10	VGAD11	VGAD11
IO, DIFFIO_T14p, (DQ5T)/(DQ5T#)	C9	C9	C9
IO, DIFFIO_T13n, (DQ5T)/(DQ5T#)	D9	D9	D9
IO, DIFFIO_T13p, (DM5T/BWS#5T)/(DM5T/BWS#5T)	E9	E9	E9
IO, (DQS4T/CQ5T)/(DQS4T/CQ5T)	A9	VGAD14	VGAD14
IO, DIFFIO_T12n, (DQ5T)/(DQ5T#)	B9	VGAD13	VGAD13
IO, DIFFIO_T12p, (DQ5T)/(DQ5T#)			

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U4H

**8
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	IO, DIFFIO_T11n, (DQ5T)/(DQ5T#)	A8	DIG1
	IO, DIFFIO_T11p, (DQ5T)/(DQ5T#)	B8	DIG6
	IO, (DQS5T/CQ5T#)/(DQS5T/CQ5T#)	C8	VGAD15
	IO, (DQ3T)/(DQ3T#)	D8	D8
IO, DIFFIO_T10n, (DATA2), (DQ3T)/(DQ3T#)	E8	E8	E8
IO, DIFFIO_T10p, (DATA3), (DQ3T)/(DQ3T#)	F8	F8	F8
IO, DIFFIO_T9n, (DQ3T)/(DQ3T#)	A7	DIG5	DIG5
IO, DIFFIO_T9p, (DATA4), (DQ3T)/(DQ3T#)	B7	DIG0	DIG0
IO, DIFFIO_T8n, (DQ3T)/(DQ3T#)	F6	CMOS_HREF	CMOS_HREF
IO, DIFFIO_T8p, (DQ3T)/(DQ3T#)	F7	F7	F7
IO, VREFB8N0	C6	CMOS_SCLK	CMOS_SCLK
IO, DIFFIO_T7n, (DQS3T/CQ3T#)/(DQS3T/CQ3T#)	A6	DIG2	DIG2
IO, DIFFIO_T7p, (DQ3T)/(DQ3T#)	B6	DIG4	DIG4
IO, (DATA5), (DQ3T)/(DQ3T#)	E7	E7	E7
IO, (DATA6), (DQ3T)/(DQ3T#)	E6	E6	E6
IO, DIFFIO_T6n, (DATA7), (DQ3T)/(DQ3T#)	A5	DIG7	DIG7
IO, DIFFIO_T5n, (DQ3T)/(DQ3T#)	A2	SEL4	SEL4
IO, DIFFIO_T5p, (DQ3T)/(DQ3T#)	B5	DIG3	DIG3
IO, DIFFIO_T4n, (DM3T/BWS#3T)/(DM5T/BWS#5T)	A4	SEL0	SEL0
	B4	SEL1	SEL1
IO, DIFFIO_T4p, (DQ3T)/(DQ3T#)	D5	CMOS_VSYNC	CMOS_VSYNC
IO, DIFFIO_T3n, (DQ3T)/(DQ3T#)	D6	CMOS_SDAT	CMOS_SDAT
IO, DIFFIO_T3p, (DQ3T)/(DQ3T#)	A3	SEL2	SEL2
IO, DIFFIO_T2n, (DQ3T)/(DQ3T#)	B3	SEL3	SEL3
IO, DIFFIO_T2p, (DQS1T/CQ1T#,DPCLK7)/(DQS1T/CQ1T#,DPCLK7)	C3	CMOS_DB7	CMOS_DB7
IO, DIFFIO_T1n, (DQ3T)/(DQ3T#)	D3	CMOS_DB5	CMOS_DB5
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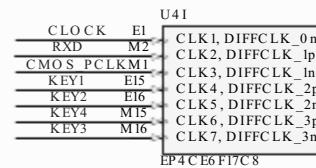
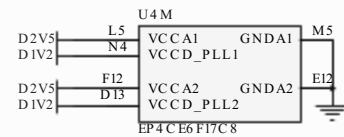
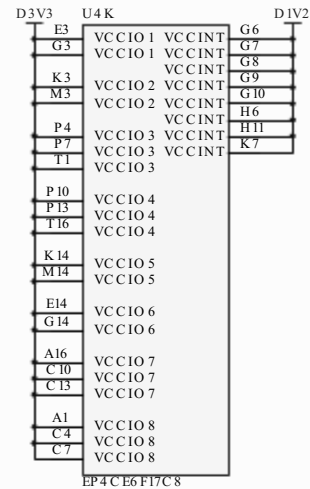
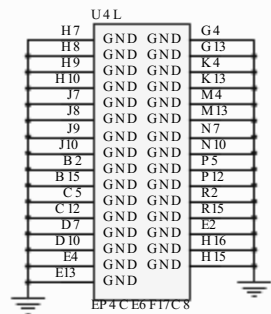
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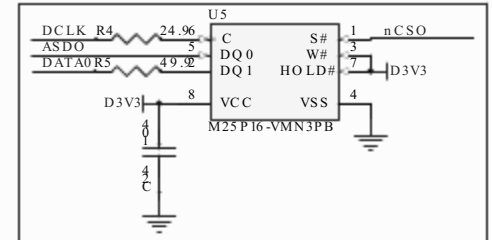
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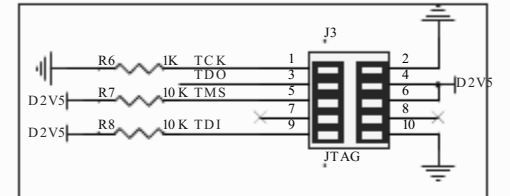




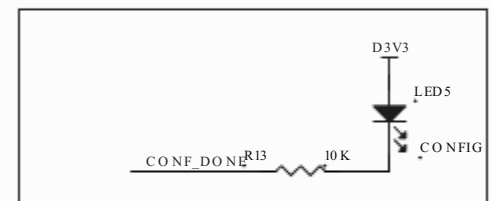
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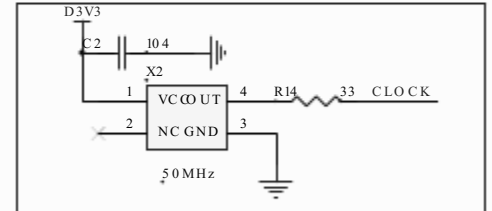
JTAG接口



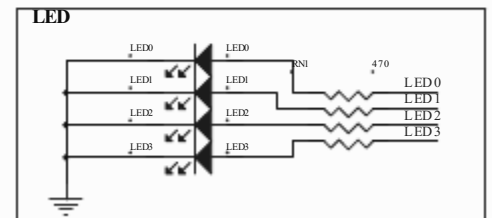
配置指示灯



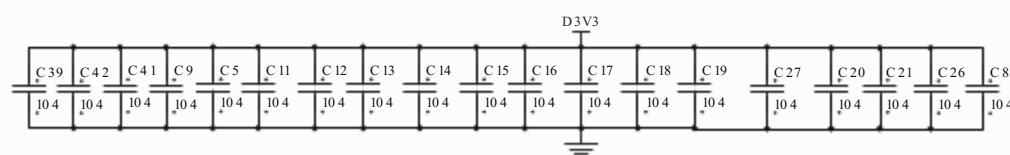
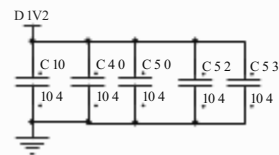
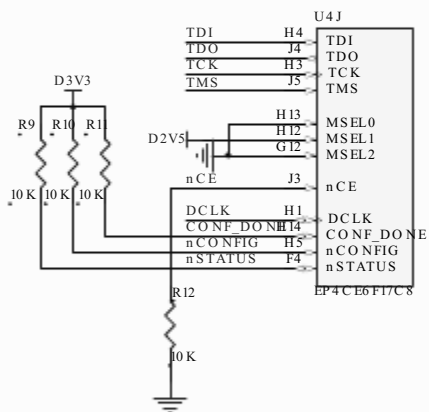
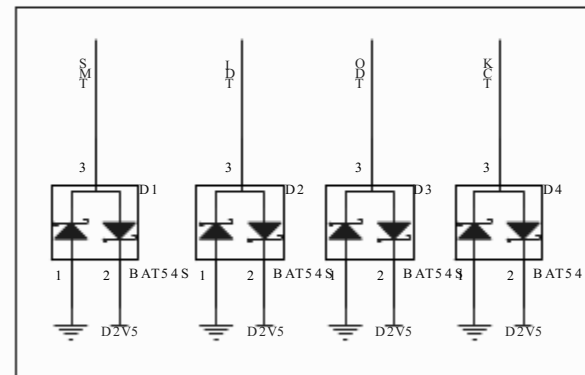
50M晶振



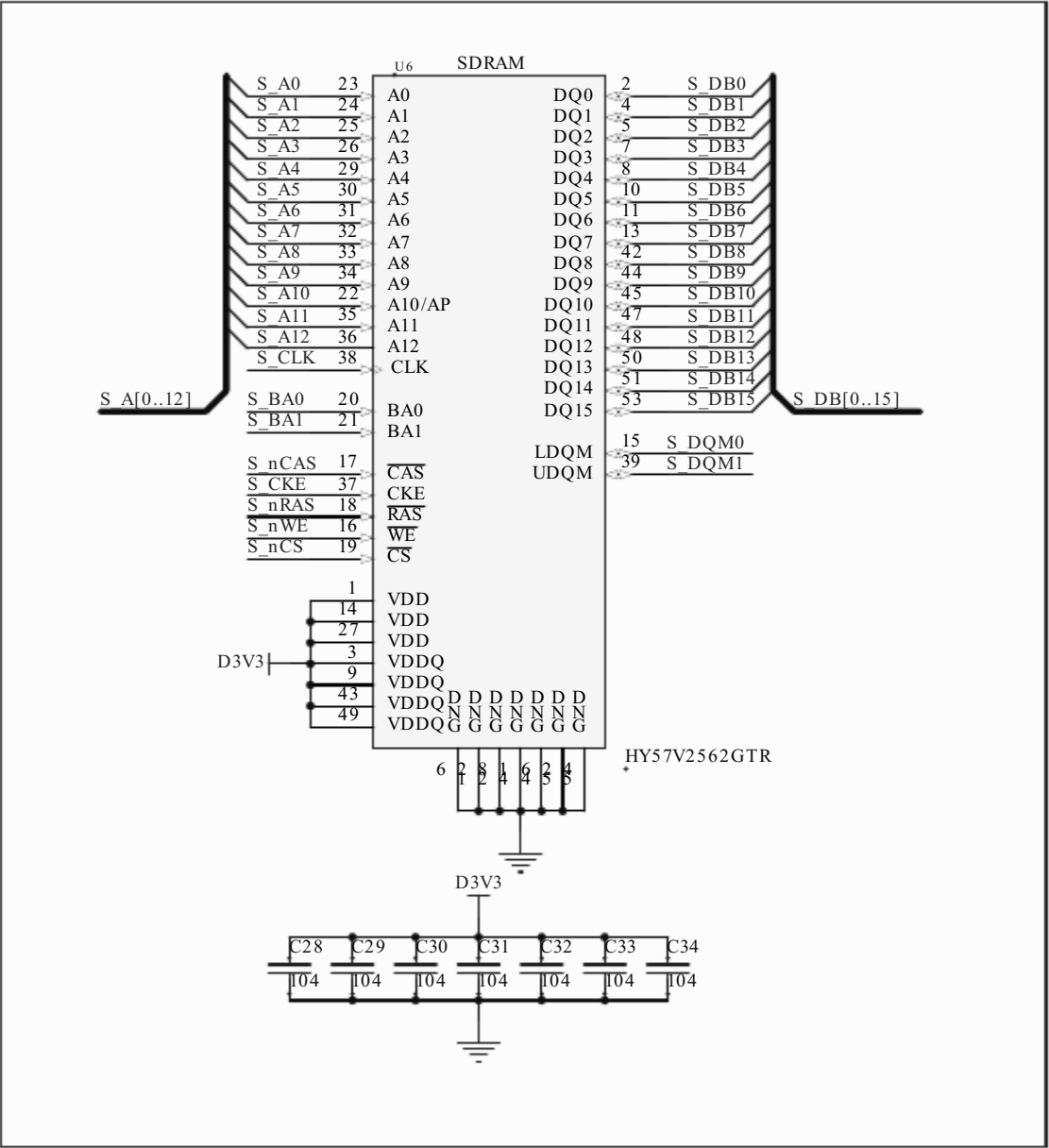
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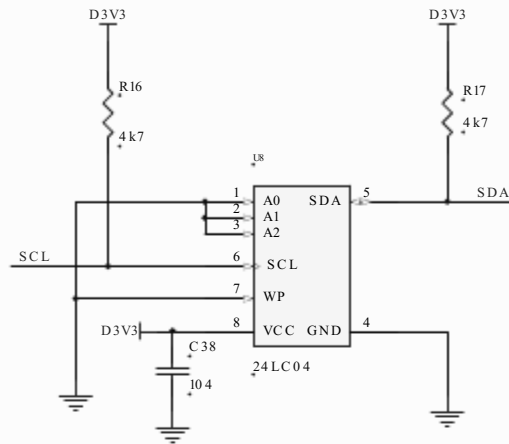
JTAG口保护电路



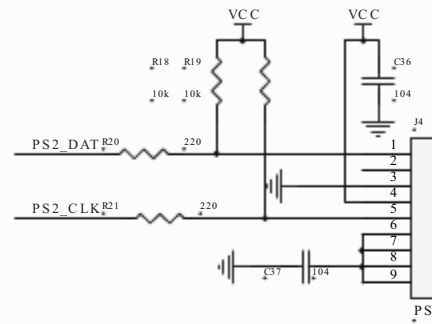
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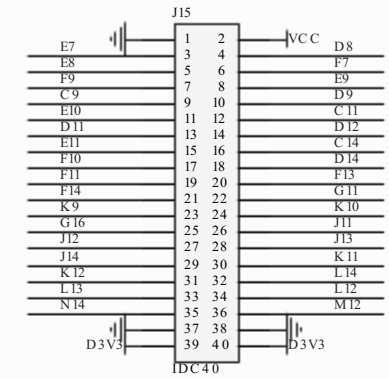
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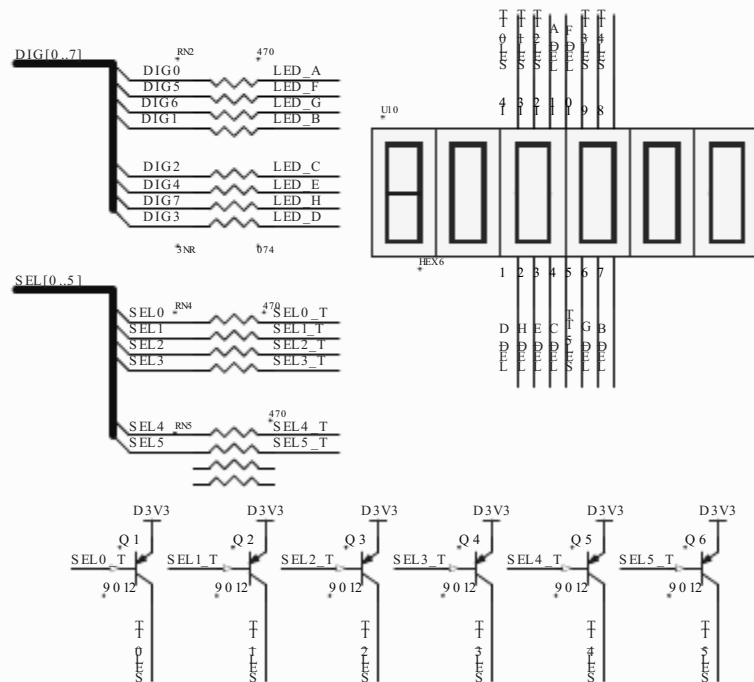
接口



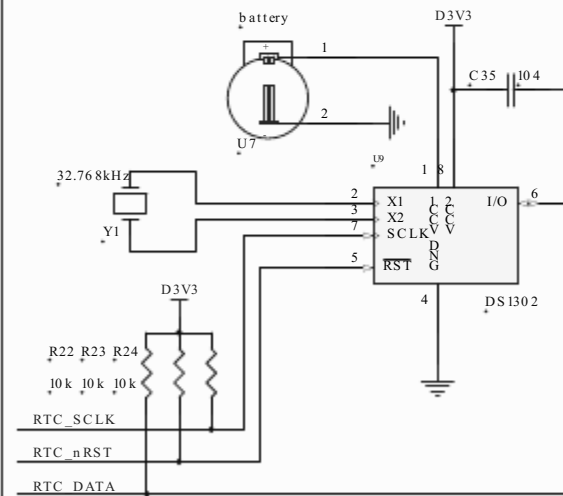
40针扩展口



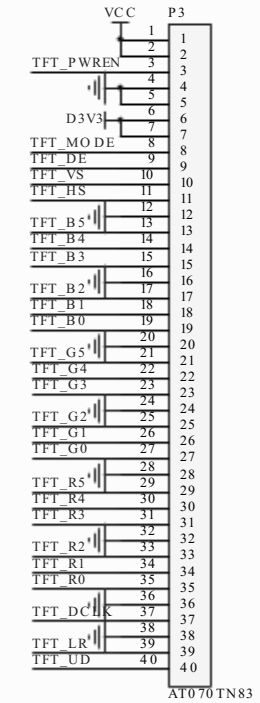
六位数码管



实时时钟



7寸TFT接口



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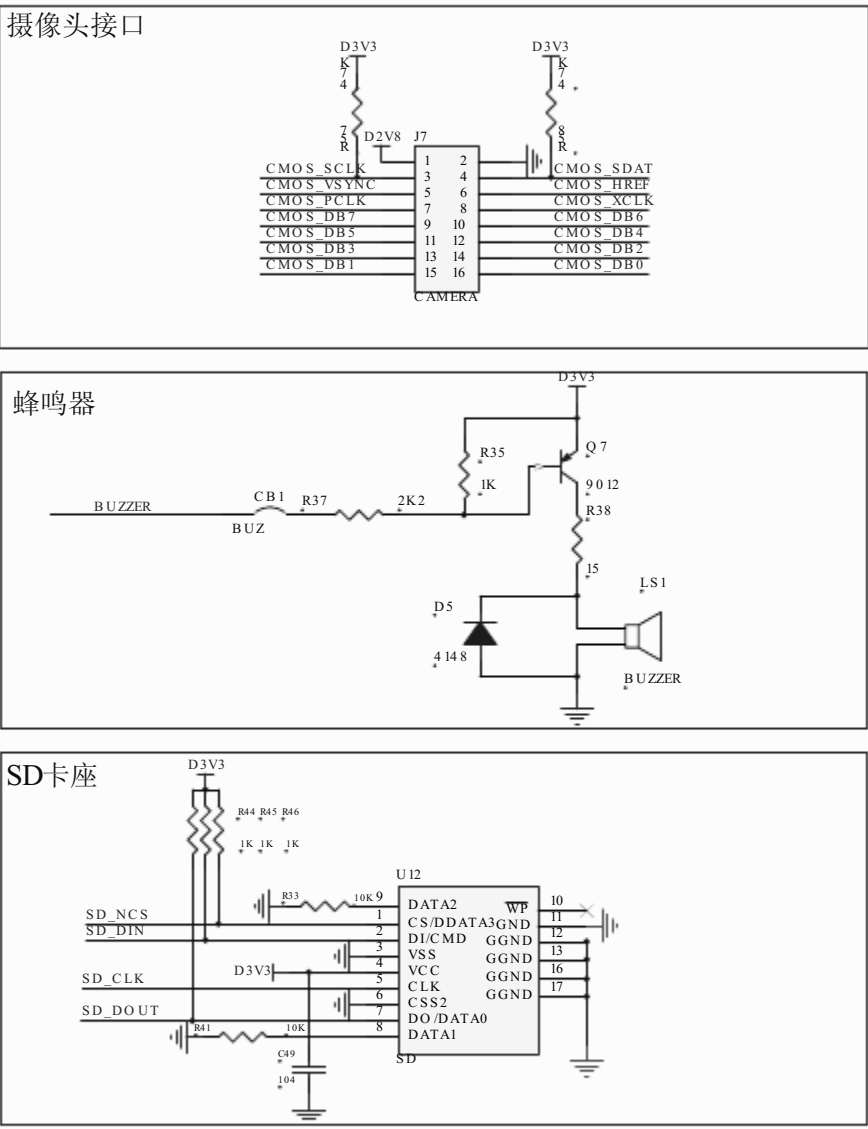
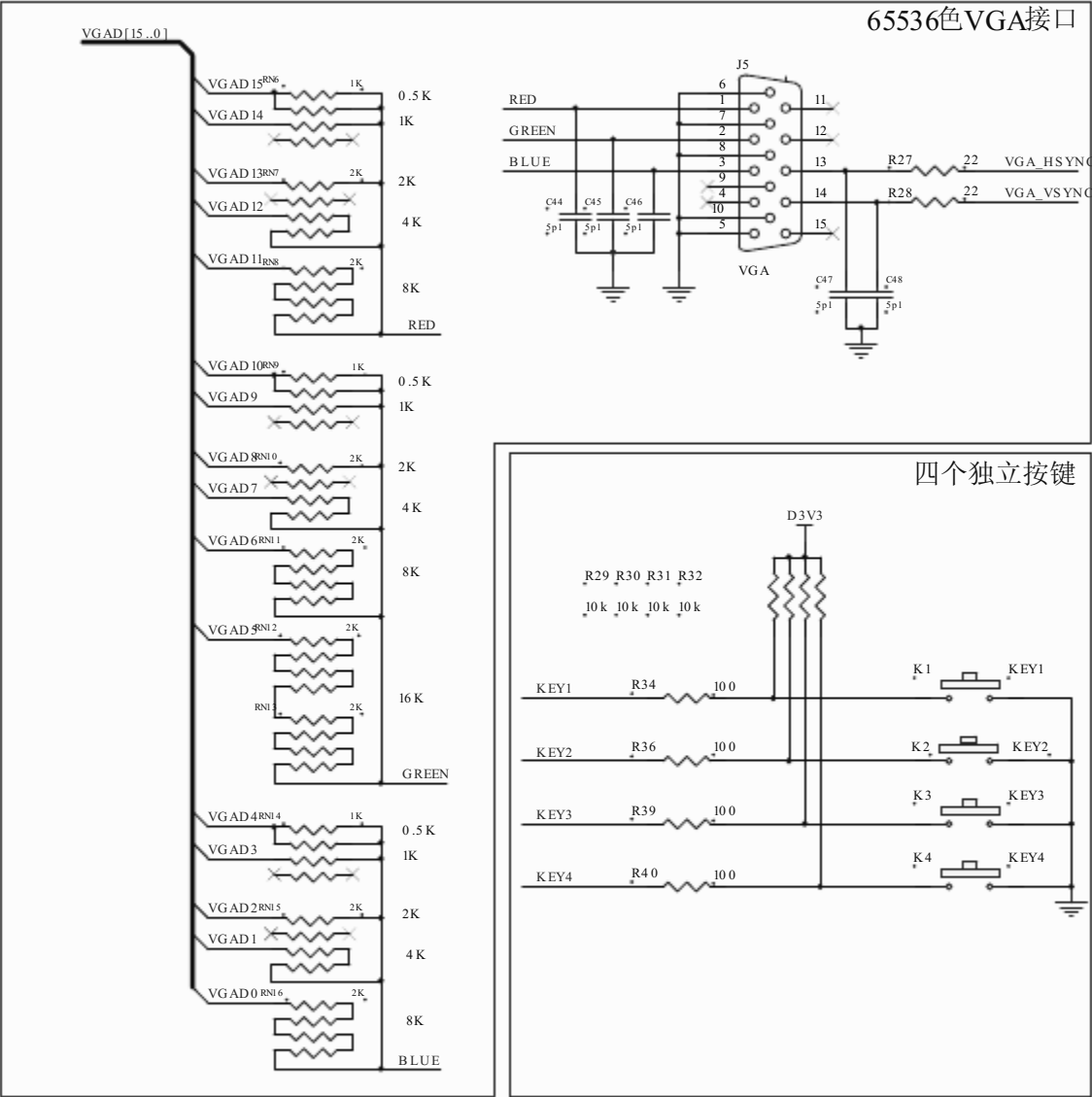
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