

4-bit Quantization of LSTM-based Speech Recognition Models

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Abstract

We investigate the impact of aggressive low-precision representations of weights and activations in two families of large LSTM-based architectures for Automatic Speech Recognition (ASR): hybrid Deep Bidirectional LSTM - Hidden Markov Models (DBLSTM-HMMs) and Recurrent Neural Network - Transducers (RNN-Ts). Using a 4-bit integer representation, a naïve quantization approach applied to the LSTM portion of these models results in significant Word Error Rate (WER) degradation. On the other hand, we show that minimal accuracy loss is achievable with an appropriate choice of quantizers and initializations. In particular, we customize quantization schemes depending on the local properties of the network, improving recognition performance while limiting computational time. We demonstrate our solution on the Switchboard (SWB) and CallHome (CH) test sets of the NIST Hub5-2000 evaluation. DBLSTM-HMMs trained with 300 or 2000 hours of SWB data achieves < 0.5% and < 1% average WER degradation, respectively. On the more challenging RNN-T models, our quantization strategy limits degradation in 4-bit inference to 1.3%.

Index Terms: LSTM, HMM, RNN-T, quantization, INT4

1. Introduction

Neural Network (NN) models for ASR have achieved tremendous success in the past decade, significantly reducing the gap with human performance [1, 2]. Long Short-Term Memory (LSTM) building blocks are at the core of ASR models, either in NN-HMM hybrid [1] or NN end-to-end form [2, 3], delivering state-of-the-art performance on standard speech benchmarks, such as the SWB corpus [4].

Many strategies have been proposed to alleviate the challenges posed by the large size and protracted training time of ASR models (these challenges get exacerbated during lowprecision quantization emulation) - limiting their application in resource constrained scenarios. These include knowledge distillation [5,6], low-rank matrix factorization [7], sparsification [8], and quantization [9]. These methods aim to reduce model size, computation, and time to convergence. Driven by advances in low-precision arithmetic and training algorithms, quantization is becoming a key methodology to speed up inference for ASR models [10, 11]. In a quantized model, parameters are stored and computations are performed with lower bit widths than the full-precision counterpart, which is frequently IEEE float32 (FP32). With Quantization Aware Training (QAT), the quantization error is introduced during the training phase via operations in low-precision arithmetic. This allows the network to progres-

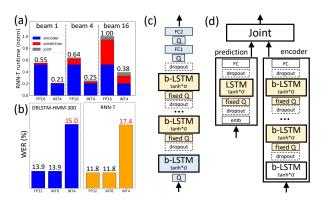


Figure 1: (a) Estimated RNN-T inference time. (b) Average WER for SWB/CH inference at different bit-widths. (c-d) Architecture of DBLSTM-HMM and RNN-T with learnable (blue) or fixed (yellow) quantization using BAC.

sively learns to incorporate the impact of reduced precision of its weights and activations and typically improves performance over Post-Training Quantization schemes [12].

Runtime estimates of 4-bit integer (INT4) quantized ASR models (Fig. 1(a)) show 2.6× faster streaming end-to-end speech recognition compared to 16-bit floating point (FP), a widespread format for model deployment nowadays (see Sec. 4.3 for more details). However, aggressive quantization of LSTM-based models can severely impact performance, especially in large models trained on extensive speech data sets. For example, Fig. 1(b) shows that quantization of weights and activations of a DBLSTM-HMM model with a simple quantizer results in no WER degradation for INT8, but in a >1% degradation for INT4. Moreover, it is observed that INT4 quantization of RNN-T models shows >5% degradation, while INT8 quantization shows the same accuracy as the FP32 model (Fig. 1(b)). Although multiple techniques have been proposed to mitigate the performance deterioration, they are limited to 8-bit quantization [10-13] and to small LSTM networks (1 or 2 layers) on simple train and test datasets (e.g., the Penn Tree Bank (PTB)) [9, 14-18], while viable 4-bit solutions for industrial scale models remain elusive.

Quantization of large LSTM-based speech models to 4 bits faces two primary challenges. (a) **Quantizer selection**: in INT4 quantization, simple quantizers fail to deliver performance comparable to full-precision models. Alternative strategies that learn quantization parameters during QAT are less effective than in non-ASR tasks due to the inherent constraints set by the LSTM cell. (b) **QAT training time**: conversion of FP32 values to lower precisions during QAT training can be costly and slow down training significantly, making the optimization of large networks on large datasets a particularly daunting task.

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In this work, we address these challenges and propose a practical methodology to quantize both weights and activations in state-of-the-art speech models using an INT4 representation. Our contributions are as follow:

- We introduce a novel quantizer (Bound Aware Clipping, BAC) that achieves minimal degradation for 4-bit quantization while speeding up QAT in LSTM-based models.
- We adopt QAT fine-tuning techniques with custom learning rate policies, addressing the training time bottleneck and shortening training duration by over 50%.
- Experimentally, we demonstrate record accuracy in largescale 4-bit LSTM based models (DBLSTM-HMM and RNN-T) trained on large speech data sets (SWB 300 h and 2000 h).

2. Speech models

We investigate the impact of INT4 quantization on ASR performance of two families of LSTM-based architectures: Hybrid DBLSTM-HMMs and RNN-T. For all networks, large training data sets (SWB) are used and testing is done on the SWB and CH test sets (NIST Hub5-2000 evaluation).

DBLSTM-HMM-300 (Fig. 1(c)): the network comprises 4 bi-directional LSTM layers (input size 140, hidden size 512 per direction, sequence length 21) followed by 2 fully connected (FC) layers in bottleneck configuration with 32k outputs corresponding to context-dependent hidden Markov model states [19]. Decoding is performed with a 4M 4-gram language model. The training set consists of 262 h of SWB 1 audio with transcripts. The optimizer is SGD, with initial LR of 0.1, annealed by $\frac{1}{\sqrt{2}}$ from epoch 10. We train for 20 epochs on a V100 GPU with batch size 128.

DBLSTM-HMM-2000 (Fig. 1(c)): the architecture is equivalent to DBLSTM-HMM-300 except for the use of 6 LSTM layers and input size 260. We train with 1975 h of audio: 262 h from SWB, 1698 h from the Fisher corpus, and 15 h of CH audio. We use a total batch size of 4096 (16 V100 GPUs) and the scaled LR schedule with a warm-up period in the first 10 epochs [20].

RNN-T (Fig. 1(d)): the acoustic encoder consists of 6 bidirectional LSTM layers (input size 240 + 100-dim i-vectors, hidden size 640 per direction, variable sequence length). The prediction network is a single unidirectional LSTM layer with 768 cells. Encoder and prediction network outputs are combined multiplicatively in a joint network [3], with an FC layer and log-Softmax over 46 output characters. We train for 20 epochs with batch size 64, using AdamW and a triangular LR policy (OneCycleLR), on the audio and character-level transcripts from the SWB corpus, augmented with speed and tempo perturbation [21], SpecAugment [22], and Sequence Noise Injection [23].

3. Quantization schemes

3.1. Quantizers

Linear Quantization (LQ) is a widely used technique to approximate full-precision values. This is achieved by mapping them onto an interval discretized into 2^{bitwidth} levels. Various approaches exist to determine the boundaries of the interval (or, equivalently, the scaling factor applied to the full-precision values). LQ allows for both symmetric and asymmetric representations, with respect to zero.

Symmetric LQ takes the form:

$$y_q = clamp \left(round(y \cdot \frac{2^k - 1}{\alpha}) \cdot \frac{\alpha}{2^k - 1}, -\alpha, \alpha \right)$$
 (1)

with y and y_q being the full-precision and quantized tensor, respectively, k the bit-width, and α the quantization boundary. The clamp function limits the range of y to $[-\alpha,\alpha]$. In asymmetric LQ, two independent parameters α_+ and α_- define the upper and lower boundary, respectively, and y is scaled using $\frac{2^k-1}{\alpha_+-\alpha_-}$.

The most common quantizer is symmetric and simply defines $\alpha = \max |y|$. Herein, we refer to this quantizer as MAX. MAX can be applied to both weight and activation quantization.

Alternatively, it was shown that knowledge of the weight distribution statistics allows for an improved representation in the low-precision space [24]. In this context, Statistics-Aware Weight Binning (SAWB), a symmetric LQ approach, aims to identify the clamp boundary α as an approximation of the optimal α^* that minimizes the mean square error introduced by the quantization process. During training, α can be calculated as the linear combination of the expected values E(|y|) and $E(y^2)$, using empirical, bit-width-dependent coefficients.

With Parameterized Clipping Activations (PACT) [24], the boundary α for activation quantization is learned. The asymmetric version of PACT uses two independently-learned boundaries, α_+ and α_- . The gradients $\frac{\partial L}{\partial \alpha_\pm} = \frac{\partial L}{\partial y_q} \frac{\partial y_q}{\partial \tilde{y}} \frac{\partial \tilde{y}}{\partial \alpha_\pm}$, with \tilde{y} being the clamped value, are derived via the Straight-Through Estimator (STE) [25], which estimates $\frac{\partial y_q}{\partial \tilde{y}}$ as 1.

In order to address the peculiar features of LSTM-based networks, we introduce a new quantizer, Bound Aware Clipping (BAC), which selectively applies different quantization strategies to different layers. Due to the large dynamic range of inputs fed into the *first layer* of the network, it is beneficial to either learn the most appropriate α_{\pm} boundaries of the quantizer during training (as in PACT) or, alternatively, to process it in high precision. On the other hand, due to the inherently restricted nature of LSTM activations, fixed boundaries are a natural choice for layers beyond the first. The use of unchanging boundaries saves computational time, providing very relevant speed-ups in some networks. As the presence of dropout scales the inputs in-training outside the initial [-1,1] range defined by the $tanh \cdot \sigma$ operation within the LSTM cell, BAC sets symmetric boundaries for the inputs using $\alpha = \frac{1}{1 - dropout}$ (corresponding to the dropout scaling factor), and symmetric boundaries for the hidden states with $\alpha = 1$, as these are not modified by dropout.

An additional effect of using dropout to regularize LSTM networks is the large number of zeros it generates. Their poor representation due to misalignment of the discrete quantization levels can result in a very significant quantization error. For INT4 quantization, an effective mitigation strategy is to use $2^{bits}-1$ levels, such that, for symmetric quantizers, the middle interval always represents zeros exactly. We apply this strategy in quantizing activations of the models introduced in Sec. 2.

3.2. Model quantization

In the DBLSTM-HMM models, we quantize weights (without biases) and activations of all LSTM layers (both directions) as well as the two FC layers. With reference to Fig. 1(c), we use our selective quantizer BAC, such that the first LSTM layer uses learnable parameters, as in PACT, while fixed boundaries are used in the subsequent LSTM layers. Full weight quantization results, for both models, in 99.8% of the network parameters being quantized. Inputs and hidden states are quantized at every call of the LSTM cell, while cell states are preserved in full-precision. The rationale is that they only participate in elementwise multiplications, resulting in a very limited contribution (\sim 0.1%) to the total FLOPs. The HMM decoder is not quantized.

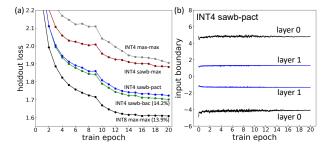


Figure 2: (a) INT quantization of weights and activations on DBLSTM-HMM-300 with different quantizers (avg WER in brackets). (b) Input boundaries α of the first 2 LSTM layers

In the RNN-T (Fig. 1(d)), we quantize weights (without biases) and activations for the LSTM layers (both directions) in the acoustic encoder. The first layer, accounting for 8.8% of the total parameters (4.8% of total inference computation with beam size 16), is either quantized or kept in full precision (see Sec. 4.2). BAC with fixed clip values is applied in the subsequent LSTM layers. The prediction network is also quantized: while carrying only 4.1% of the total parameters, it is repeatedly executed at inference time as part of a beam search algorithm. Consequently, a quantized prediction network speeds up inference time by $2.6\times$, compared to a FP16 RNN-T (see Sec. 4.3). FC layers and the joint network account for <1% of total parameters and are kept in full-precision. With this configuration, 90.2% of the parameters are INT4.

3.3. QAT fine-tuning

INT4 QAT of large speech LSTM models requires extensive computational resources, which hinders its application in many practical scenarios. With the aim of shortening the QAT time, we initialize our networks with state-of-the-art FP32 pre-trained models. We then apply a decreasing LR policy, which gently adapts the pre-trained weights to compensate for the noise introduced by the quantization process. Specifically, in DBLSTM-HMMs we modify the LR schedule into a linear decay (by a 0.5 factor), replacing the constant LR or its warm-up. For RNN-T, we customize the LR schedule to start from a small initial value $(4 \cdot 10^{-4})$, decrease it to 10^{-5} over 8 epochs, and stabilize it afterwards (Fig. 3(d)). We observed that our QAT fine-tuning strategy significantly reduces INT4 QAT training time, thus enabling optimization of large LSTM-based speech models (see details in the next section).

4. Experimental results

4.1. DBLSTM-HMM

Figure 2(a) presents the evolution of holdout losses of the DBLSTM-HMM-300 model during a 20-epoch training for different QAT quantizers. INT8 quantization of weights and activations with the simplest MAX quantizer closely matches the FP32 results in both losses and WER, while the INT4 MAX quantizer shows degraded performance in terms of losses and WER, for a SWB / CH WER gap with the FP32 baseline of 1.1% / 1.1%, as shown in Table 1. Replacing the MAX quantizer for the LSTM and FC weights with SAWB produces some improvements in both losses and WER. Although the gain in losses is limited (<2% in relative terms), the improvement in WER is more pronounced, shrinking the SWB / CH accuracy gap to 0.8% / 0.6%. Further improvements are obtained by quantizing

Table 1: 4-bit DBLSTM-HMM quantization. ^a10-epoch finetuning from pre-trained FP32 model

model	quantizer		bits	WER		
	W	A	W&A	SWB	СН	avg
HMM-300	-	-	32	9.9	17.9	13.9
HMM-300	max	max	8	10.0	17.7	13.9
HMM-300	max	max	4	11.0	19.0	15.0
HMM-300	sawb	max	4	10.7	18.5	14.6
HMM-300	sawb	pact	4	10.5	18.2	14.4
HMM-300	sawb	bac	4	10.2	18.2	14.2
$\mathbf{HMM} ext{-}300^a$	sawb	bac	4	10.4	18.3	14.4
HMM-2000	-	-	32	7.6	13.0	10.3
HMM-2000	max	max	8	7.5	13.0	10.3
HMM-2000	max	max	4	9.8	16.2	13.0
HMM-2000	sawb	max	4	9.0	15.6	12.3
HMM-2000	sawb	pact	4	8.1	14.2	11.2
HMM-2000	sawb	bac	4	8.0	14.2	11.1
$\mathbf{HMM-2000}^a$	sawb	bac	4	8.0	14.0	11.0

activations (inputs and hidden states) with asymmetric PACT: loss degradation is significantly reduced and the average WER improves 0.2% compared to the SAWB/MAX combination.

Figure 2(b) shows the evolution of the boundaries α_{\pm} for the *inputs* of the first two LSTM layers with a PACT quantizer. For layers beyond the first, these values converge within a single epoch to the constraints set by the LSTM cell (± 1), scaled by the dropout layer to ± 1.3 . A similar behavior is seen for the boundaries on the hidden states, which quickly approach ± 1 (no dropout scaling is applied in between time steps). In separate experiments, we also observed that the convergence values of the boundaries in the first layer, which do take several epochs to settle, are not very sensitive to their initial choice.

The BAC quantizer obtains the best results when applied to the LSTM layers of DBLSTM-HMM-300, with a slight improvement in losses compared to PACT and a WER gap of just 0.3% on either test set, compared to the FP32 model. Training time is $\sim\!10\%$ faster than PACT. Qualitatively, the learned boundaries α_{\pm} for the first layer follow a similar trend as with PACT, and are constant in all following layers (see Sec. 3.1). DBLSTM-HMM-2000 QAT displays a similar behavior: at 8 bits, a simple MAX quantizer is sufficient to preserve the accuracy of the FP32 model (WER SWB/CH = 7.5/13.0%). On the other hand, INT4 QAT with SAWB for weights and PACT for activations shows 1% degradation compared to the best performing model. Replacing PACT with BAC results in our best average WER (0.8% gap), with a reduction in training time of $\sim\!10\%$.

In addition of adopting the BAC quantizer, we apply our QAT fine-tuning strategy to achieve state-of-art INT4 DBLSTM-HMM models. Figure 3(a) compares the losses during QAT of DBLSTM-HMM-300 trained either from scratch or from a pre-trained FP32 model. By adjusting the LR schedule as described in Sec. 3.3, training time is halved: by epoch 10 losses have stabilized and a WER of 10.4/18.3% is obtained. As shown in Table 1, with significantly longer training time, QAT from scratch may achieve marginally better results. This strategy however does not scale well in larger LSTM-based models such as DBLSTM-HMM-2000 and RNN-T. Figure 3(b) compares losses in similar scenarios using the DBLSTM-HMM-2000 model. The QAT fine-tuning strategy rapidly achieves small holdout losses within 5 epoch and results in the best WER (SWB/CH = 8.0/14.0%), outperforming QAT from scratch with only half of the training time.

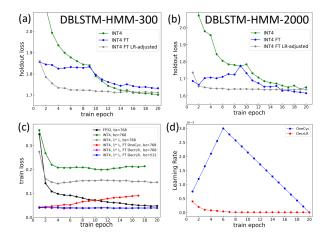


Figure 3: (a,b) DBLSTM-HMM QAT fine-tuning (FT) from pretrained model. (c) RNN-T QAT, with and without fine-tuning. (d) RNN-T QAT LR schedules (triangular and decreasing)

4.2. RNN-T

RNN-T training is computationally expensive, which makes QAT much harder than for other workloads [26]. To speed up training, we adopt large-batch training techniques [27] and use 24 V100 GPUs with a per-worker batch size of 32. We also leverage BAC, with $\alpha = 1.25$ on the inputs and $\alpha = 1$ on the hidden states. Compared to QAT with learnable quantizer parameters (PACT), BAC achieves a 1.4× speed-up in this RNN-T implementation. Although these solutions make INT4 QAT exploration more efficient, they are not sufficient to achieve high accuracy. As shown in Fig. 3(c) (green line), INT4 QAT causes a significant degradation in losses compared to the FP32 baseline (black line). To improve accuracy we further apply our QAT fine tuning strategy (see Sec. 3.3) in RNN-T. In detail, (1) since a proper representation of the first LSTM layer encoder inputs is critical and the impact of this layer on computation is limited (see Sec. 3.2), we retain it in full-precision. This decisively mitigates the degradation (grey line). (2) We apply QAT to fine-tune a state-of-the-art pre-trained model (red line)¹; it has been shown that starting from a pre-trained model requires significantly fewer train iterations [28]; this is particularly important for RNN-T. (3) Instead of OneCycleLR [3], we use a custom LR policy (see Sec. 3.3 and Fig. 3(d)) which achieves better performance (purple line). (4) To further improve the QAT result, we reduce the total batch size from 768 to 512; this mitigates large batch size training challenges [29] with an acceptable trade-off in training time. With help from these tricks, our INT4 QAT quickly reaches a small training loss in 8 epochs and achieves 13.1% average WER (8.8% for SWB and 17.4% for CH), a 1.3% degradation compared to the FP32 baseline of 11.8% (WER SWB 8.0%, CH 15.6%) [3]. Table 2 summarizes our RNN-T quantization experiments.² Notice that a FP32 prediction network achieves the best WER (13.05%) but also increases decoding inference time by $3.3 \times$, compared to a 4-bit prediction network (see Sec. 4.3). Thus, optimizing both accuracy and inference speed requires its quantization.

Table 2: 4-bit RNN-T quantization.

QAT	FT	BZ	LR	b	its	WER			
				1st L	PrNN	SWB	СН	avg	
BL	BL	64	OneCyc	32	32	8.0	15.6	11.8	
N	_	_	= '	4	4	12.2	22.6	17.4	
Y	N	768	OneCyc	4	4	30.1	40.9	35.5	
Y	N	768	OneCyc	32	4	17.3	27.3	22.3	
Y	Y	768	OneCyc	32	4	9.8	18.7	14.25	
Y	Y	768	DecrLR	32	4	9.2	17.4	13.3	
Y	\mathbf{Y}	512	DecrLR	32	4	8.8	17.4	13.1	
Y	Y	512	DecrLR	32	32	8.8	17.3	13.05	

4.3. Inference performance in end-to-end models

We quantify the runtime improvement achieved through INT4 quantization relative to FP16 implementation. We consider a system with a CPU attached to a co-processor capable of executing common deep learning operations such as GEMM, LSTM activation *etc.* at various data precisions [30]. The co-processor executes the compute-heavy data-parallel portions of the workload, while control-heavy operations (*e.g.*, sorting and pruning hypothesis) are mapped to the CPU. We utilize a detailed performance model [31,32] to estimate runtime of the co-processor, which has been calibrated to within 1% of silicon measurements. We further instrumented our CPU implementation with performance counters to measure the execution time of workload portions mapped to the CPU.

Fig. 1(a) shows the improvement in INT4 inference runtime on the RNN-T model for an average sequence length of 152 and different beam widths. The total execution time is broken into the encoder, prediction network, and joint components. In the acoustic encoder, all LSTM layers beside the first are quantized to 4 bits; this dramatically increases their computation throughput and reduces encoder runtime by 2.6× (blue bars). Due to the iterative beam search (decoding) process [3], the decoder runtime increases significantly with beam width. Thanks to the quantized prediction network, the decoding time (red bars) scales well between FP16 and INT4, achieving 3.3× speed-up, mitigating the impact of wider beams. On the other hand, the joint network runtime (gray bars) remains constant, as it is always executed in FP16 and involves control-heavy operations executed on the CPU. As the fraction of operations contributed by the joint network is small, this has little impact on the end-toend runtime. Overall, the INT4 implementation achieves $2.6 \times$ speed-up compared to FP16 across different beam widths.

5. Conclusions

4-bit quantization is a promising technique to dramatically reduce inference time in LSTM-based ASR systems, but has not yet found wide adoption in real-world scenarios, primarily due to lack of demonstrations on large models and standard datasets, and concerns about accuracy degradation. In this paper, we proposed a practical methodology to achieve state-of-art accuracy at 4 bits, in hybrid and end-to-end LSTM speech networks trained on large corpora and evaluated on a standard test set. Our work lays down the foundation to introduce ultra-low precision inference in speech recognition domains.

¹QAT fine tuning from FP32 model with batch size (64) shows comparable training losses to FP32 training with large batch size (768)

²In Table 2, acronyms are as follows. BL: baseline; FT: fine tuning; BZ: batch size; LR: learning rate; 1st L: 1st LSTM layer of encoder; PrNN: prediction network; OneCyc: one cycle learning rate policy; DecrLR: decreasing learning rate policy. The best WER obtained in QAT is reported.

³As beam width increases, so do the proportion of both the prediction network (red) and non-accelerable computations (gray), resulting in the beam width having minor impact on the end-to-end acceleration.

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