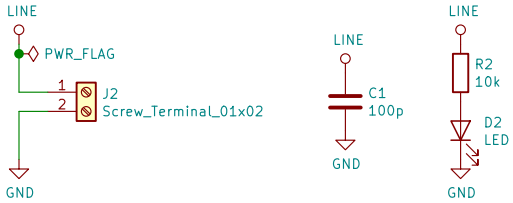
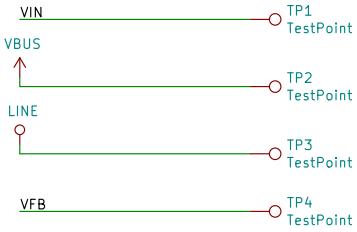
Power Input: approx. 15V

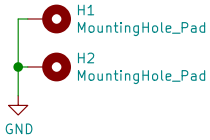
Power Output: 3.3 to 15V



Test Points



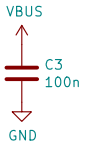
Mounting Holes



Regulator Circuit: Variable Line Voltage

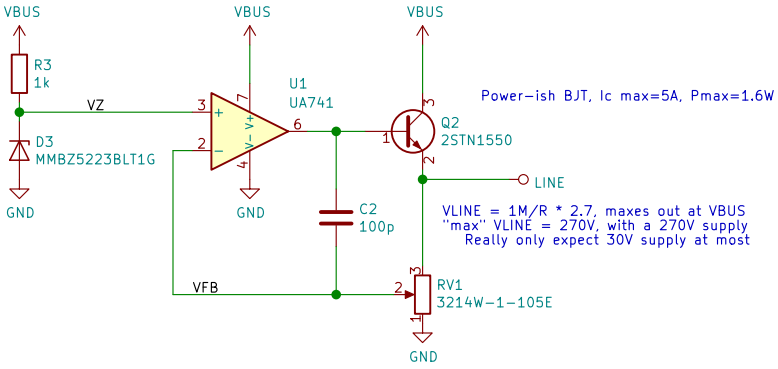
Vz=2.7 gives output range 3v3 to VBUS

C2, C3 100pF b/c worked on breadboard without



Pz = Vz*Iz < 0.2W
Iz = (VBUS−Vz)/R

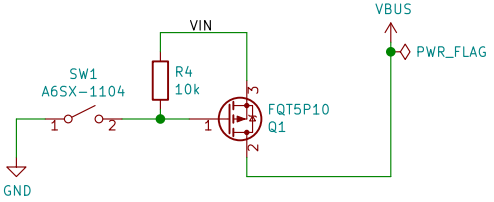
(15−2.7)/R*2.7<0.2
1660hms < R



Rmin = 1M*0.01 = 10k
Pmax = 30^2/10k = 0.09V < 0.25W
Assuming max input voltage of 30V

Power Switch: Higher Current Rating

15^2/10k = 0.02W
15/10k = 1.5mA



Engineer: Marion Anderson		
Sheet: / File: vreg−741.sch		
Title: Adjustable Linear Voltage Regulator − 741 base		
Size: USLetter	Date:	**Rev: 1**
KiCad E.D.A. kicad 5.0.2+dfsg1−1		Id: 1/1