

Homework 2

Ben Lowman | CS 3330

January 7th, 2013

1. One might choose to use the add instruction as opposed to lw as the addi instruction accepts immediate values - that is, constants do not necessarily have to be stored in a register before you add them to another register. More importantly, because of its structure type ("Type I"), 16 bits are available to process the offset from a base memory address. With lw, there are only five available bits. This limits the maximum offset value to 32, which is low enough to frequently cause problems.
2. Loading the array into registers...

```
1 | ; lw is used in this case as the offset value does not exceed
   | 32
2 |
3 | lw $t0 0($s3)
4 | lw $t1 4($s3)
5 | lw $t2 8($s3)
6 | lw $t3 12($s3)
7 | lw $t4 16($s3)
8 | lw $t5 20($s3)
9 | lw $t6 24($s3)
```

Storing the array in reverse order...

```
1 | sw $t6 0($s3)
2 | sw $t5 4($s3)
3 | sw $t4 8($s3)
4 | sw $t3 12($s3)
5 | sw $t2 16($s3)
6 | sw $t1 20($s3)
7 | sw $t0 24($s3)
```

3. Occasionally computer programs will contain more variables than the processor has registers. As such, the compiler will attempt to put less

frequently used variables into memory instead. This allows more frequently accessed variables to benefit from the faster speed of registers.

4. MIPS pseudocode is used for this problem (no corresponding registers were given)

```

1 | sub h, h, 5
2 | add f, g, h

```

5. Equivalent C instructions:

```

1 | f = g + h + i;

```

- 6.

```

1 | sub $t0, $s3, $s4
2 | sll $t0, t0, 2
3 | add $t1, $s7, $t0
4 | lw $t2, 0($t1)
5 | sw $t2, 32($s6)

```

- 7.

$$\begin{aligned}
 0xabcdef12 &= 10 \times 16^7 + 11 \times 16^6 + 12 \times 16^5 + 13 \times 16^4 + \\
 &14 \times 16^3 + 15 \times 16^2 + 1 \times 16^1 + 2 \times 16^0 = 2882400018
 \end{aligned}$$

8. Equivalent C instructions:

```

1 | A[0] = A[1];
2 | f = A[1] + A[0];

```

9. The instruction is an add instruction. It executes the following code:

```

1 | add $s0, $s0, $s0

```

10. t2 has a value of 3

- 11.

```

1 | fib:
2 |     slti $t0, $a0, 2      ;continue statements
3 |     beq $t0, $zero, cont
4 |     addi $v0, $zero, 1    ;else make the return value 1

```

```

5      jr $ra
6
7  cont:
8      ;open up space in the stack
9      addi $sp, $sp, -16
10     sw $ra, 0($sp)
11     sw $a0, 4($sp)
12
13     ; getting n-1
14     addi $a0, $a0, -1
15     jal fib
16     sw $v0, 8($sp)
17     lw $a0, 4($sp)
18
19     ;getting n-2
20     addi $a0, $a0, -2
21     jal fib
22     sw $v0, 12($sp)
23
24     ;cleaning up the stack
25     lw $ra, 0($sp)
26     lw $t0, 8($sp)
27     lw $t1, 12($sp)
28     addi $sp, $sp, 16
29
30     ;final computation
31     add $v0, $t0, $t1
32
33     jr $ra

```