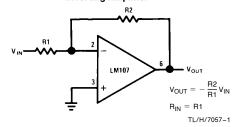
Op Amp Circuit Collection

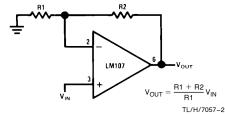
National Semiconductor **Application Note 31** February 1978



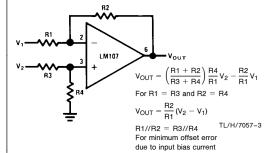
SECTION 1—BASIC CIRCUITS **Inverting Amplifier**



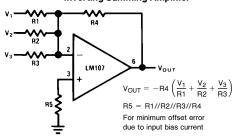
Non-Inverting Amplifier



Difference Amplifier

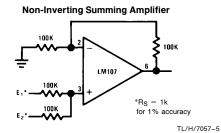


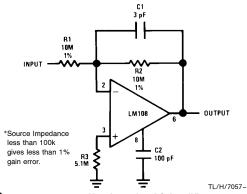
Inverting Summing Amplifier



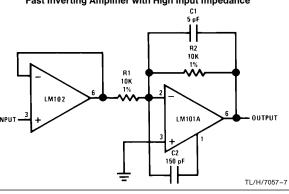
TL/H/7057-4

Inverting Amplifier with High Input Impedance

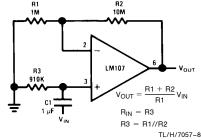


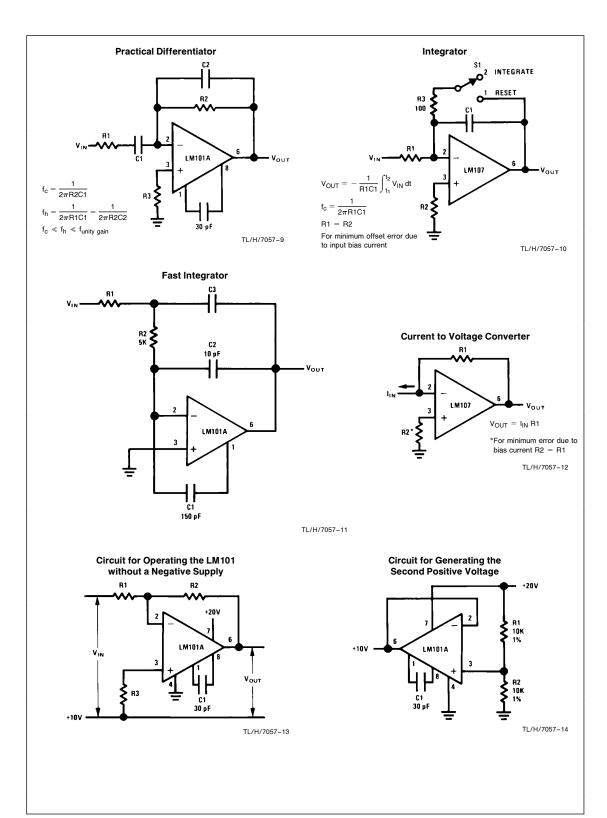


Fast Inverting Amplifier with High Input Impedance



Non-Inverting AC Amplifier

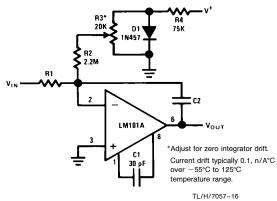




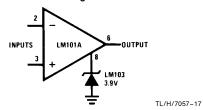
Neutralizing Input Capacitance to Optimize Response Time OUTPUT LM107

TL/H/7057-15

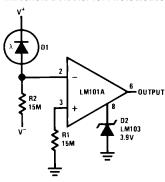
Integrator with Bias Current Compensation



Voltage Comparator for Driving DTL or TTL Integrated Circuits

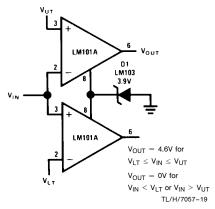


Threshold Detector for Photodiodes

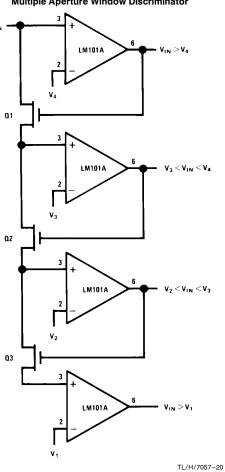


TL/H/7057-18

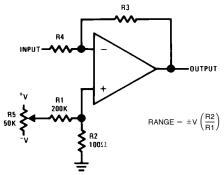
Double-Ended Limit Detector



Multiple Aperture Window Discriminator

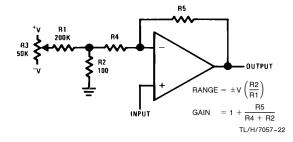


Offset Voltage Adjustment for Inverting Amplifiers Using Any Type of Feedback Element

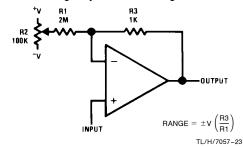


TL/H/7057-21

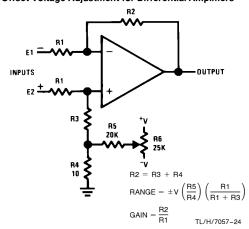
Offset Voltage Adjustment for Non-Inverting Amplifiers Using Any Type of Feedback Element



Offset Voltage Adjustment for Voltage Followers

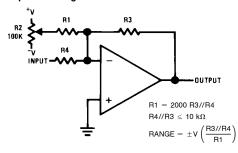


Offset Voltage Adjustment for Differential Amplifiers



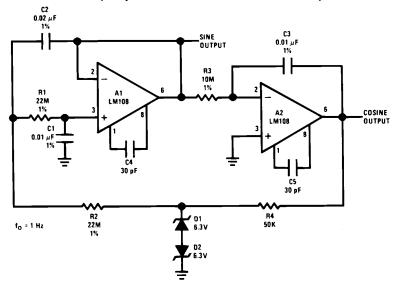
TL/H/7057-25

Offset Voltage Adjustment for Inverting Amplifiers Using 10 ${\bf k}\Omega$ Source Resistance or Less



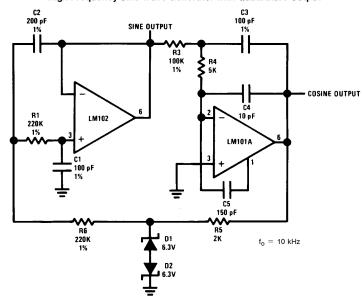
SECTION 2 — SIGNAL GENERATION

Low Frequency Sine Wave Generator with Quadrature Output

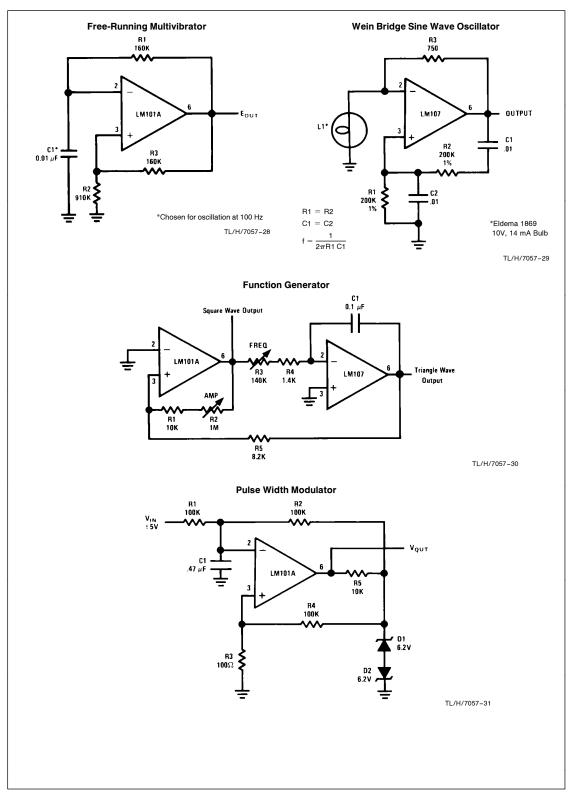


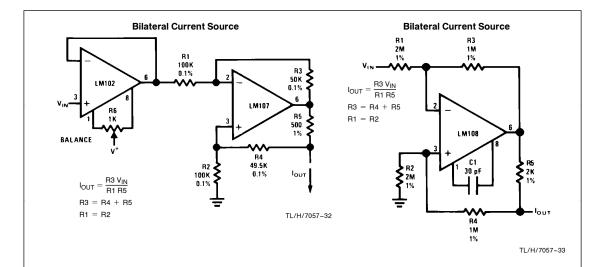
TL/H/7057-26

High Frequency Sine Wave Generator with Quadrature Output

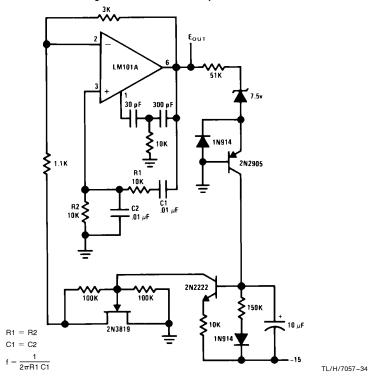


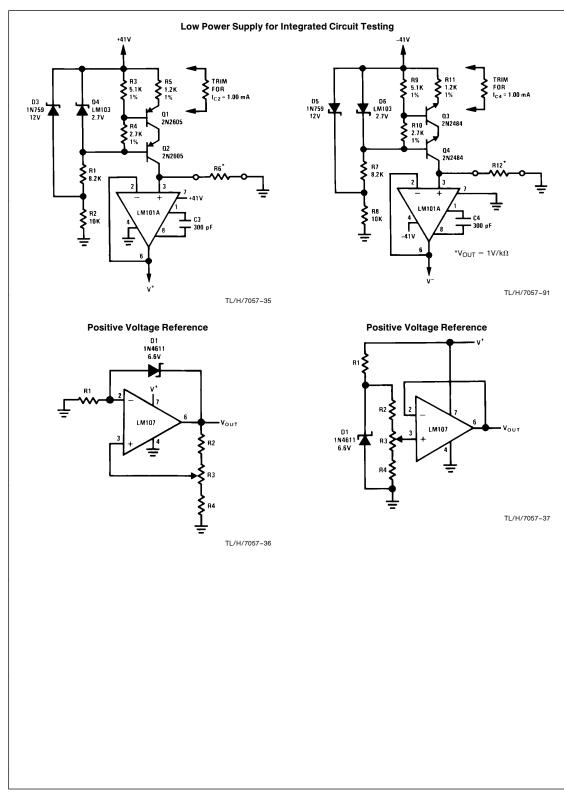
TL/H/7057-27

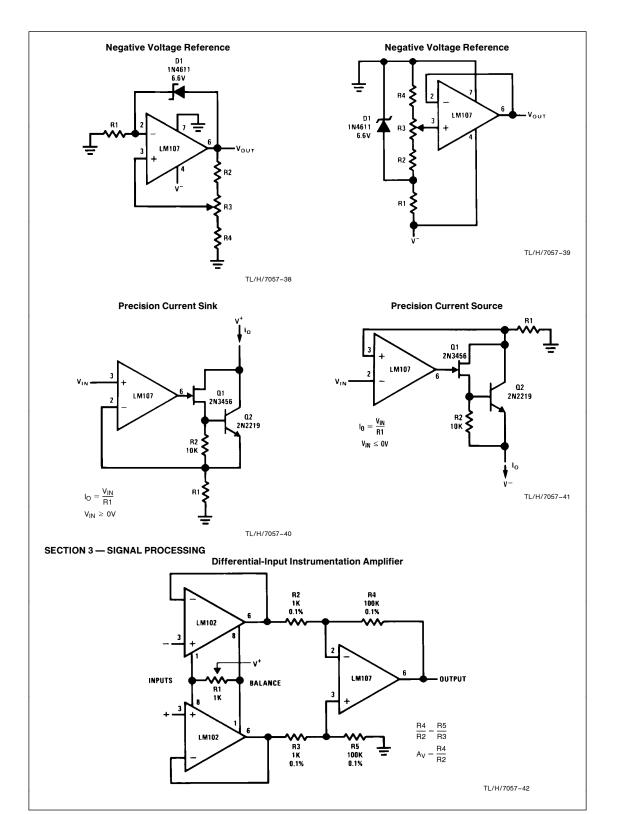


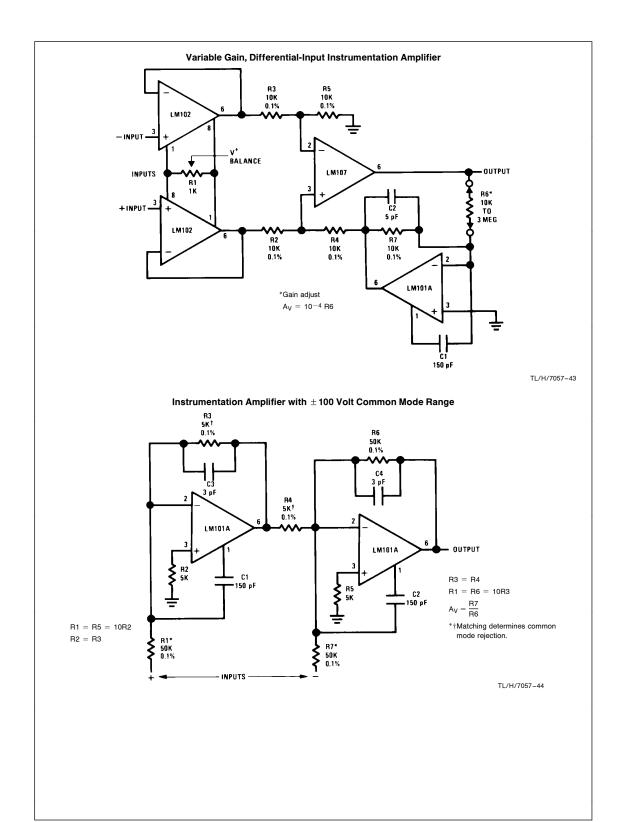


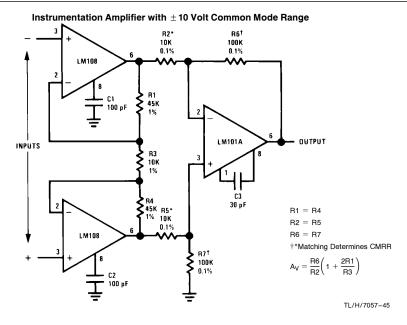
Wein Bridge Oscillator with FET Amplitude Stabilization



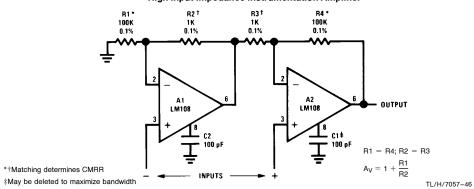




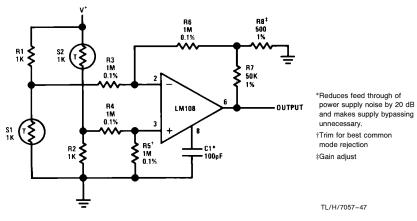


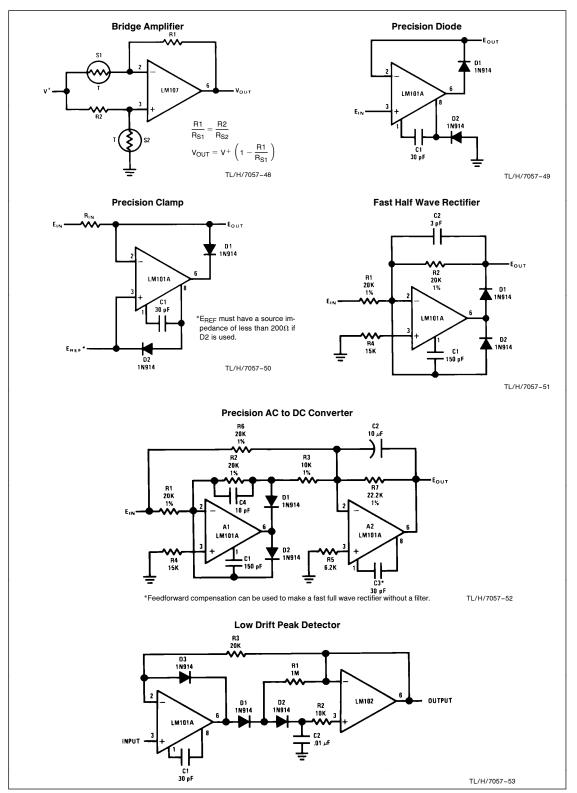


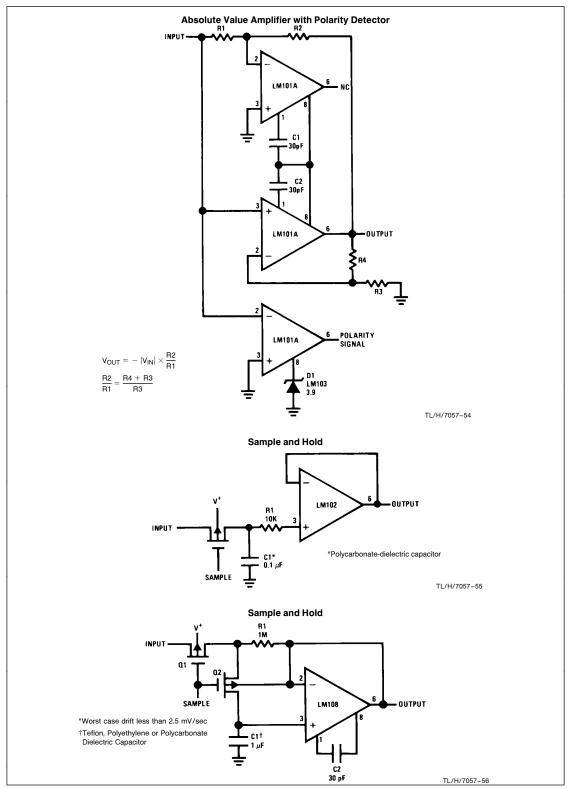
High Input Impedance Instrumentation Amplifier



Bridge Amplifier with Low Noise Compensation





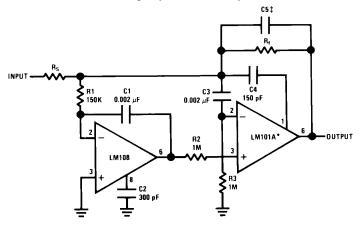


Low Drift Integrator R2 100K 01* 02 -RESET LM108 OUTPUT C3

*Q1 and Q3 should not have internal gate-protection diodes.

 $$\rm TL/H/7057{-}57$$ Worst case drift less than 500 $\mu V/sec$ over $-55^{\circ} C$ to $+125^{\circ} C.$

Fast† Summing Amplifier with Low Input Current

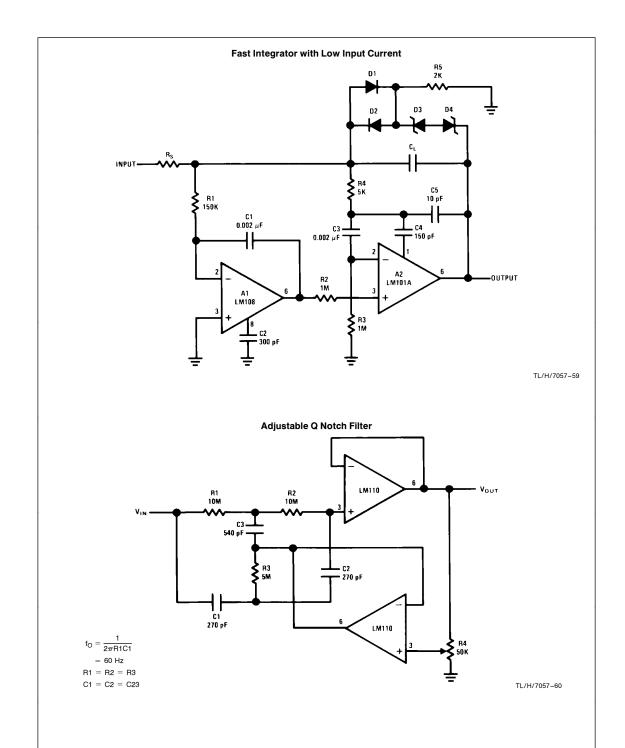


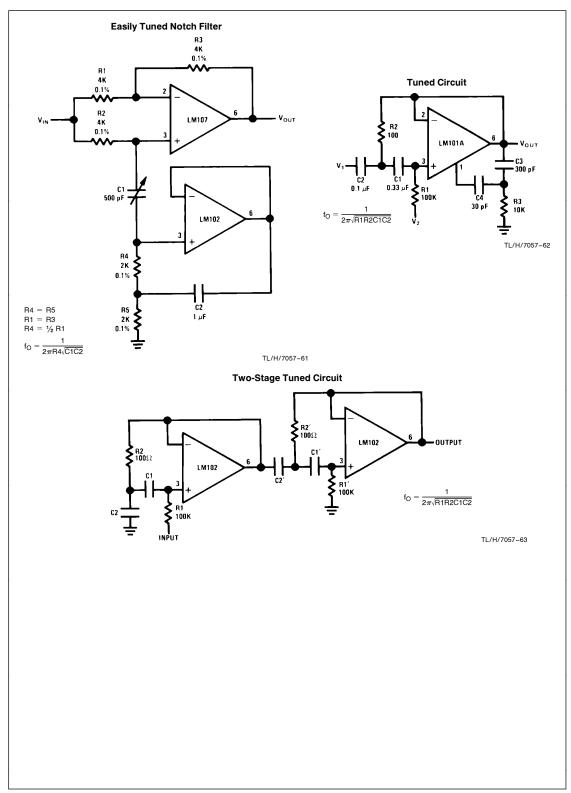
TL/H/7057-58

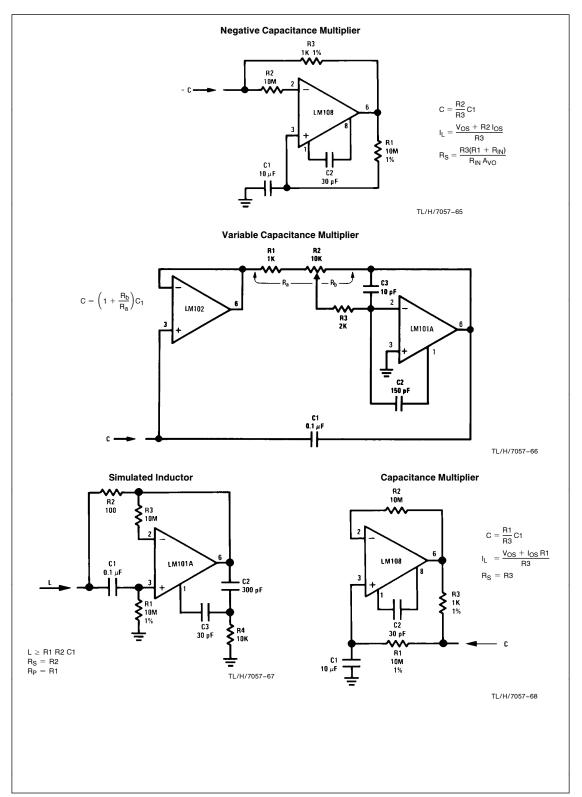
In addition to increasing speed, the LM101A raises high and low frequency gain, increases output drive capability and eliminates thermal feedback.

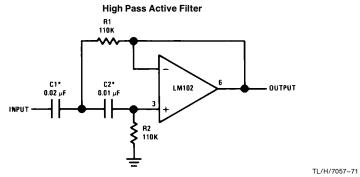
† Power Bandwidth: 250 kHz Small Signal Bandwidth: 3.5 MHz Slew Rate: 10V/µs

$$\ddagger C5 = \frac{6\times 10^{-8}}{R_f}$$



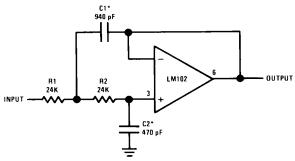






*Values are for 100 Hz cutoff. Use metalized polycarbonate capacitors for good temperature stability.

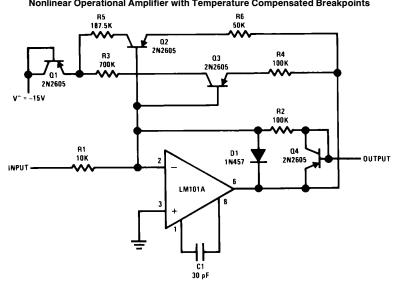
Low Pass Active Filter



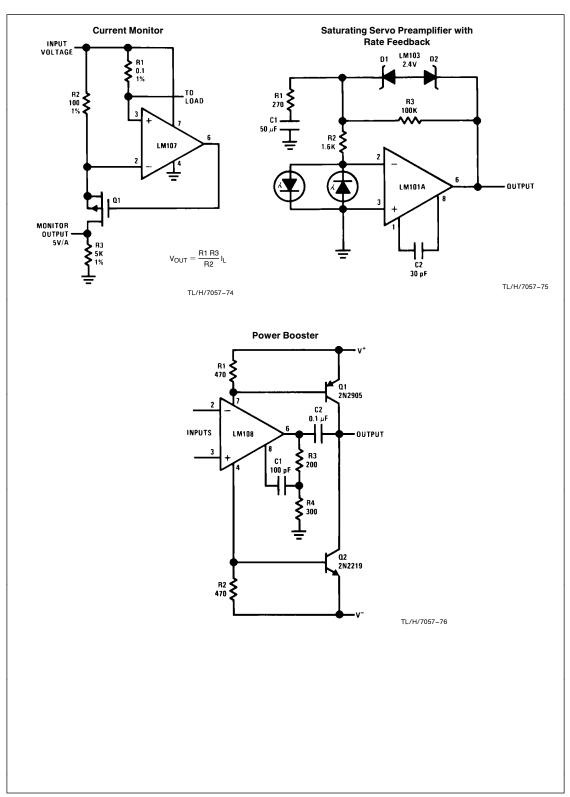
TL/H/7057-72

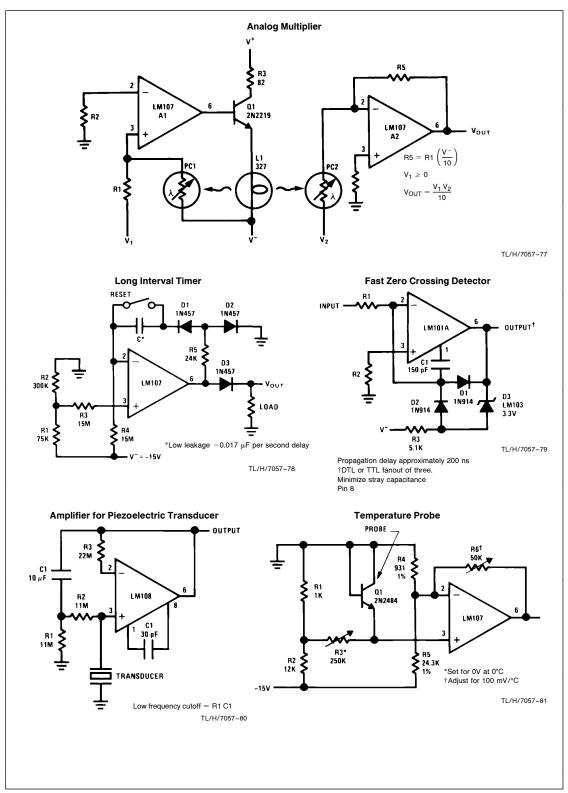
*Values are for 10 kHz cutoff. Use silvered mica capacitors for good temperature stability.

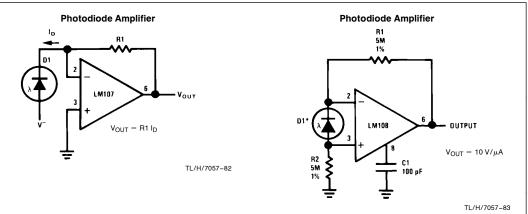
Nonlinear Operational Amplifier with Temperature Compensated Breakpoints



TL/H/7057-73

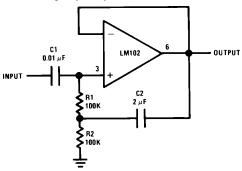






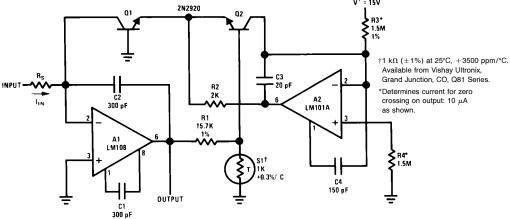
*Operating photodiode with less than 3 mV across it eliminates leakage currents.

High Input Impedance AC Follower



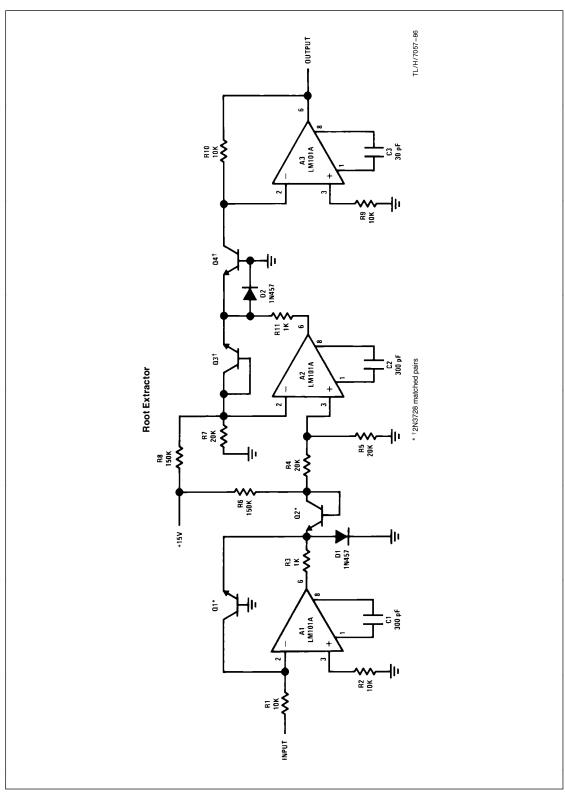
TL/H/7057-84

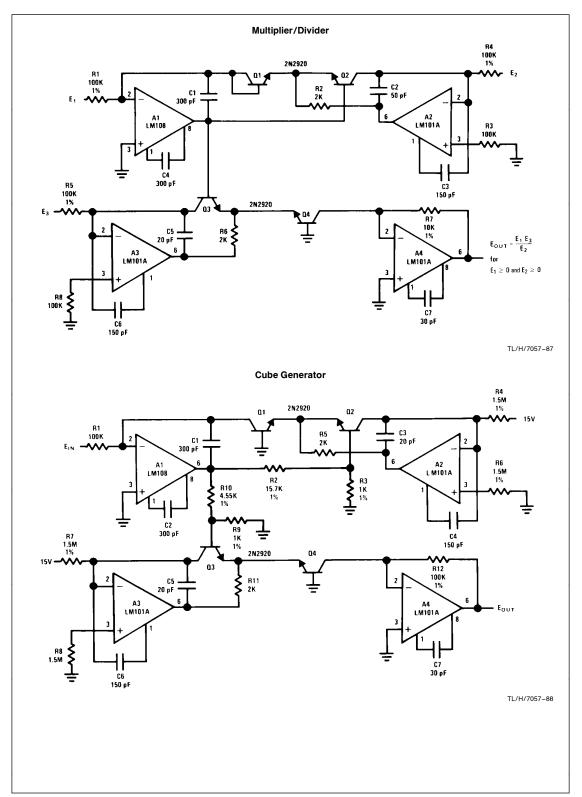
Temperature Compensated Logarithmic Converter

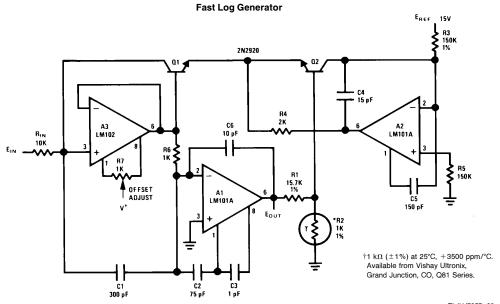


TL/H/7057-85

 $\begin{array}{l} \mbox{10 nA} < \mbox{I}_{\mbox{IN}} < \mbox{1 mA} \\ \mbox{Sensitivity is 1V per decade} \end{array}$

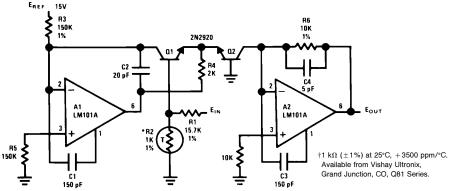






TL/H/7057-89

Anti-Log Generator



TL/H/7057-90

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