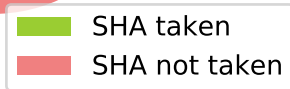
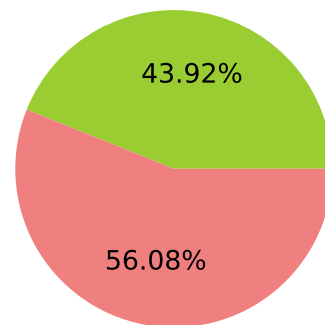
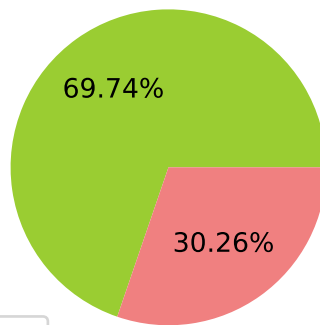
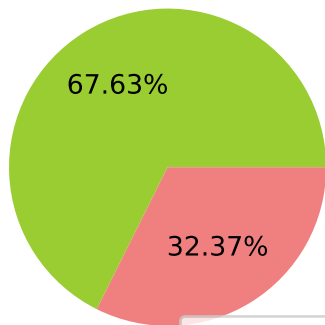


in

out

inout

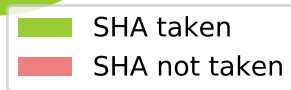
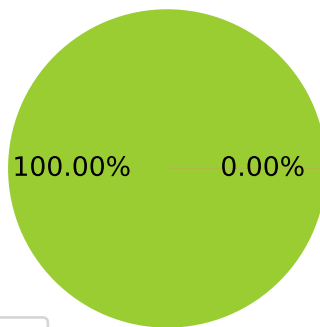
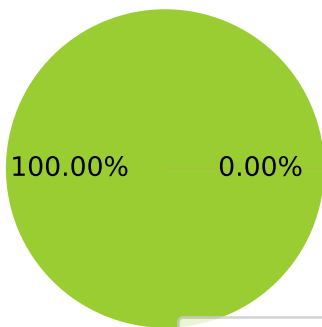
puce 0



in

out

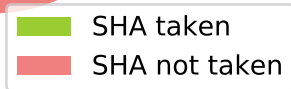
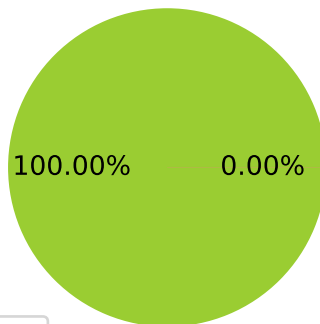
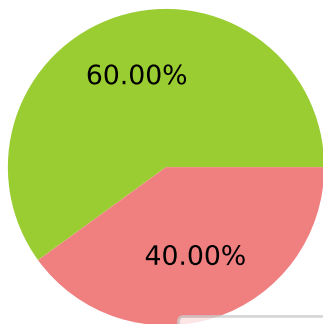
puce 5



in

out

puce 402

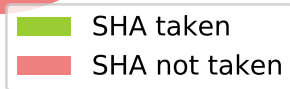
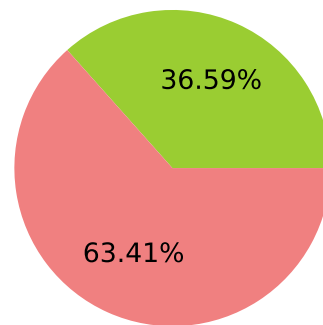
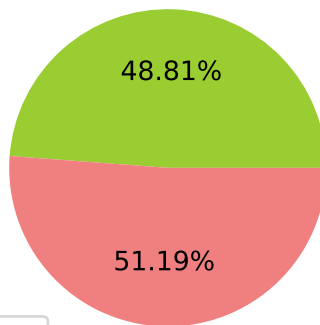
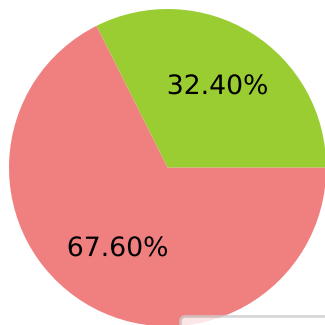


in

out

inout

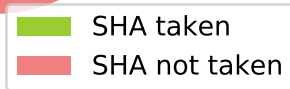
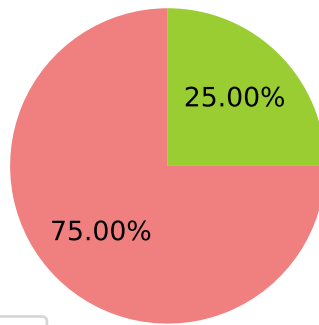
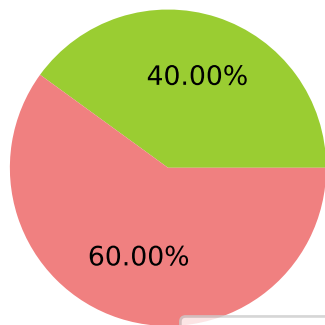
puce 404



in

out

puc 412

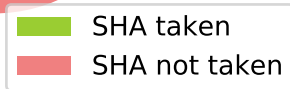
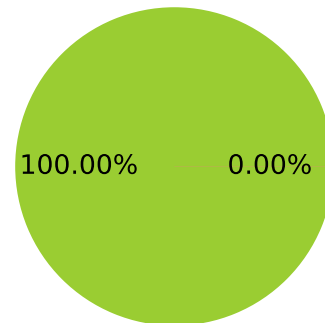
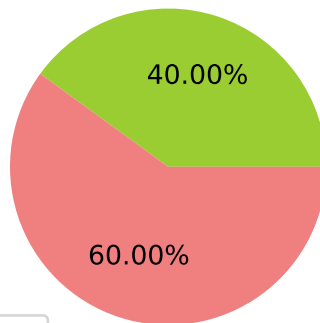
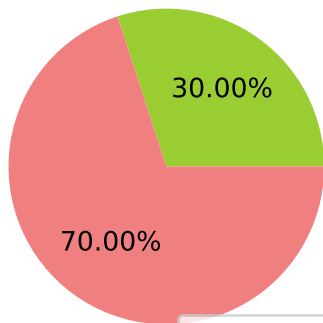


in

out

inout

puce 425

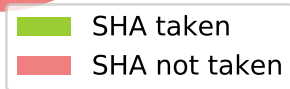
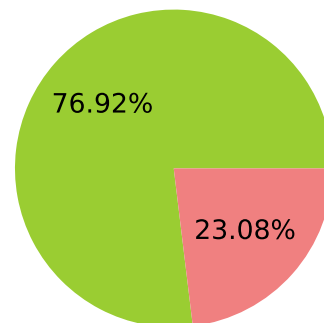
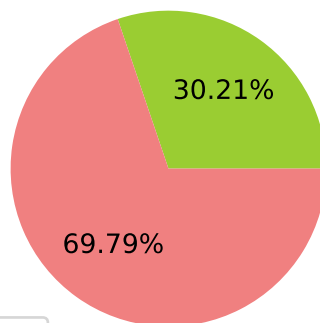
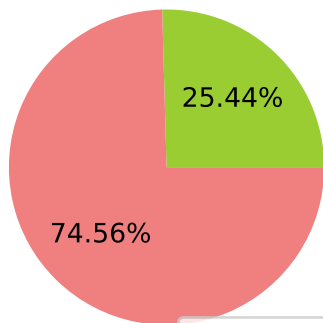


in

out

inout

puce 435

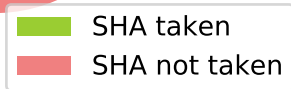
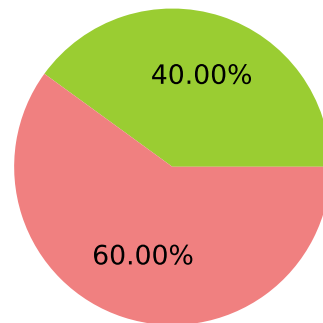
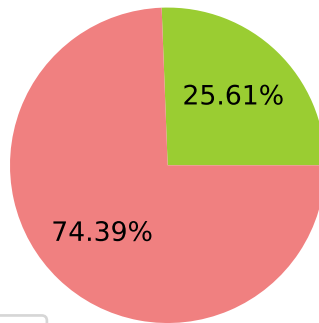
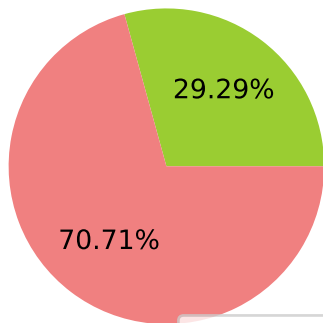


in

out

inout

puce 446

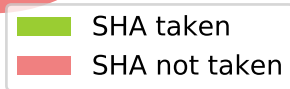
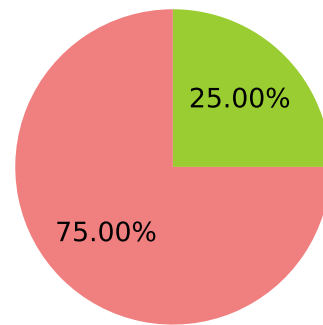
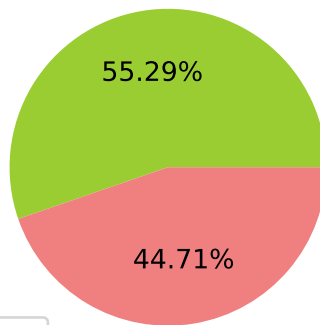
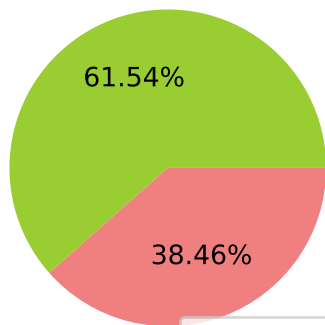


in

out

inout

puce 501

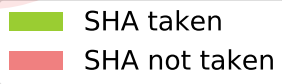
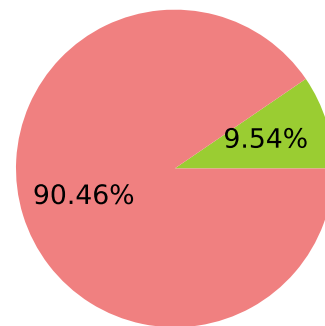
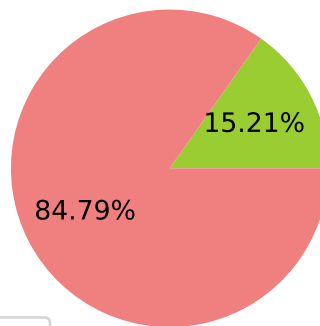
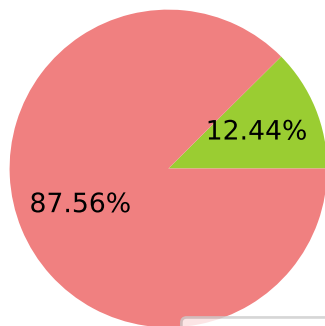


in

out

inout

puce 530

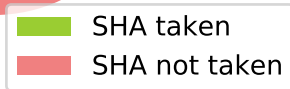
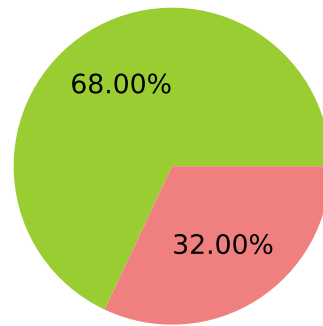
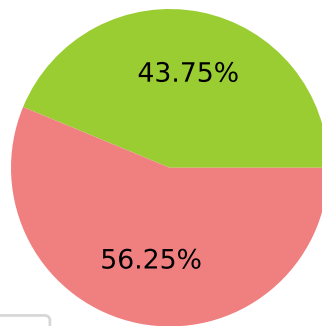
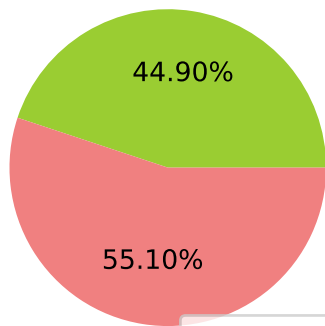


in

out

inout

puce 534

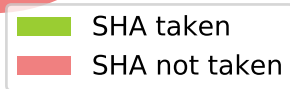
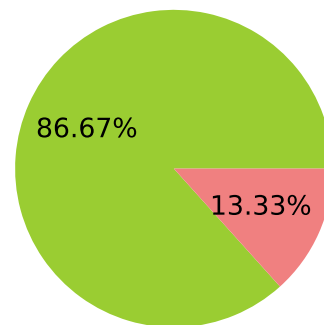
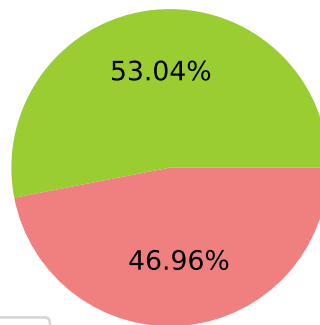
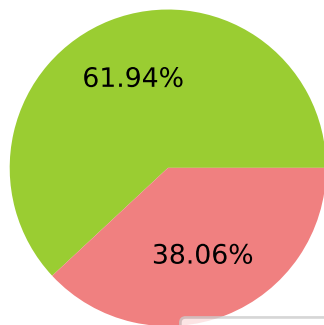


in

out

inout

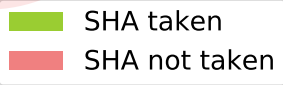
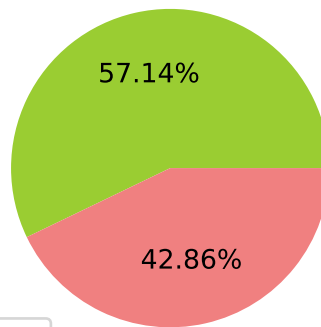
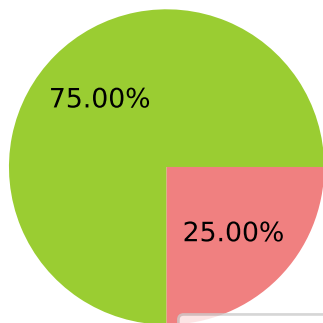
puce 581



in

out

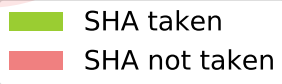
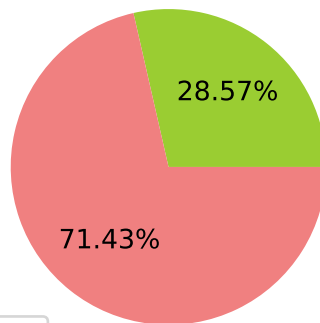
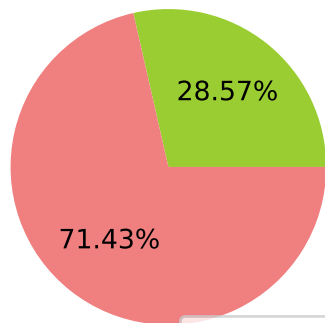
puce 598

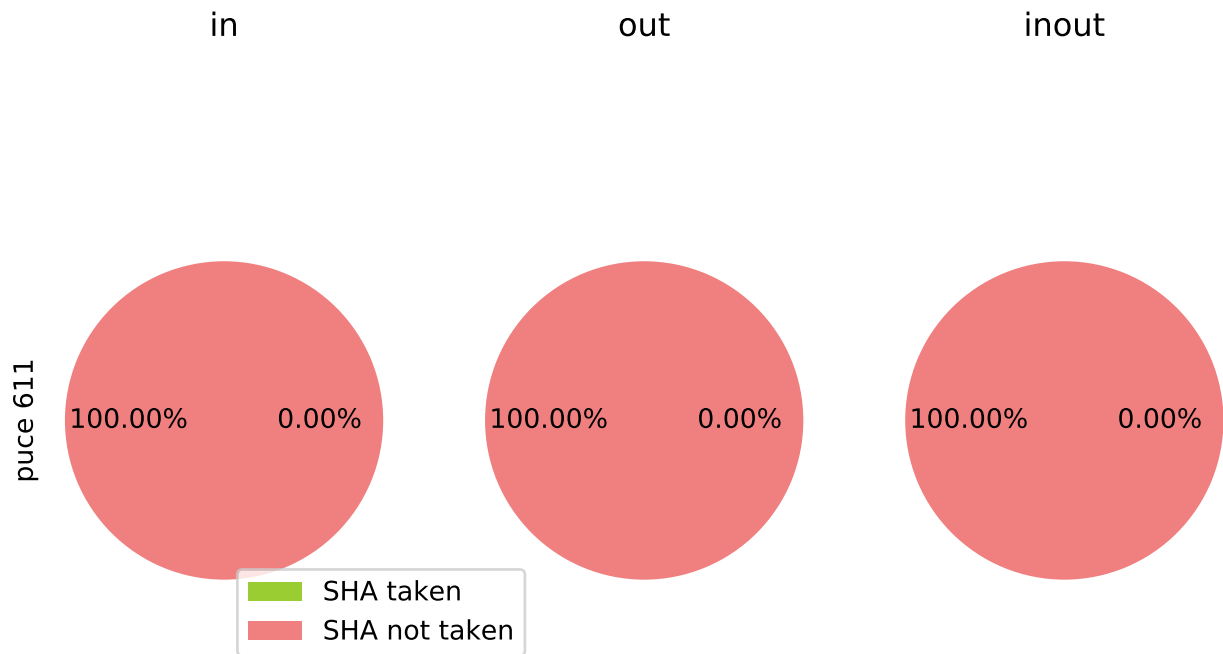


in

out

puce 605



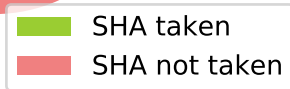
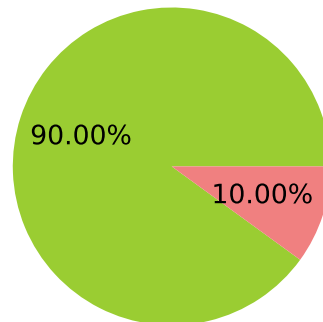
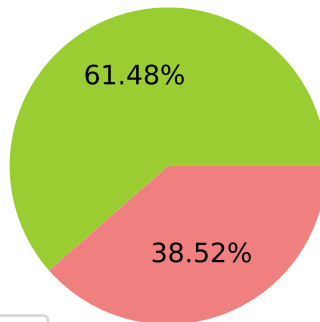
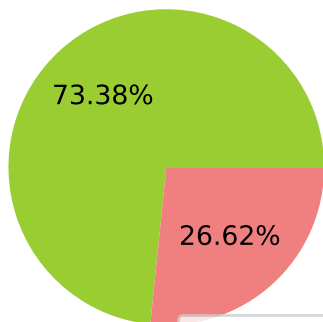


in

out

inout

puce 620

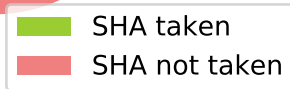
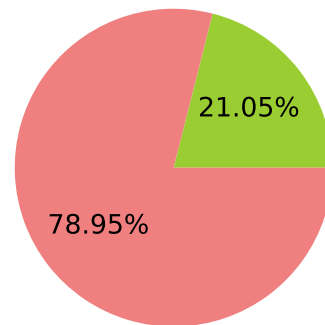
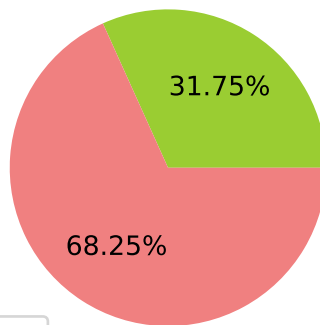
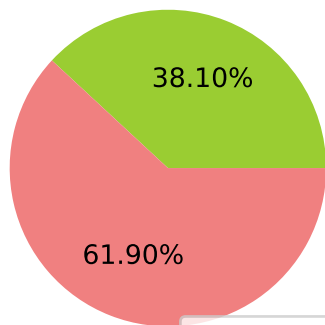


in

out

inout

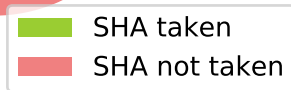
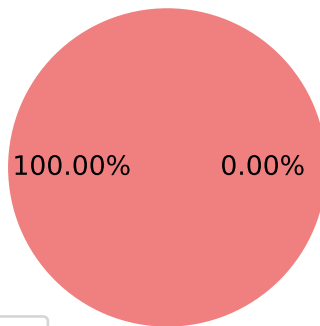
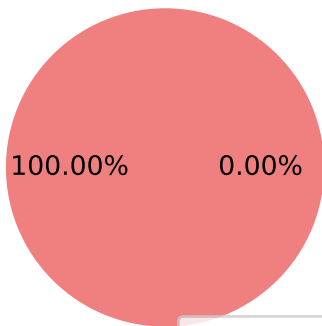
puce 685



in

out

puce 688

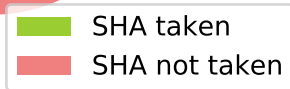
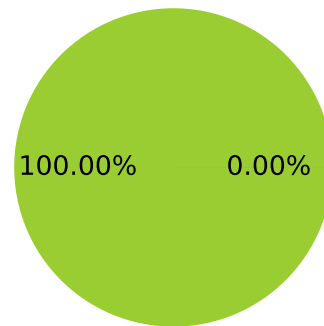
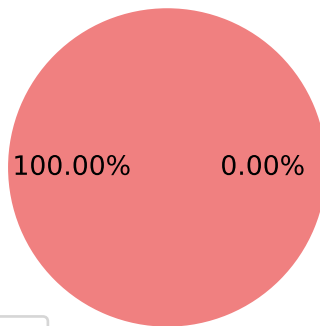
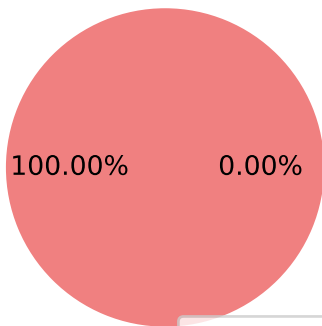


in

out

inout

puce 694

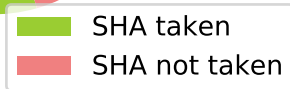
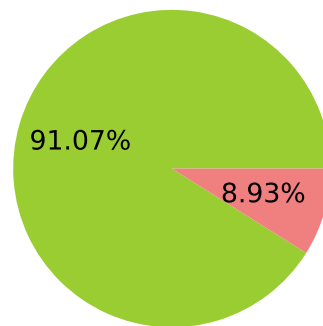
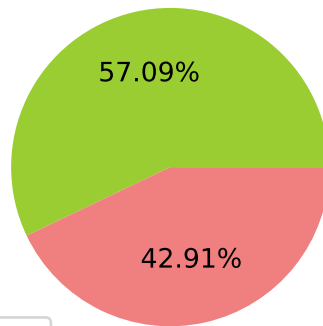
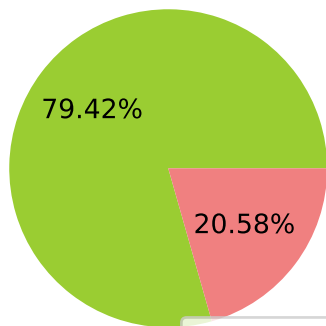


in

out

inout

puce 697

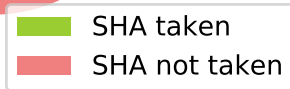
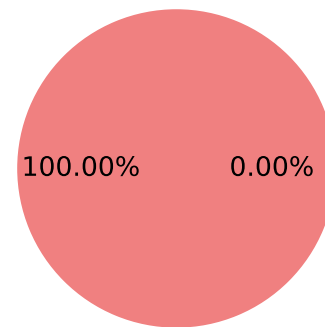
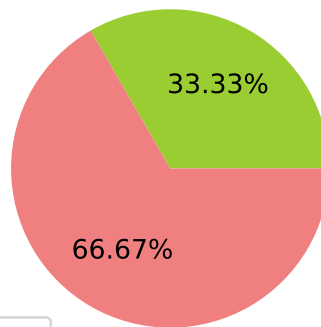
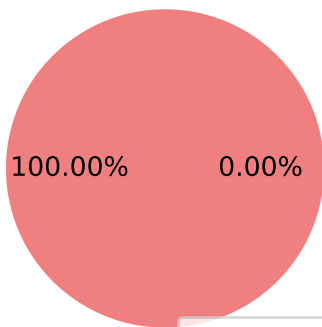


in

out

inout

puce 698

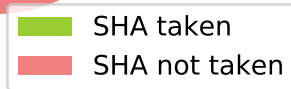
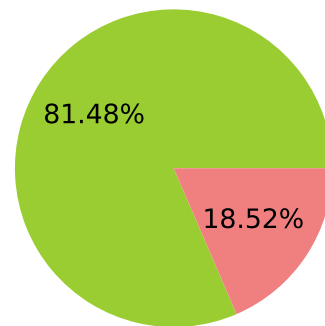
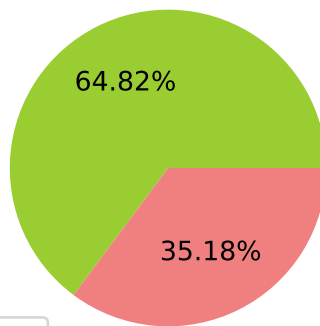
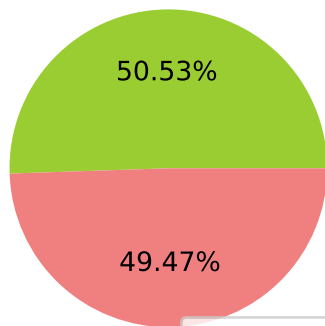


in

out

inout

puce 699

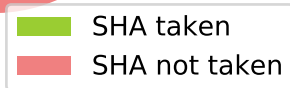
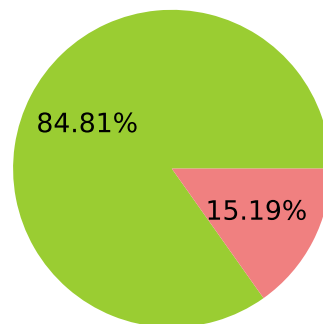
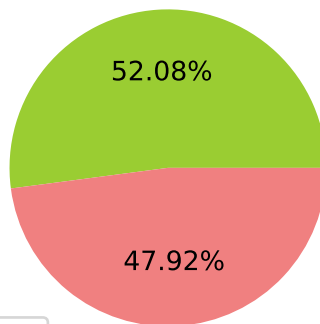
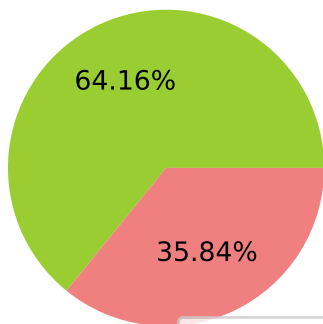


in

out

inout

puce 702

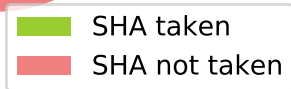
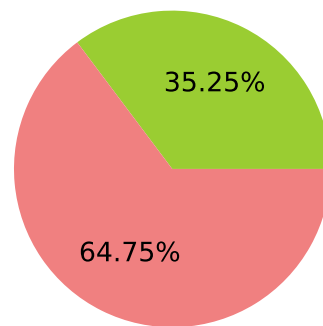
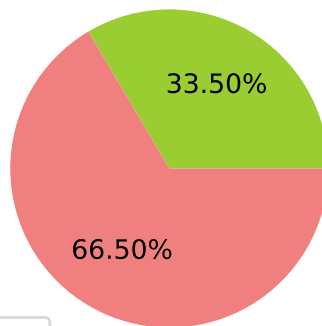
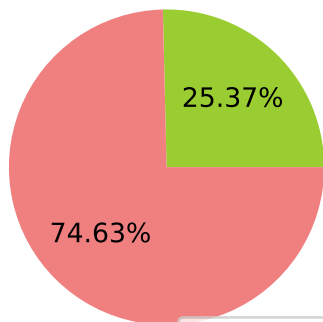


in

out

inout

puce 708

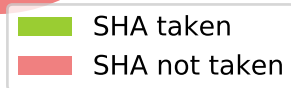
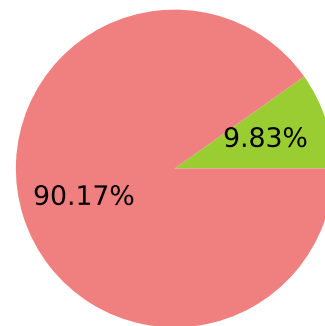
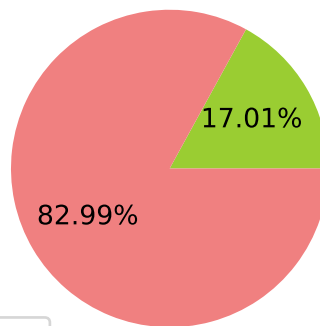
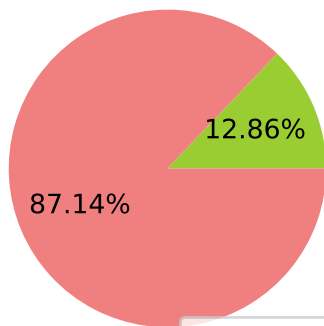


in

out

inout

puce 712

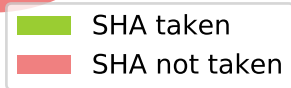
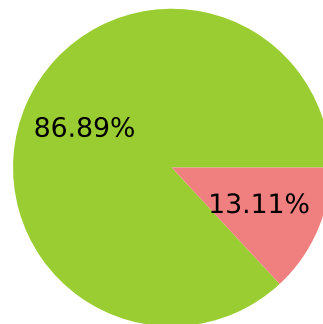
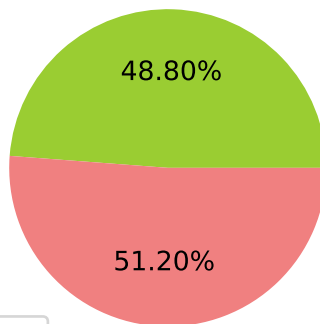
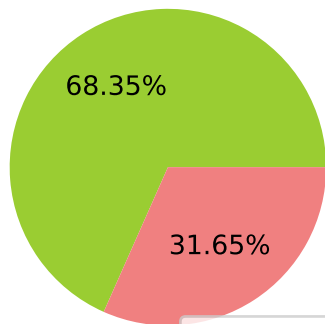


in

out

inout

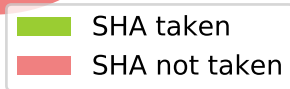
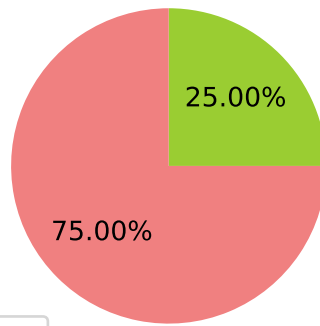
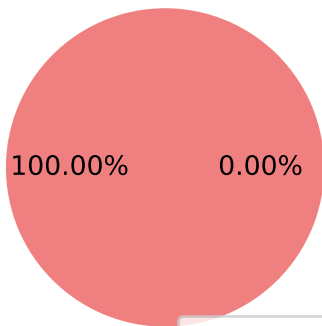
puce 714



in

out

puc 716

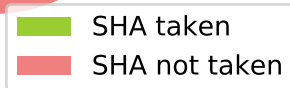
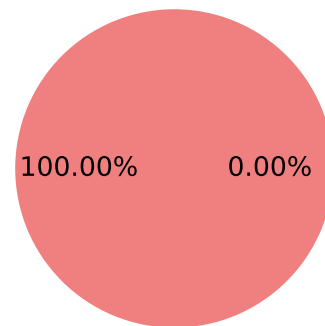
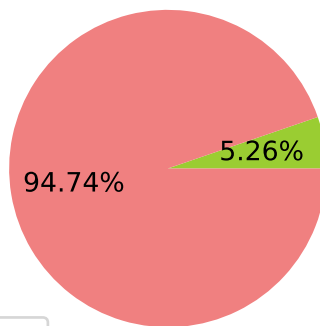
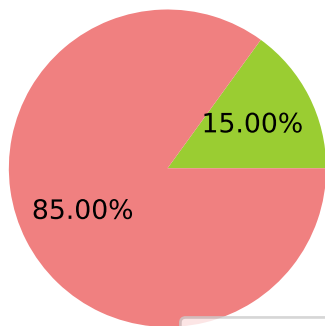


in

out

inout

puce 717

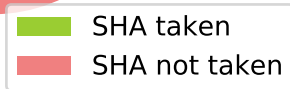
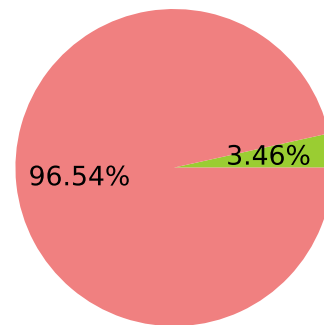
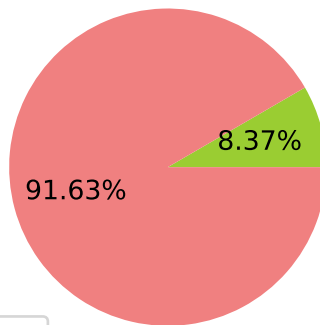
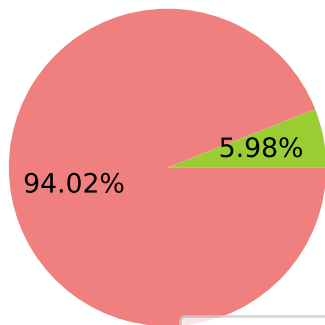


in

out

inout

puce 719

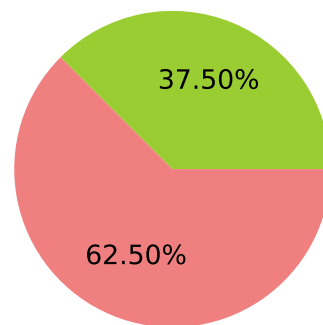
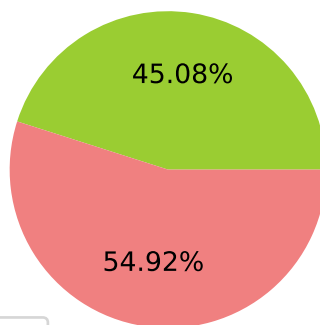
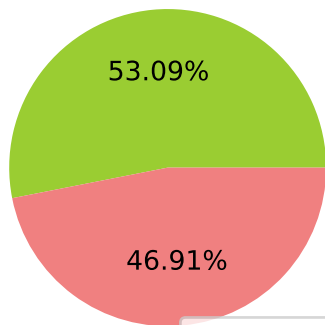


in

out

inout

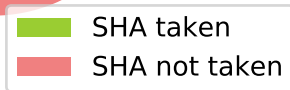
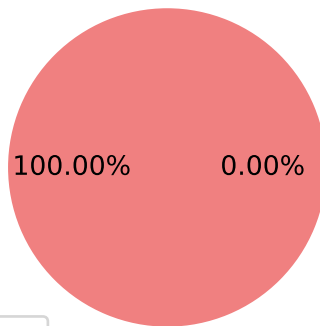
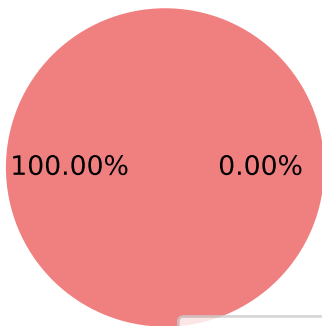
puce 721



in

out

puce 723

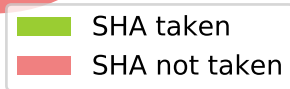
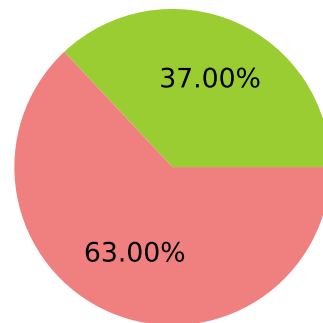
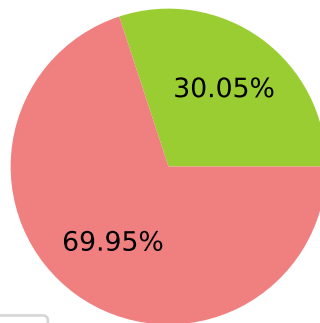
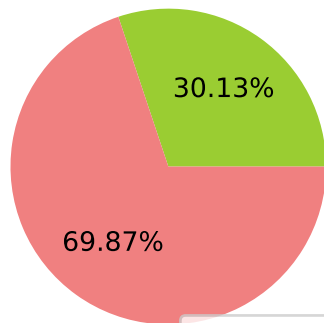


in

out

inout

puce 725

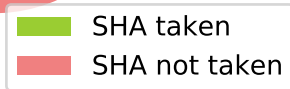
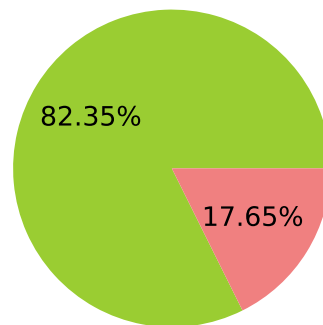
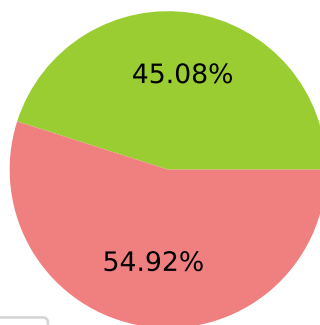
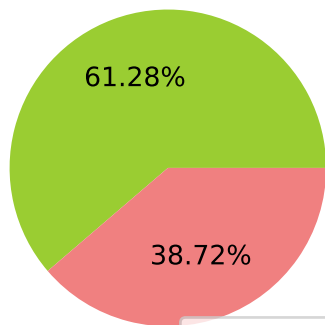


in

out

inout

puce 726

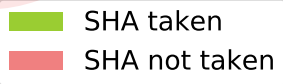
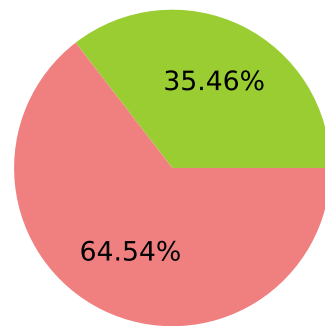
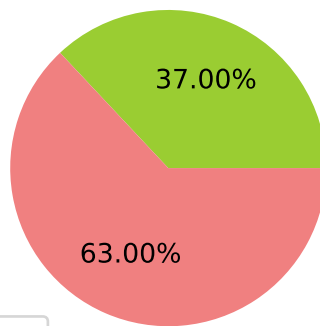
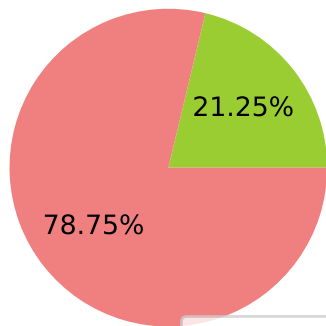


in

out

inout

puce 727

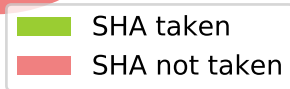
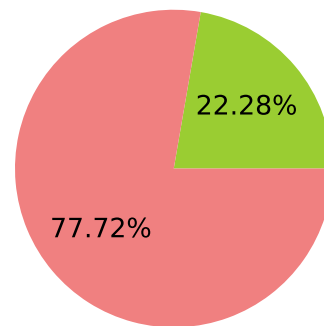
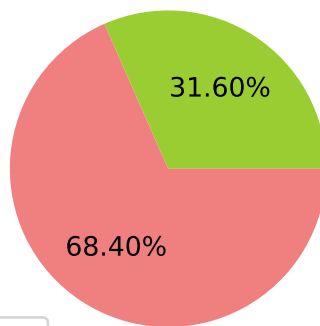
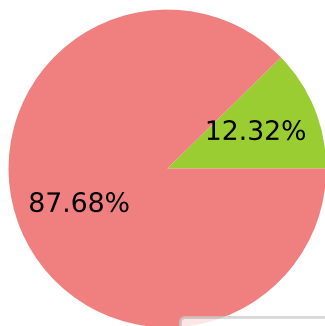


in

out

inout

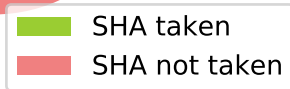
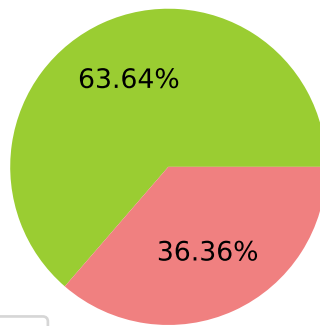
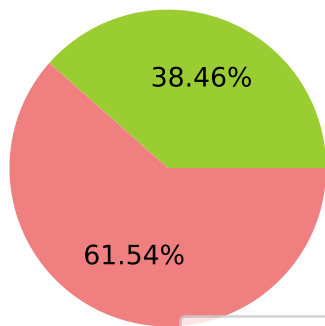
puce 729



in

out

puce 730

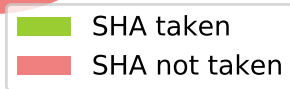
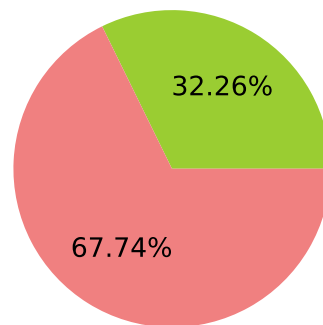
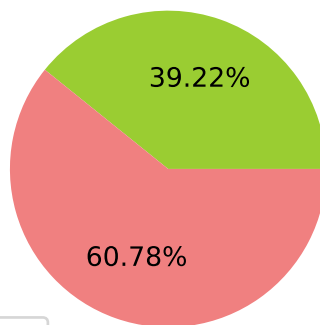
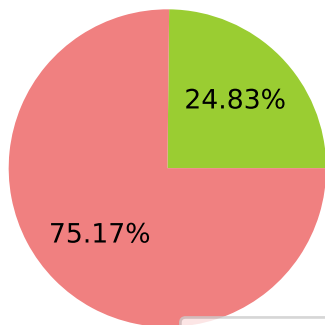


in

out

inout

puce 731

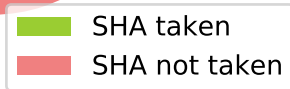
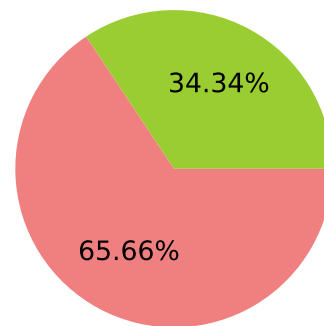
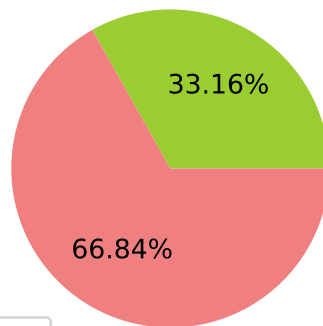
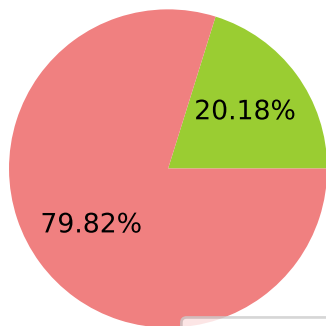


in

out

inout

puce 732

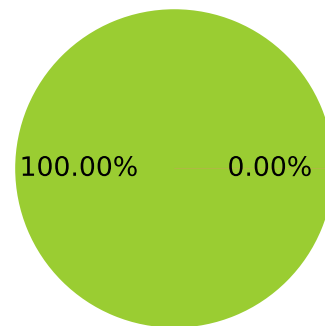
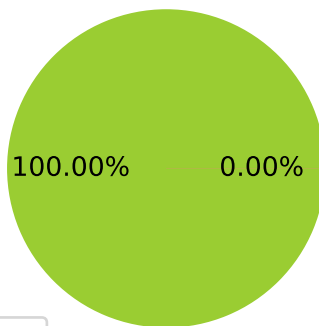
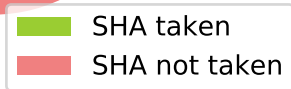
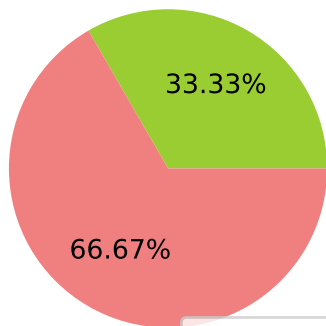


in

out

inout

puce 733

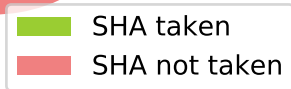
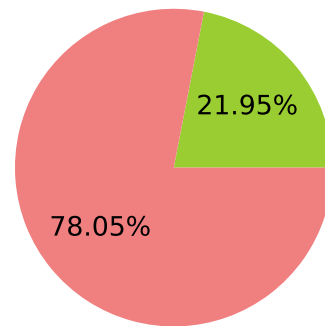
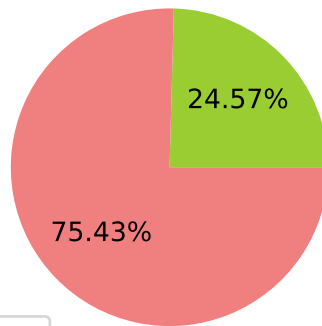
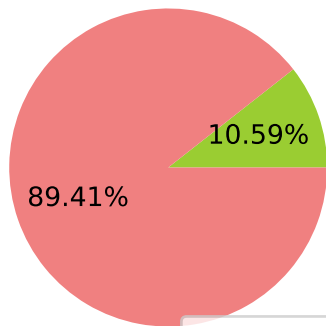


in

out

inout

puce 736

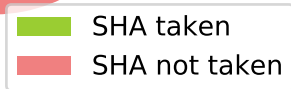
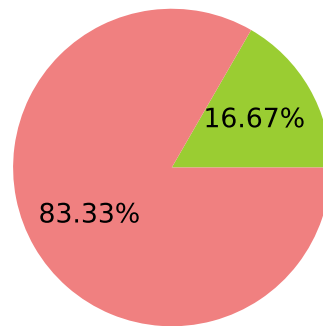
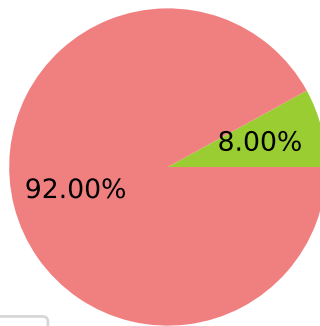
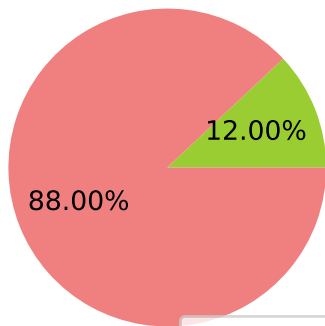


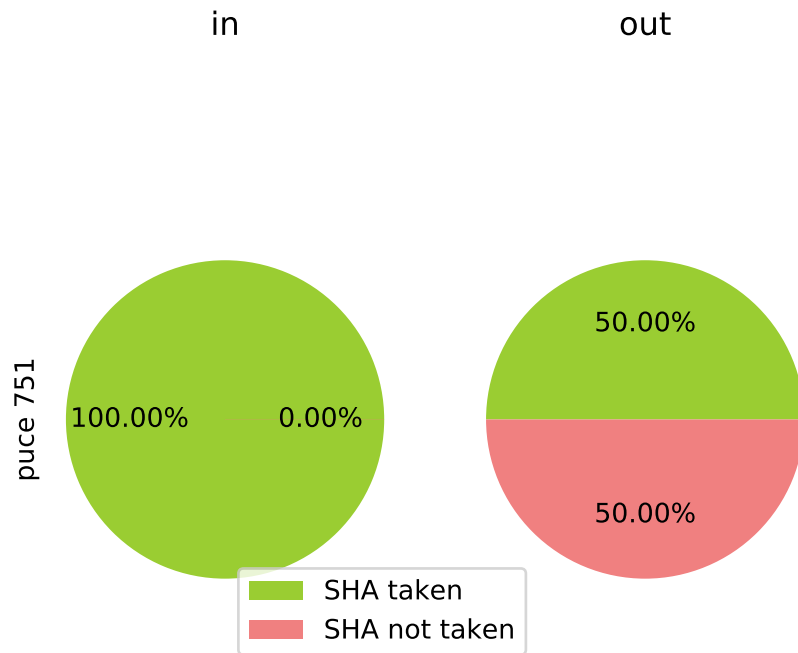
in

out

inout

puce 747



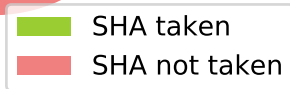
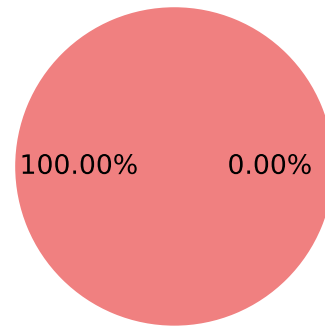
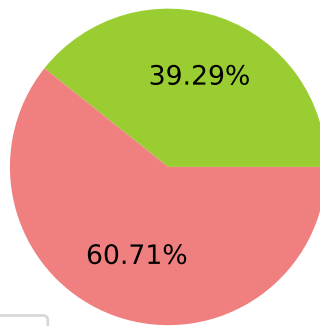
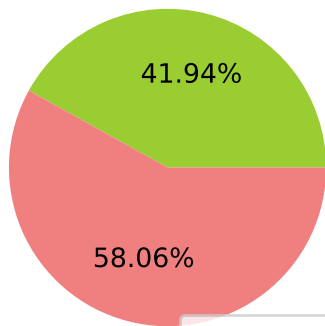


in

out

inout

puce 753

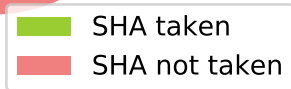
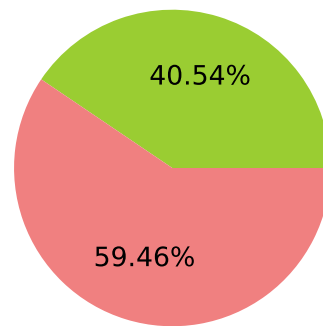
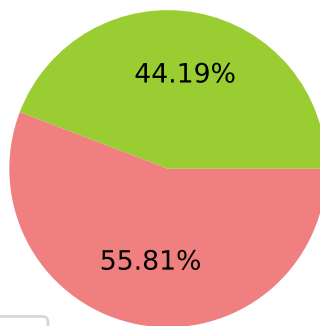
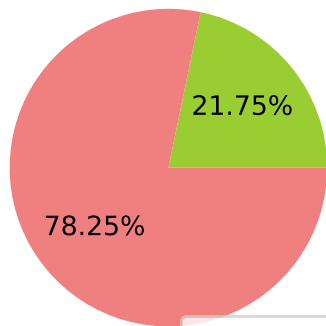


in

out

inout

puce 754

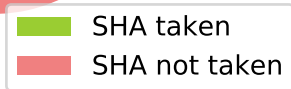
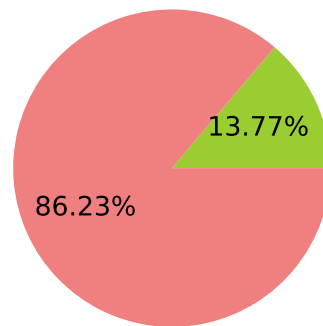
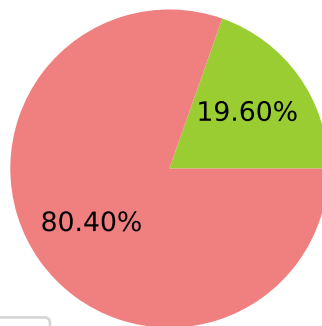
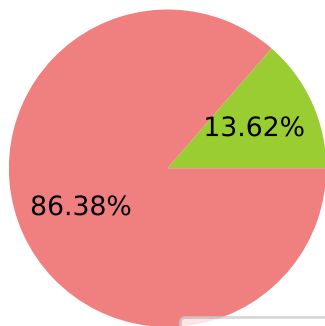


in

out

inout

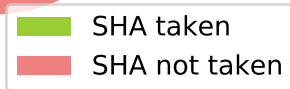
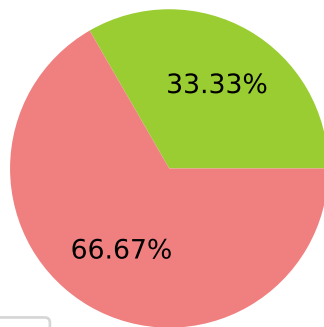
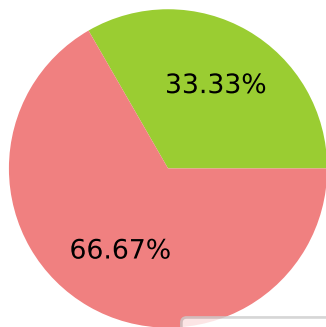
puce 755



in

out

puce 756

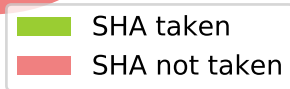
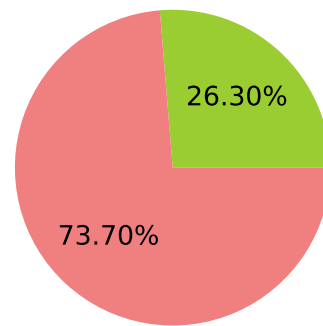
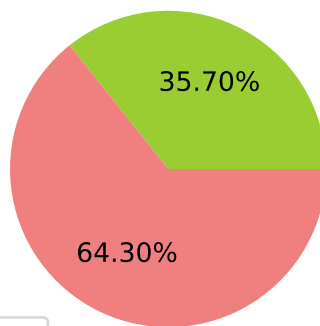
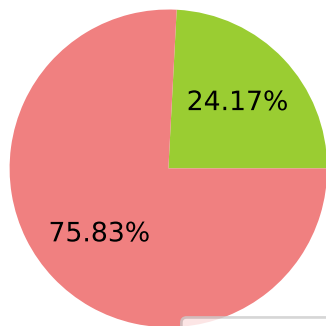


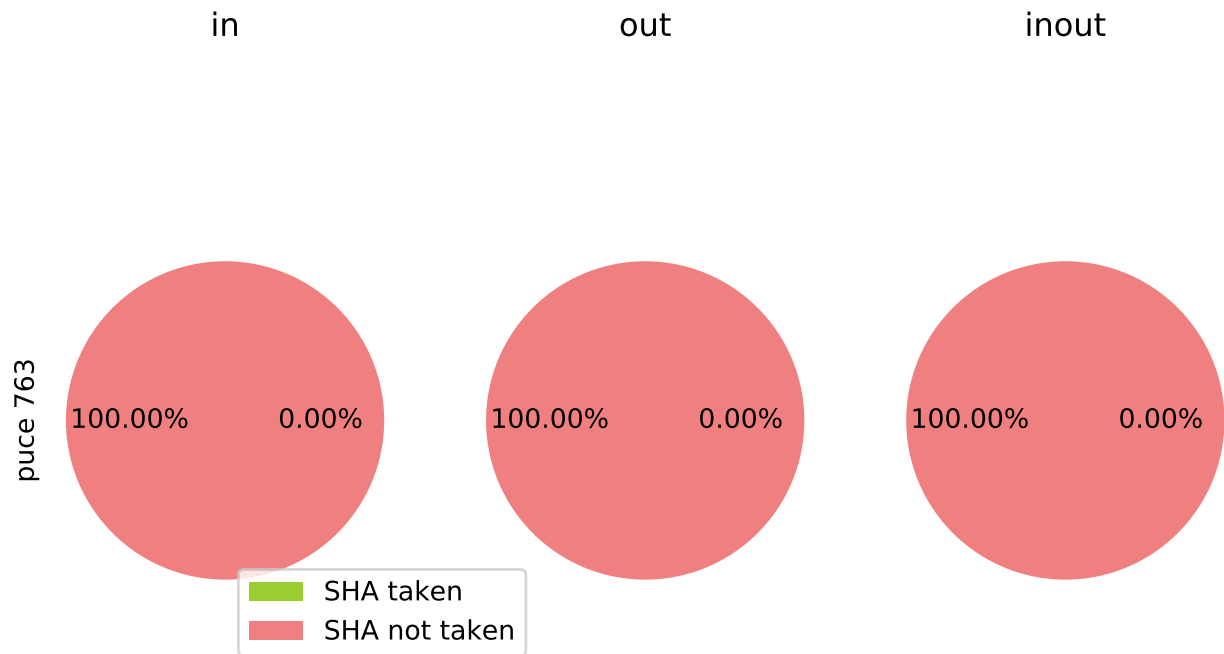
in

out

inout

puce 761



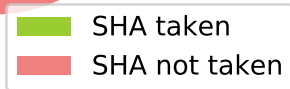
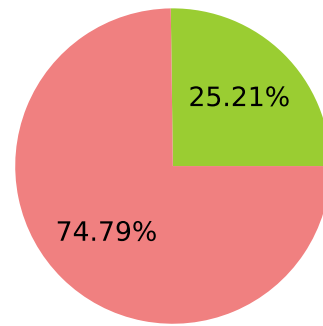
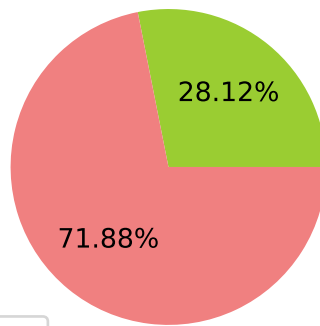
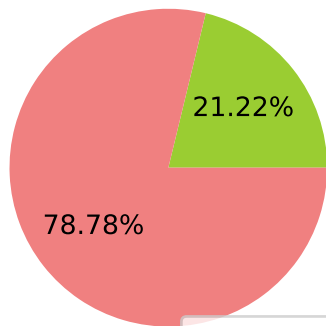


in

out

inout

puce 804

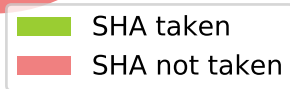
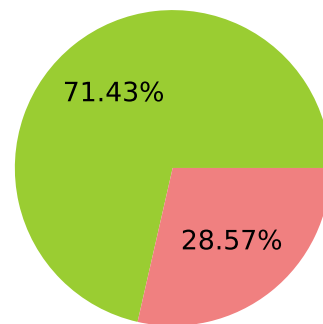
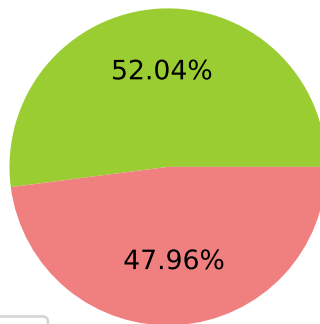
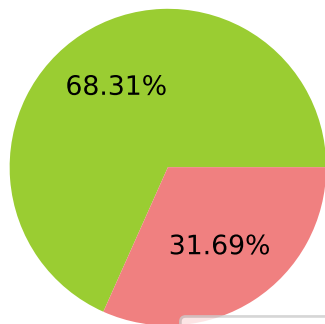


in

out

inout

puce 811

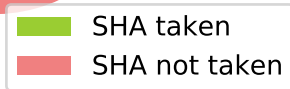
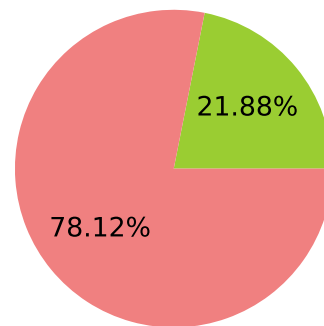
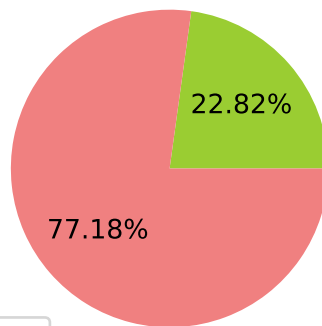
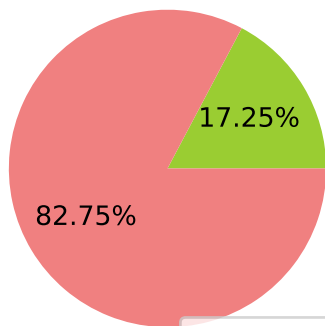


in

out

inout

puce 812

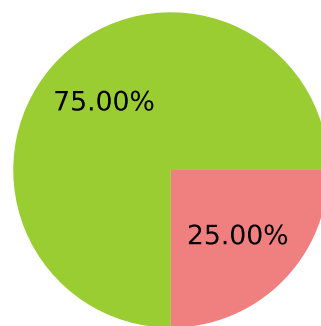
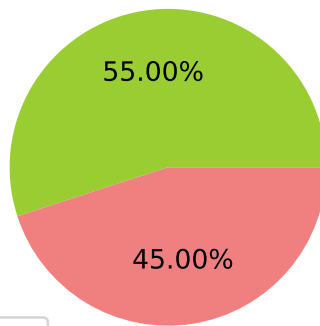
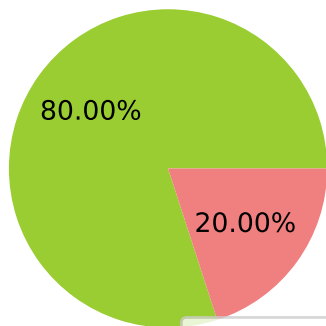


in

out

inout

puce 813



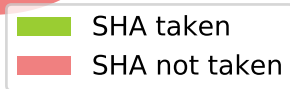
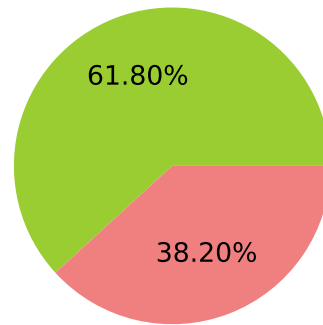
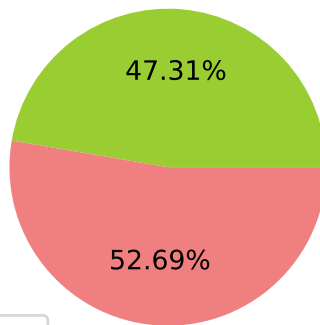
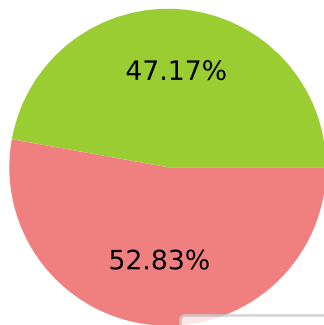
SHA taken
SHA not taken

in

out

inout

puce 814

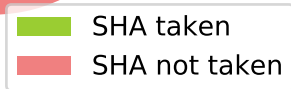
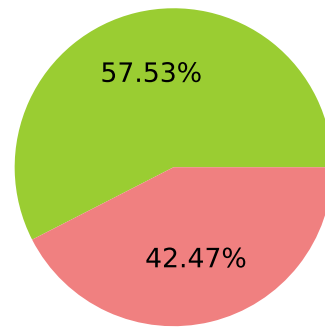
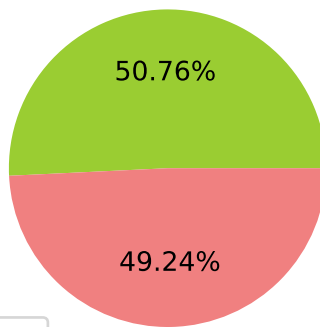
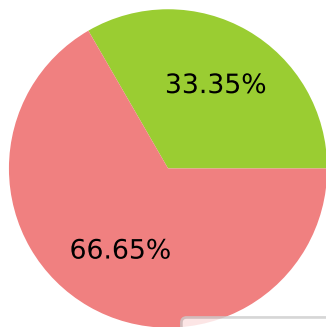


in

out

inout

puce 816

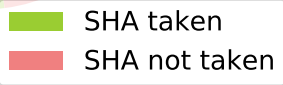
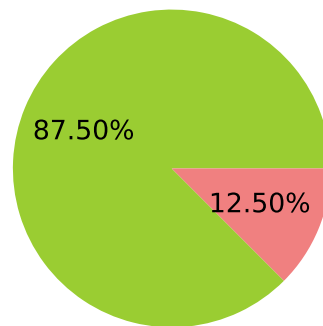
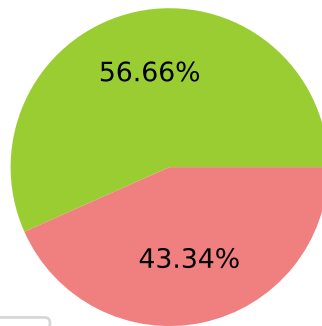
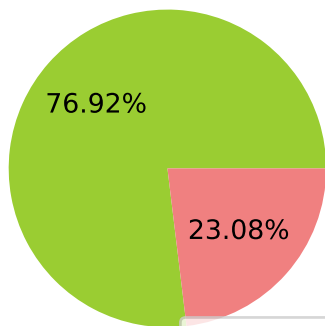


in

out

inout

puce 817

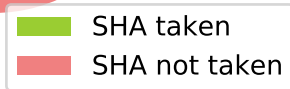
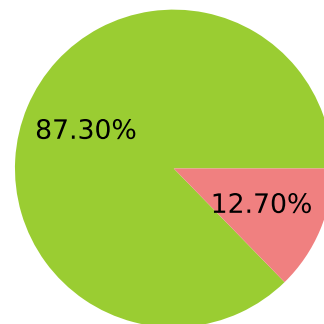
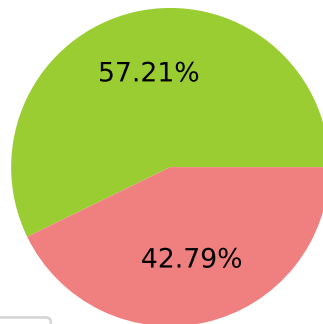
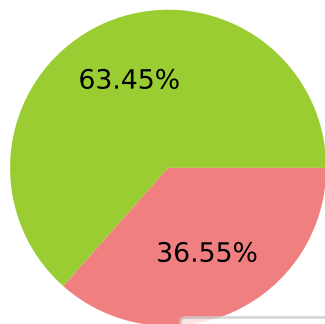


in

out

inout

puce 818

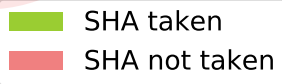
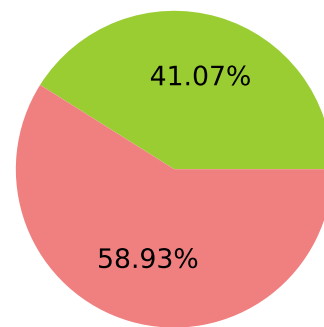
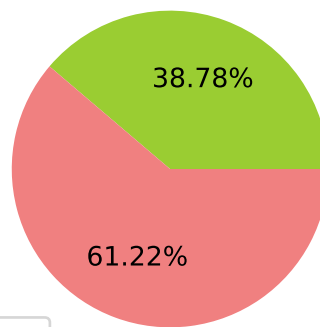
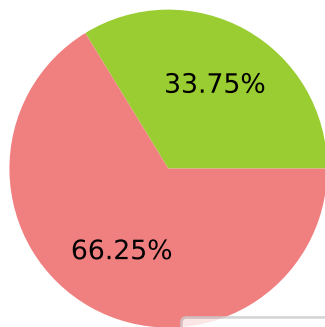


in

out

inout

puce 819

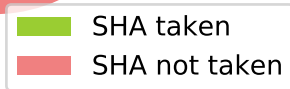
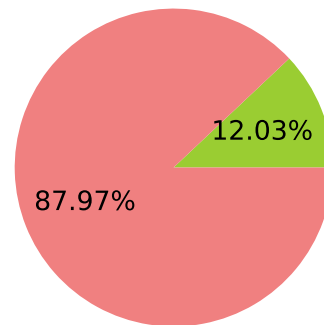
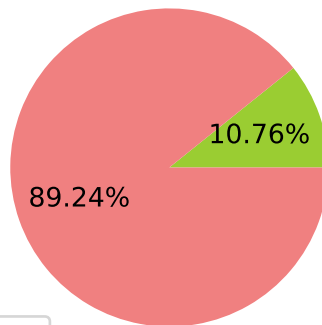
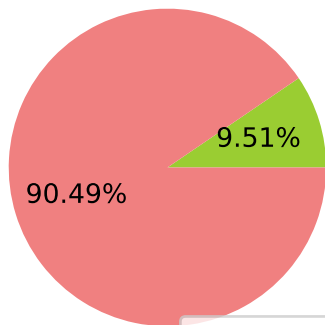


in

out

inout

puce 820

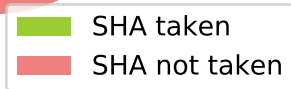
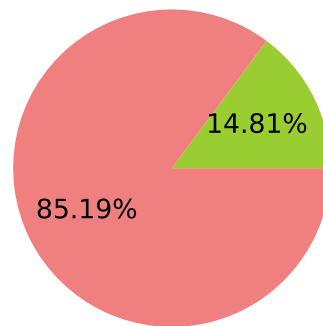
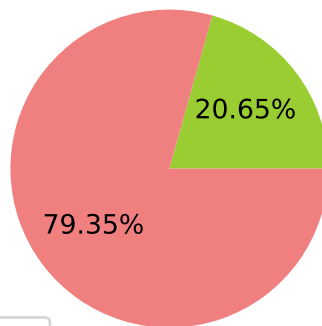
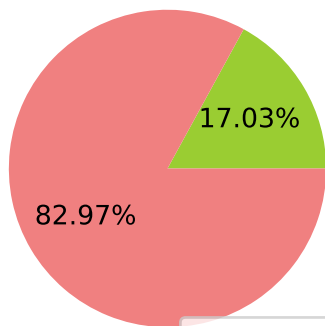


in

out

inout

puce 821

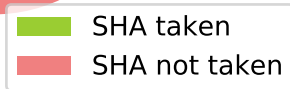
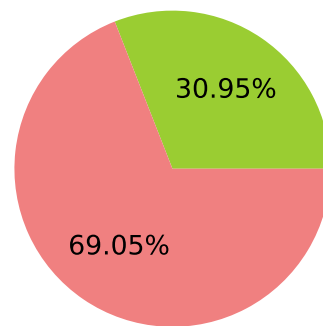
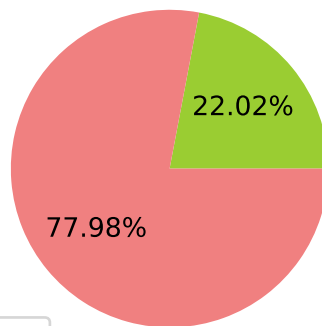
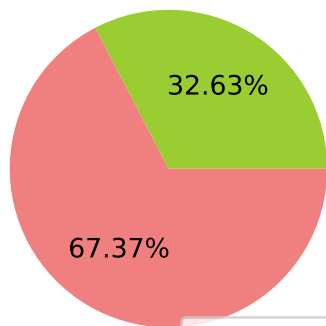


in

out

inout

puce 822

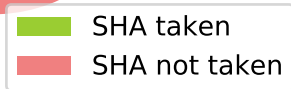
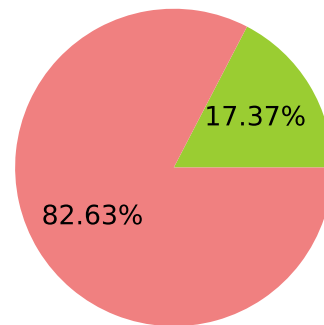
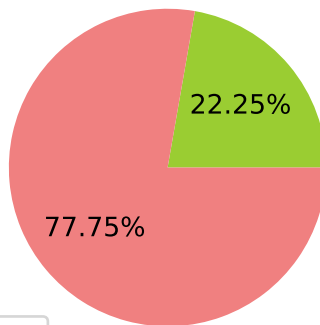
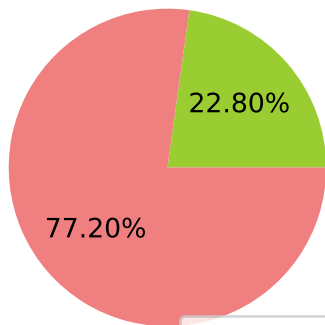


in

out

inout

puce 823

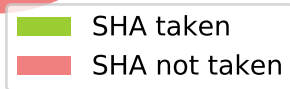
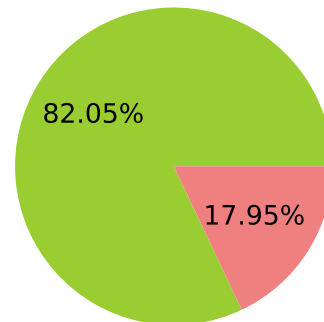
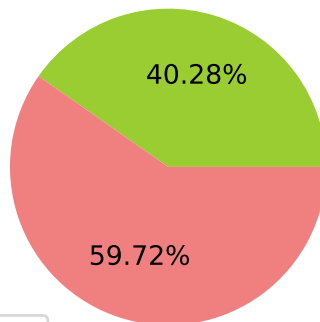
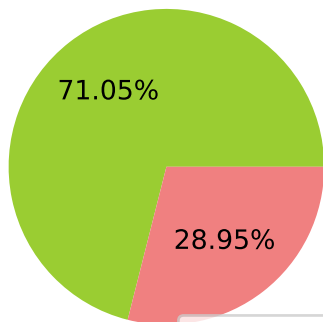


in

out

inout

puce 824

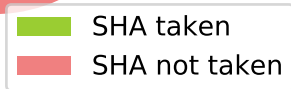
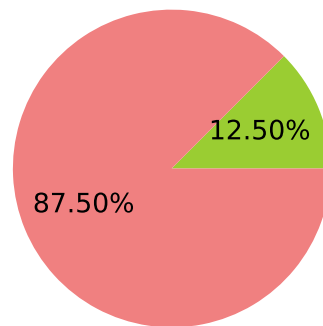
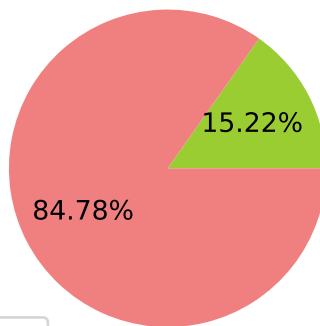
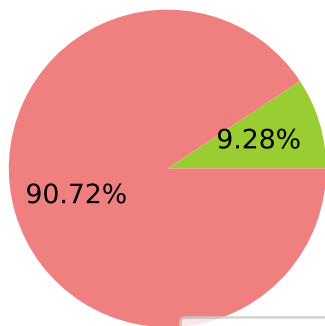


in

out

inout

puce 828

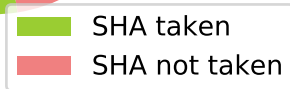
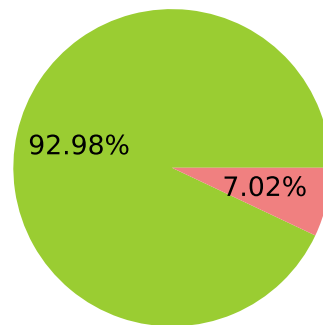
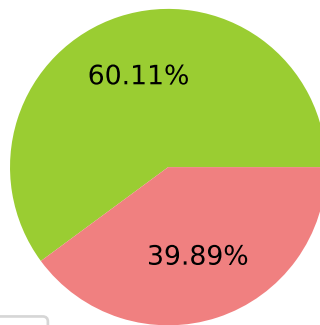
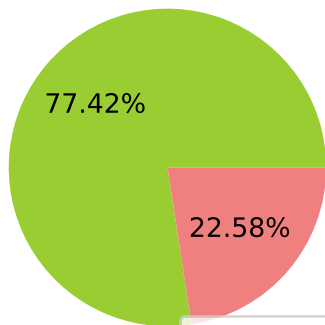


in

out

inout

puce 849

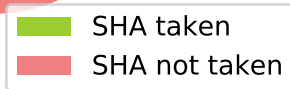
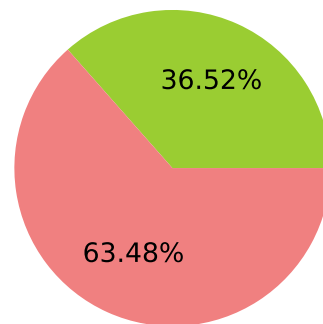
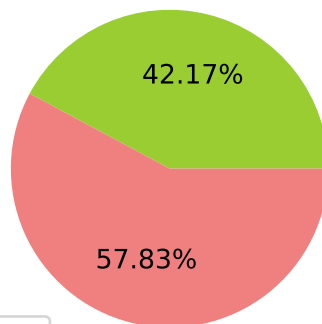
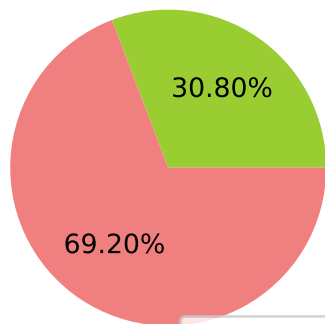


in

out

inout

puce 853

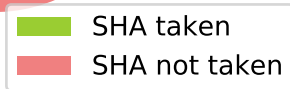
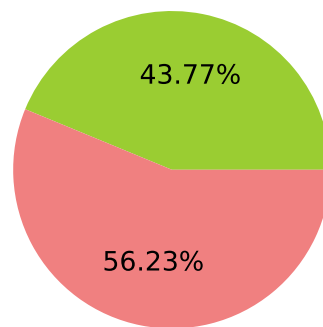
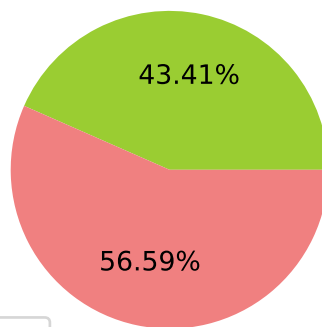
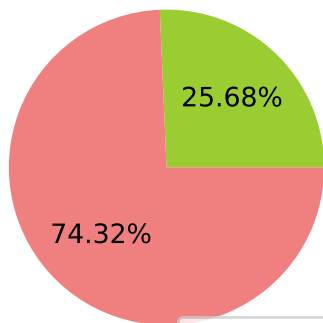


in

out

inout

puce 857

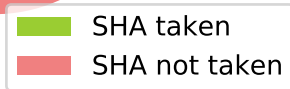
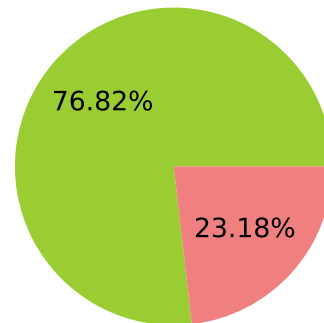
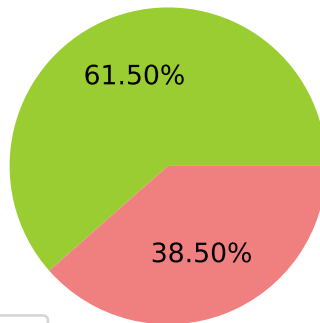
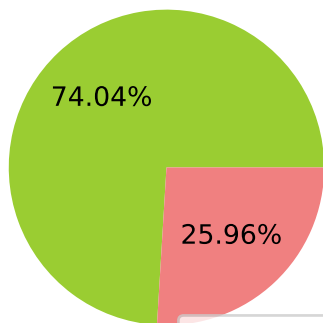


in

out

inout

puce 864

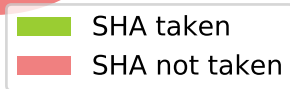
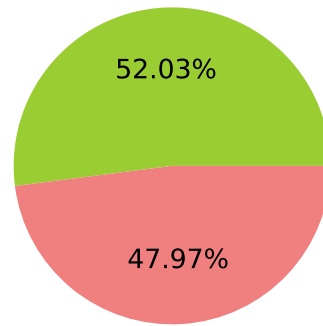
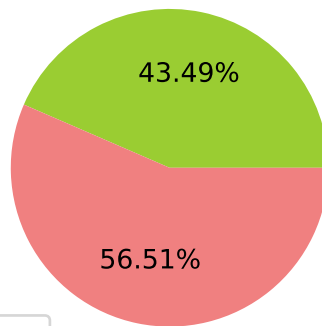
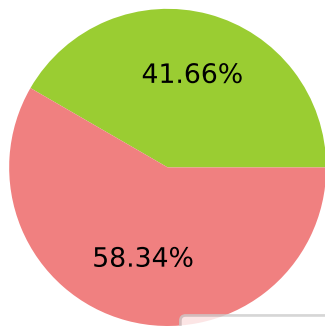


in

out

inout

puce 871

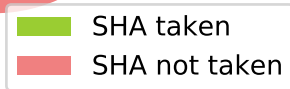
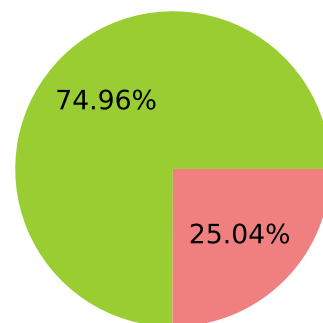
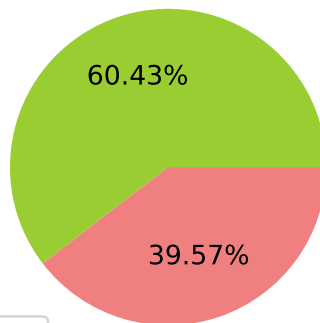
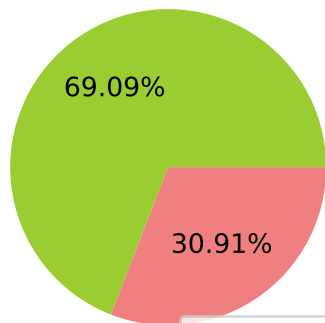


in

out

inout

puce 900

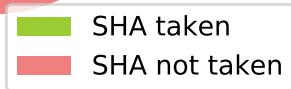
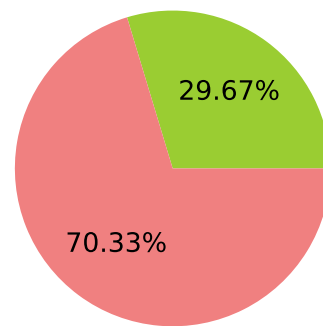
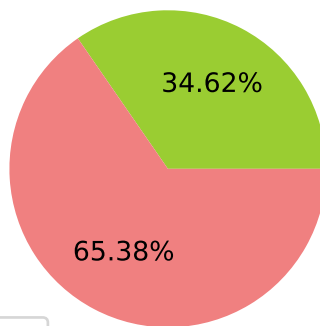
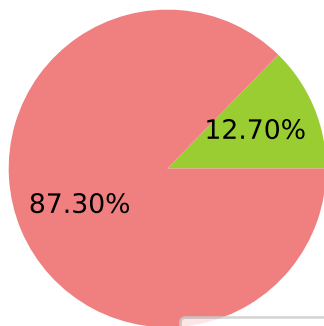


in

out

inout

puce 902

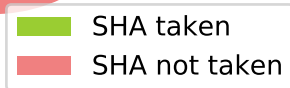
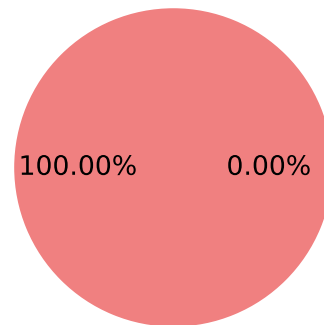
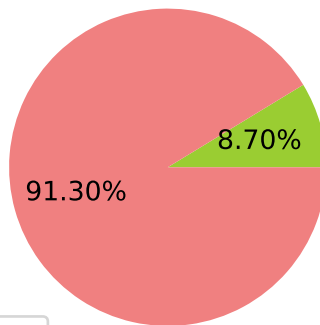
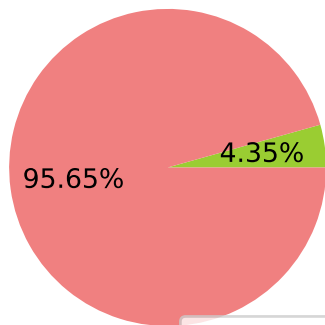


in

out

inout

puce 903

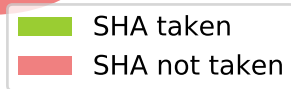
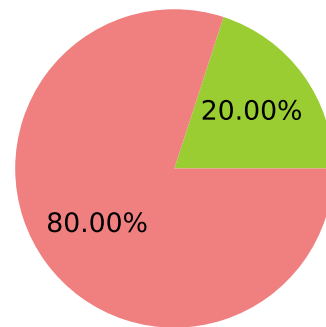
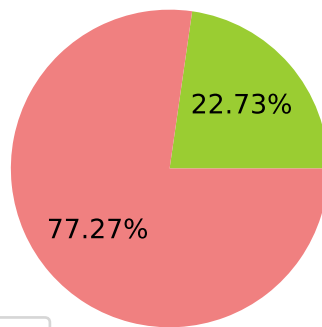
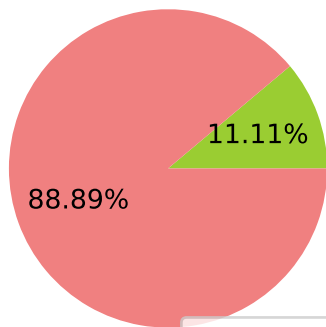


in

out

inout

puce 904

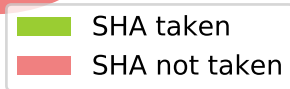
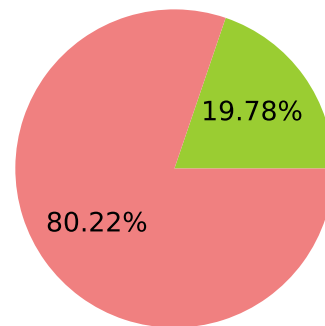
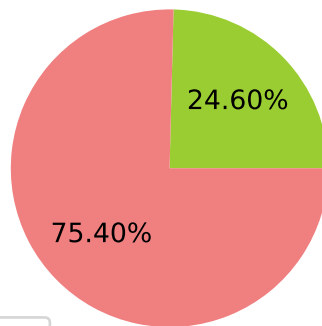
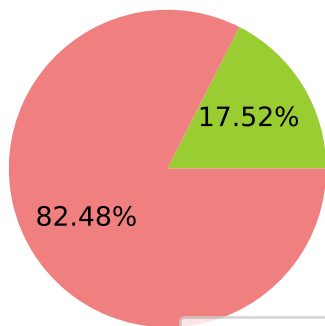


in

out

inout

puce 905

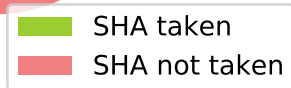
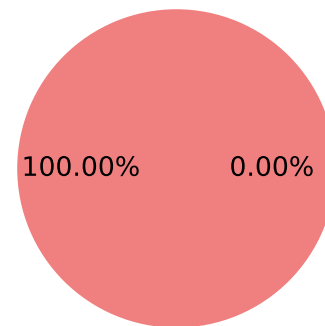
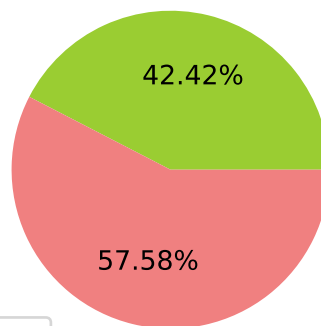
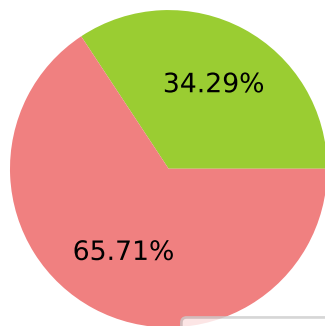


in

out

inout

puce 906

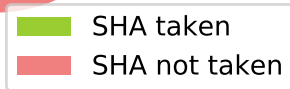
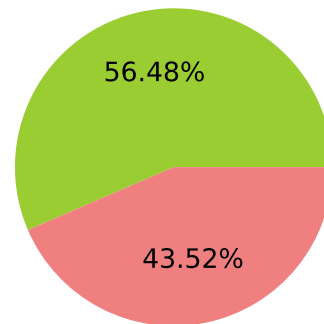
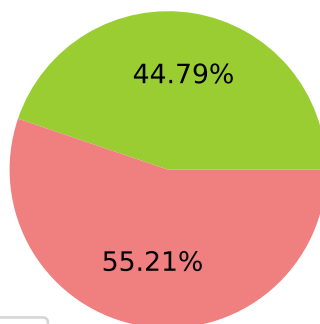
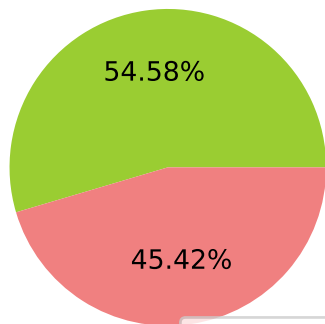


in

out

inout

puce 907

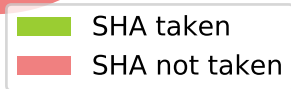
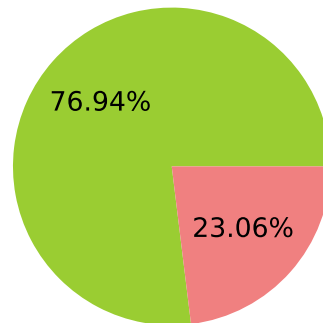
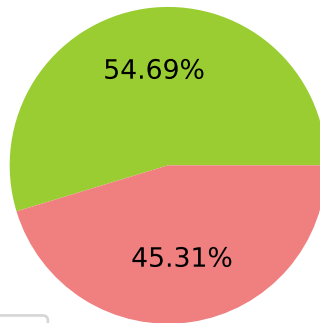
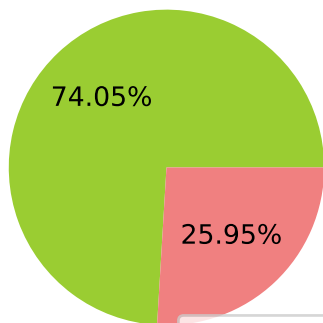


in

out

inout

puce 908

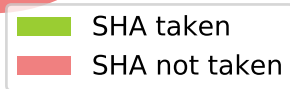
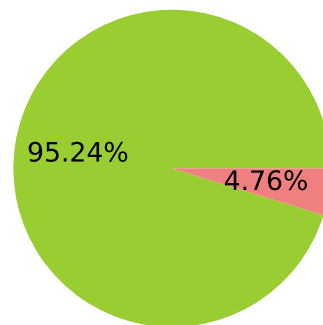
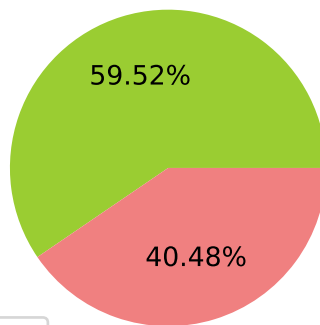
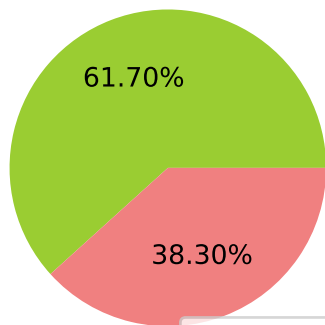


in

out

inout

puce 909

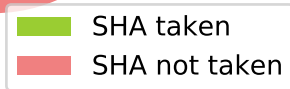
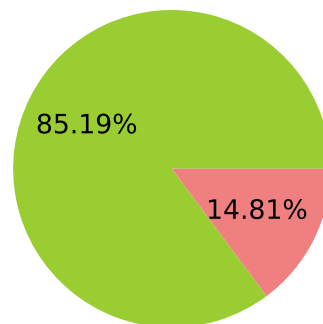
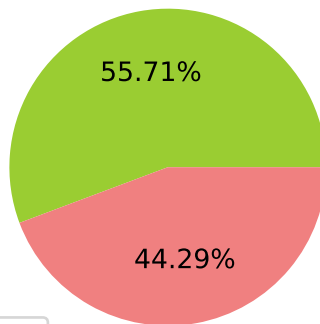
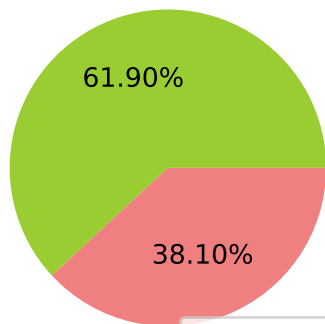


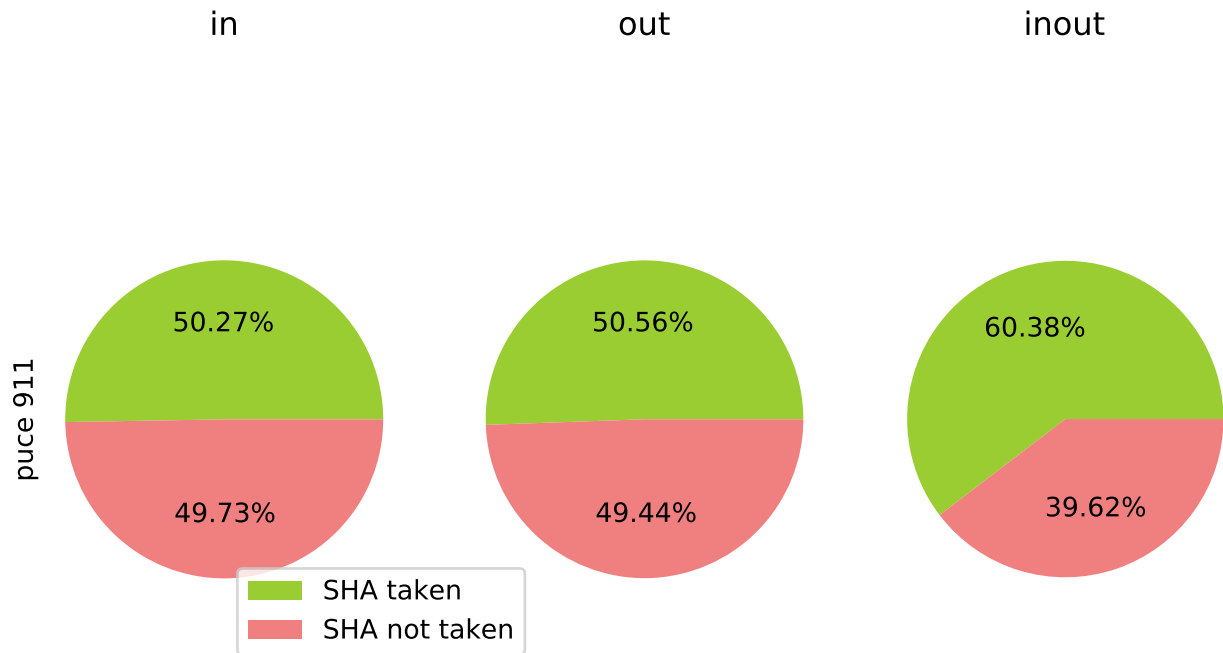
in

out

inout

puce 910



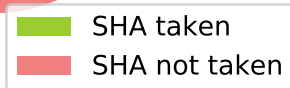
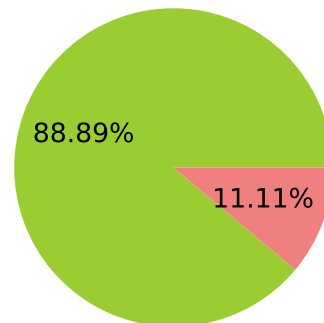
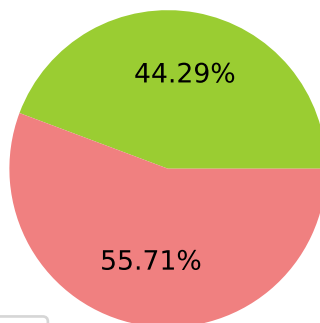
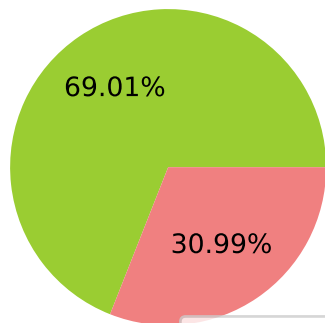


in

out

inout

puce 912

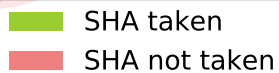
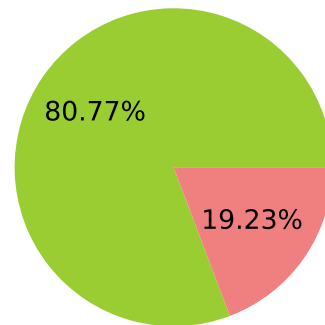
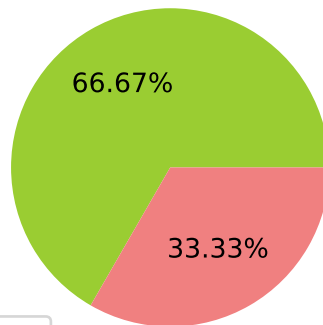
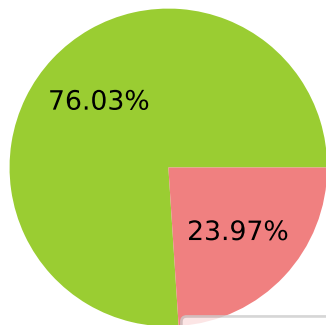


in

out

inout

puce 913

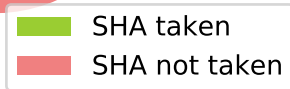
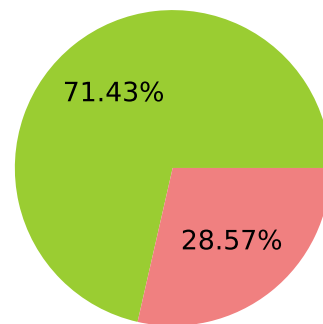
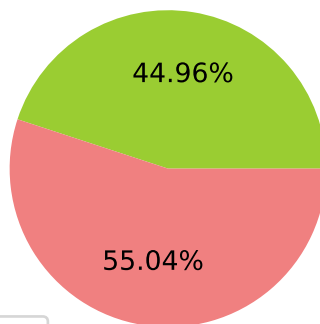
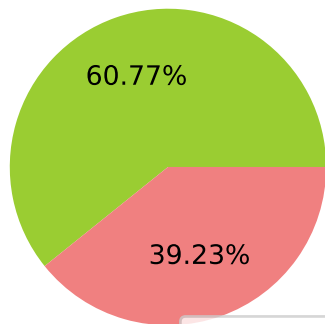


in

out

inout

puce 914

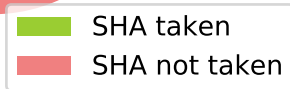
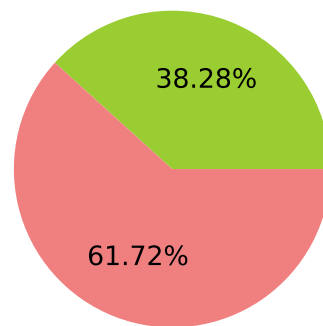
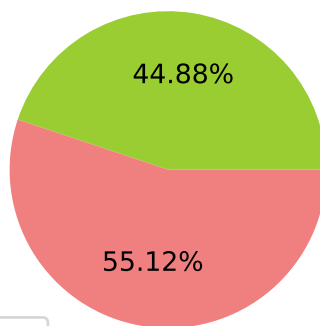
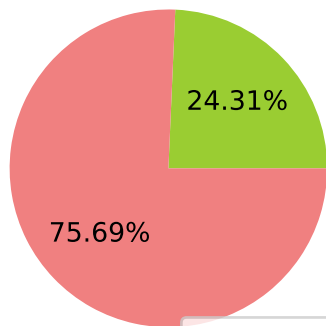


in

out

inout

puce 915

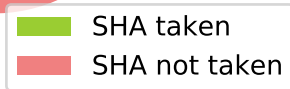
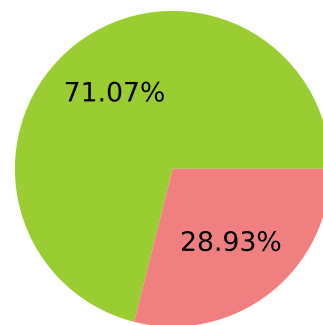
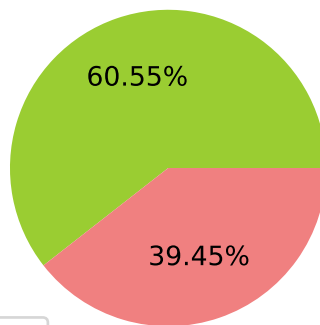
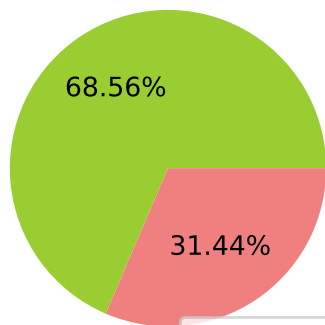


in

out

inout

puce 917

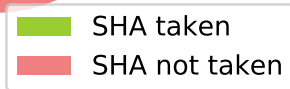
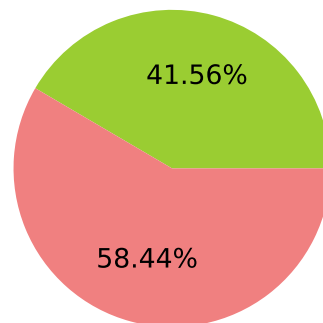
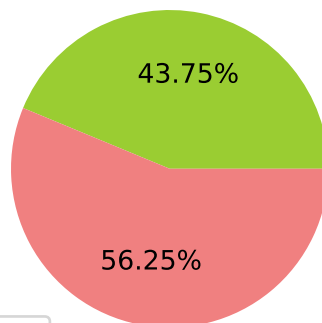
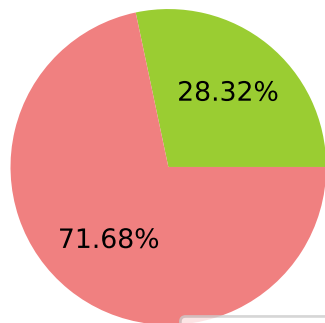


in

out

inout

puce 918

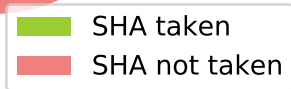
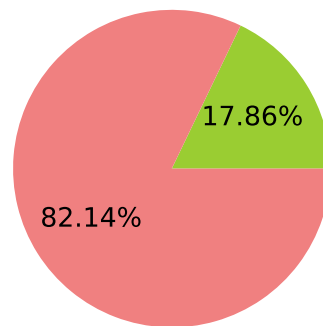
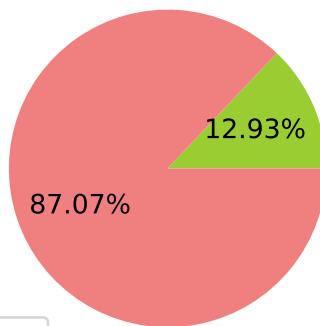
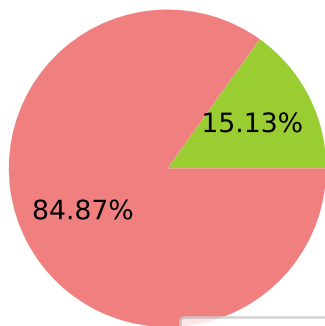


in

out

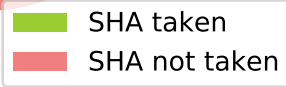
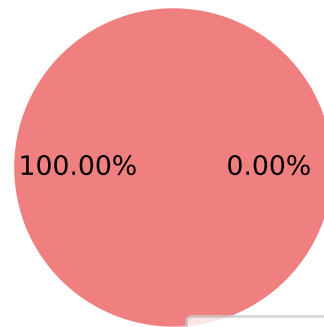
inout

puce 919



inout

puce 920

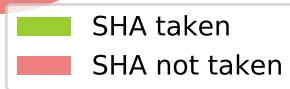
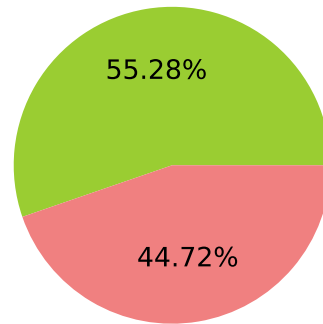
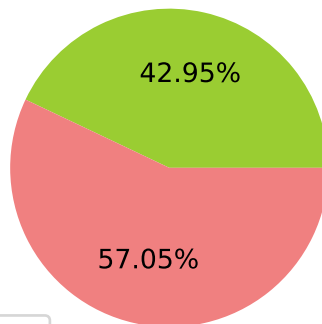
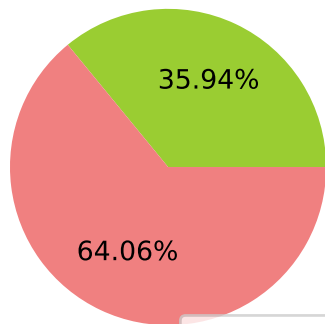


in

out

inout

puce 921

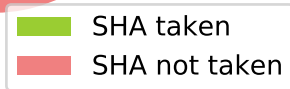
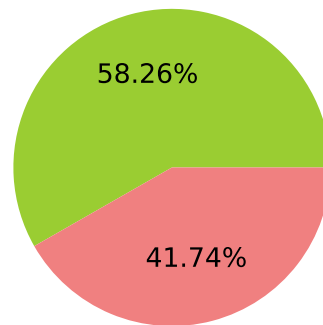
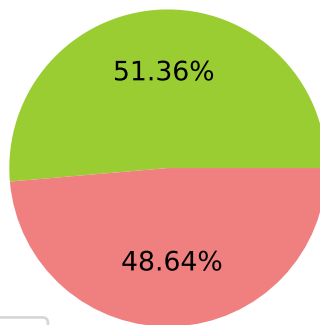
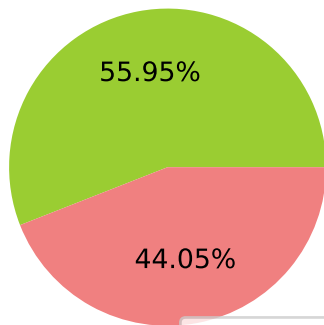


in

out

inout

puce 922

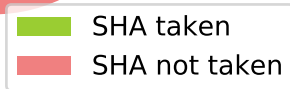
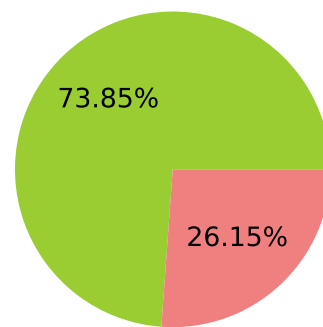
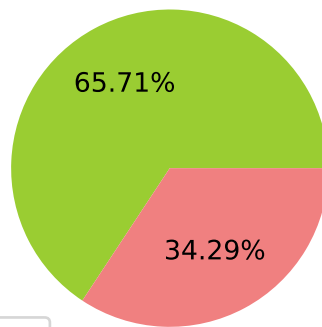
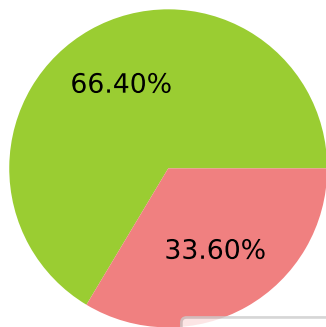


in

out

inout

puce 924

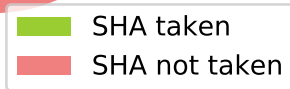
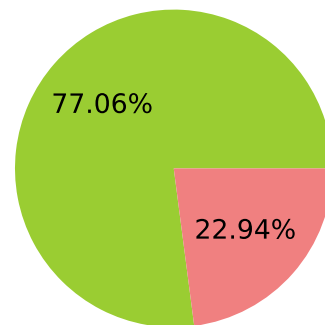
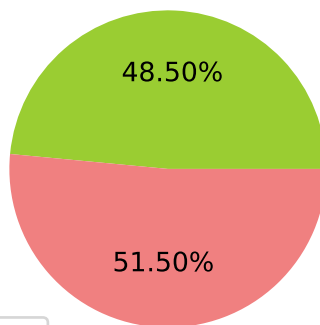
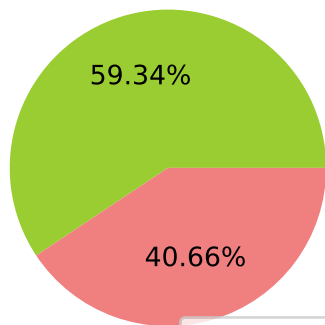


in

out

inout

puce 925

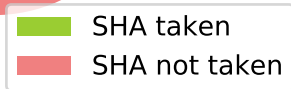
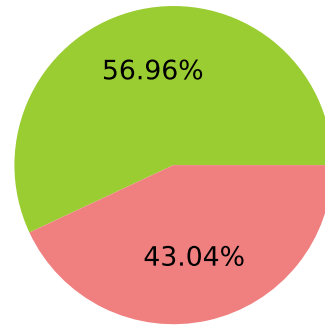
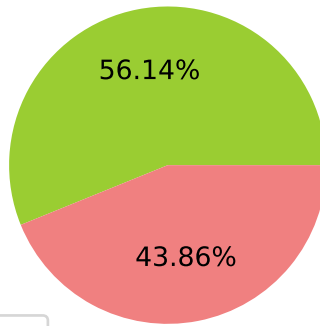
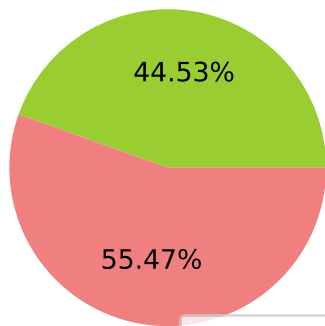


in

out

inout

puce 926

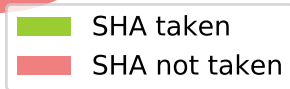
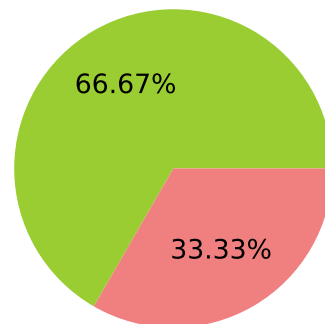
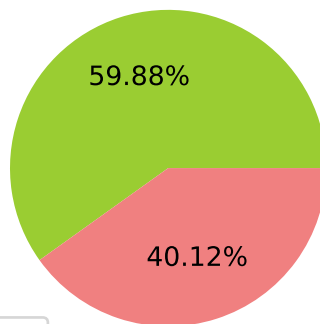
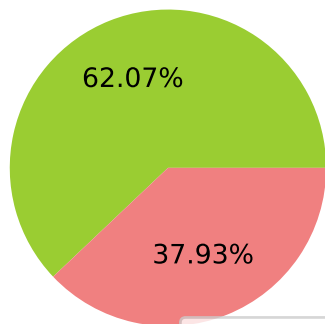


in

out

inout

puce 927

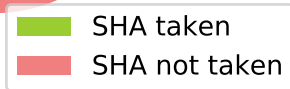
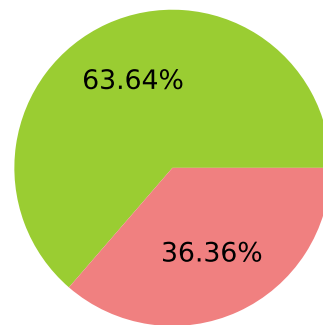
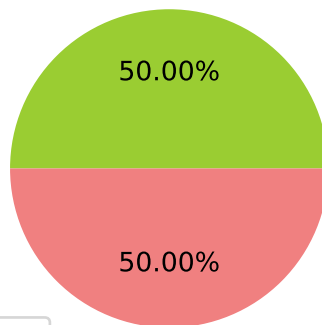
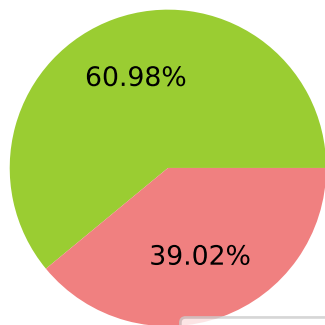


in

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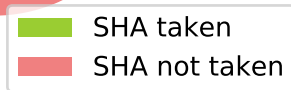
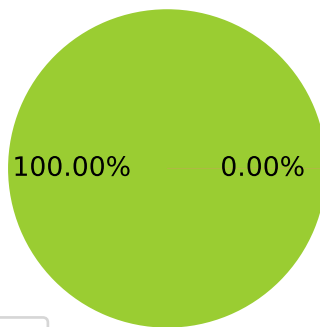
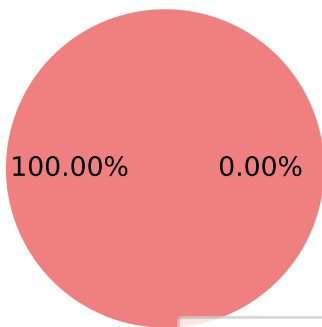
puce 929



in

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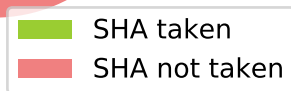
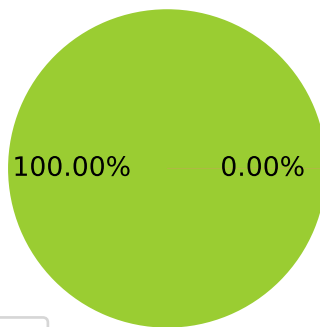
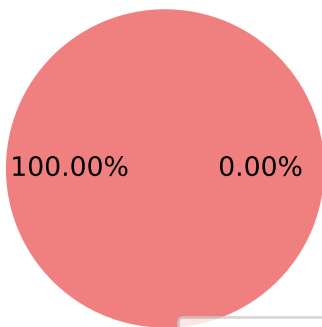
puce 932



in

out

puce 936

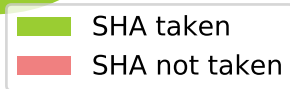
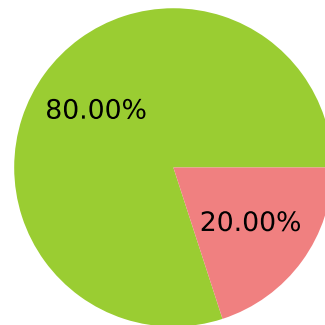
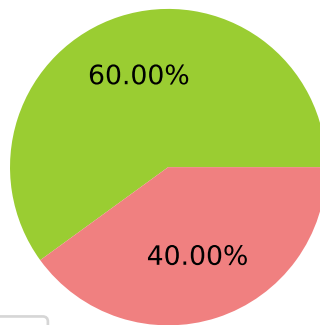
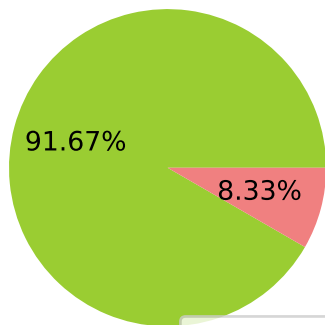


in

out

inout

puce 938

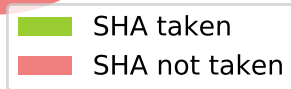
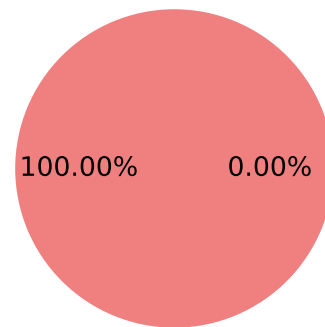
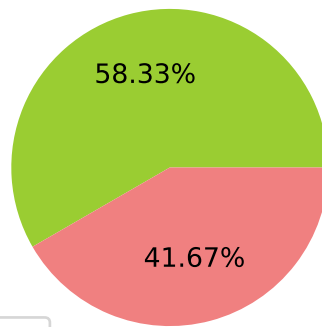
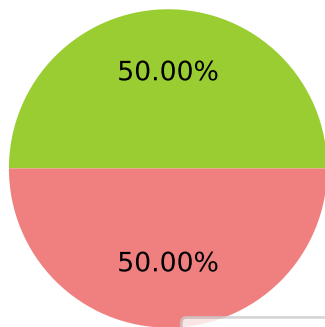


in

out

inout

puce 940

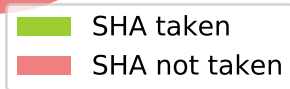
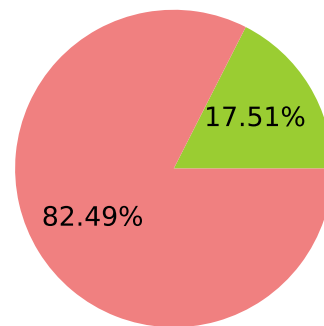
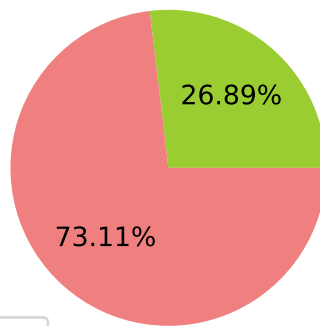
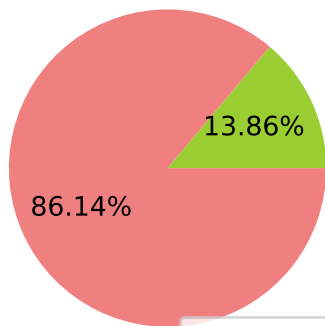


in

out

inout

puce 945

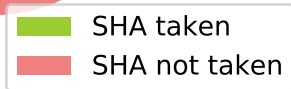
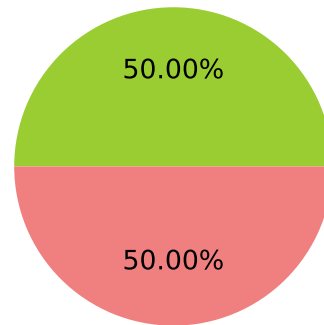
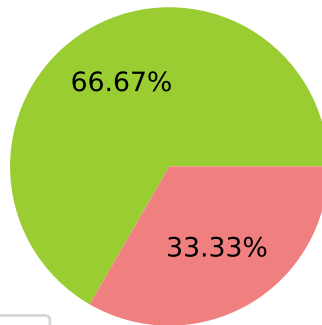
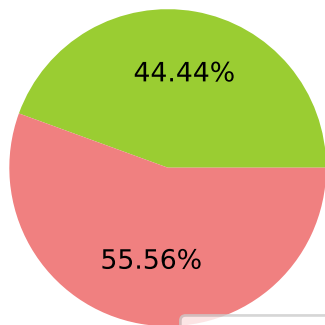


in

out

inout

puce 953

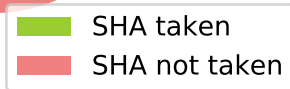
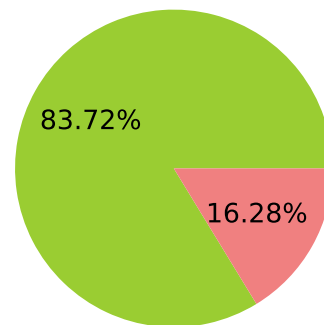
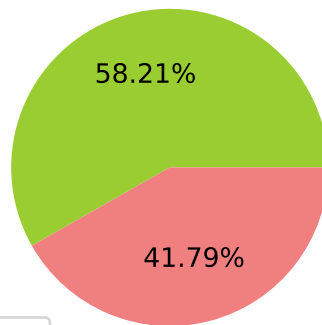
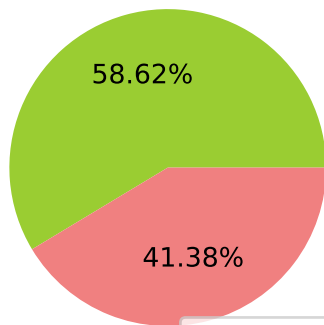


in

out

inout

puce 954

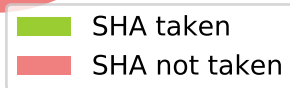
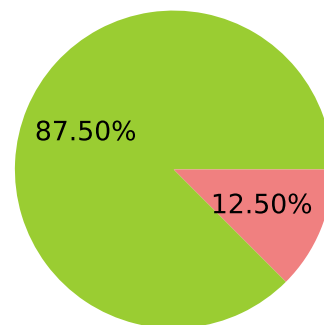
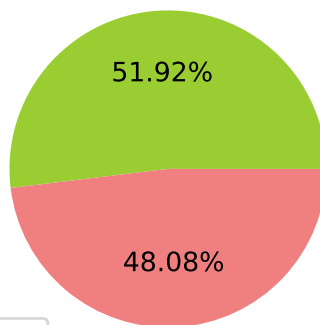
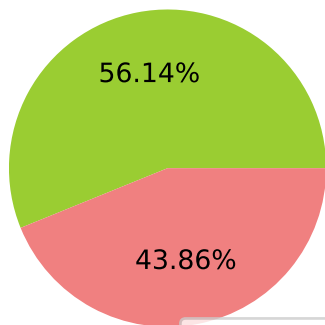


in

out

inout

puce 955

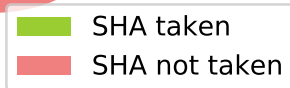
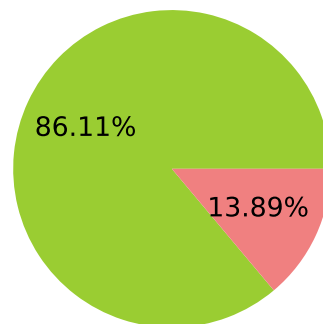
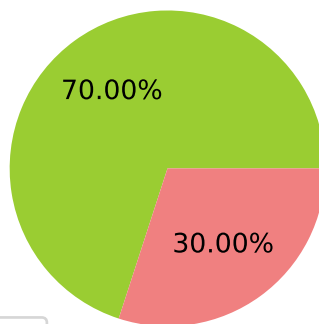
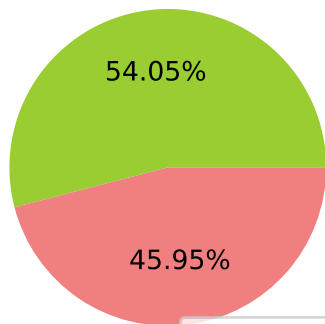


in

out

inout

puce 956

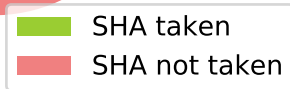
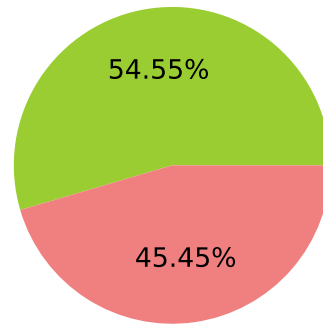
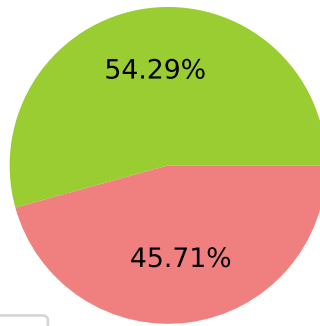
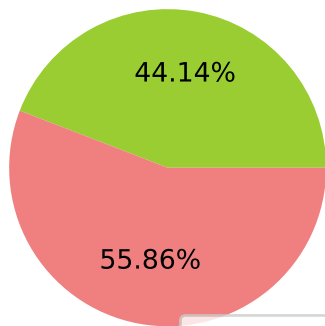


in

out

inout

puce 957

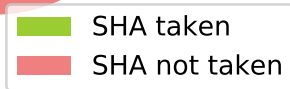
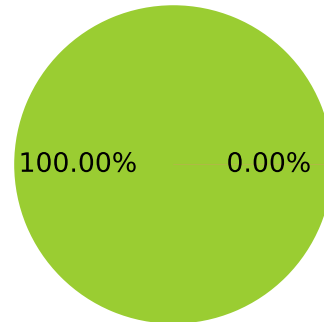
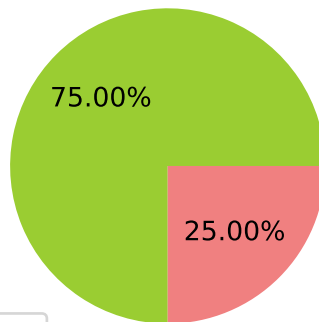
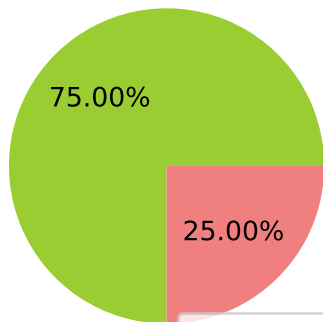


in

out

inout

puce 958

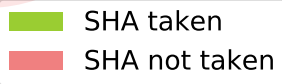
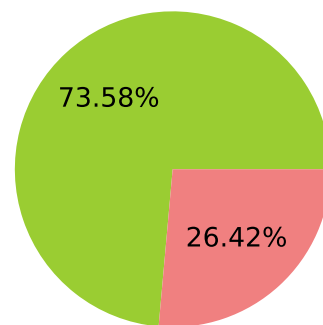
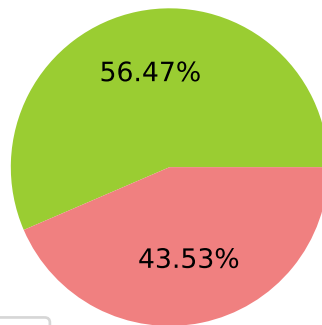
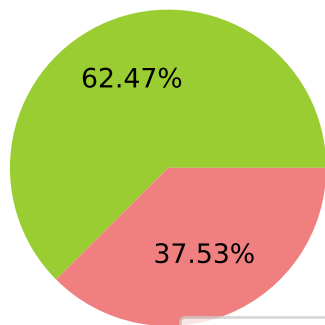


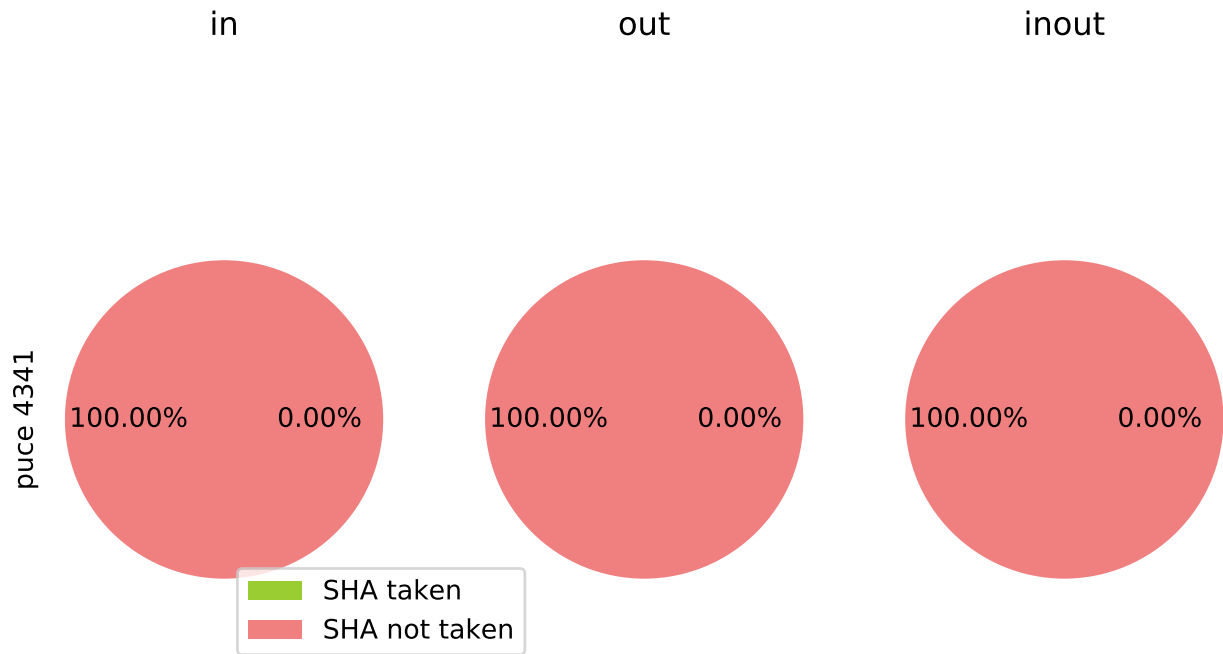
in

out

inout

puce 2379



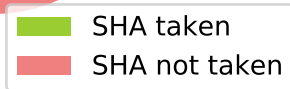
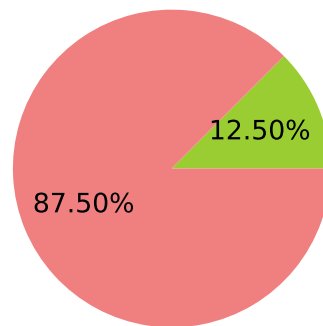
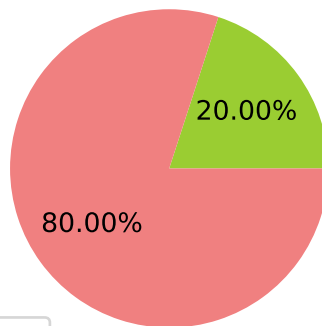
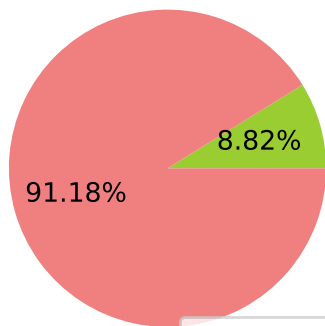


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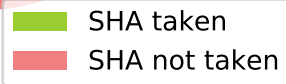
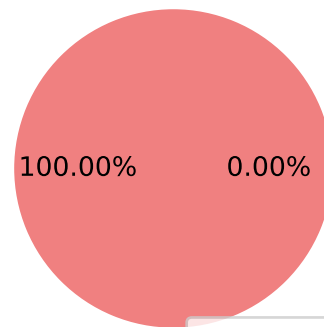
inout

puce 16384



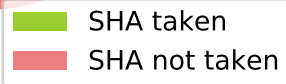
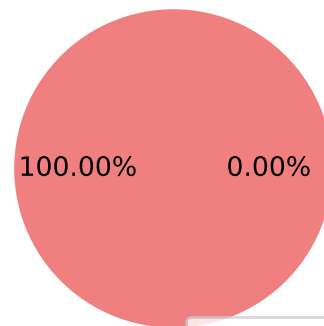
inout

puce 16640



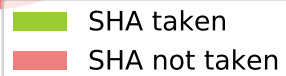
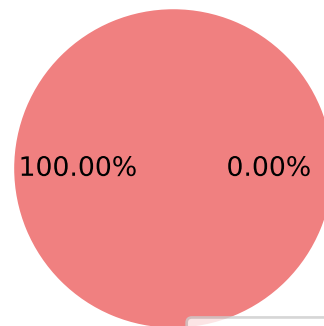
inout

puce 16896



inout

puce 20480



in

out

inout

puce 30004

