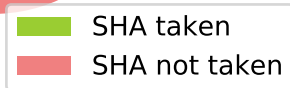
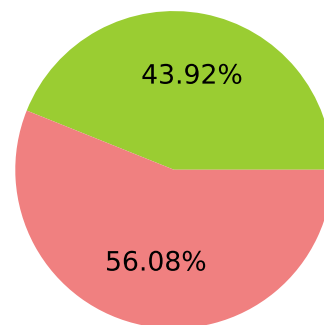
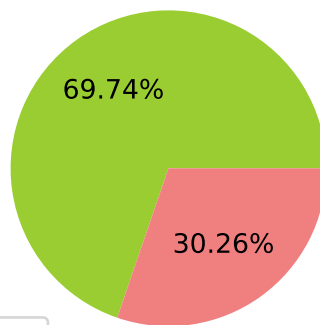
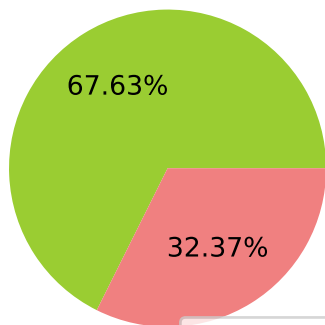


in

out

inout

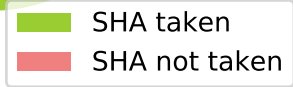
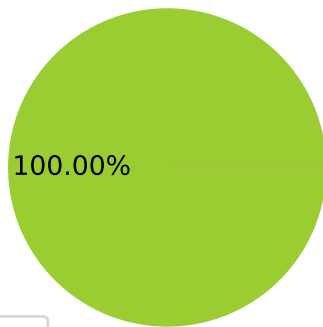
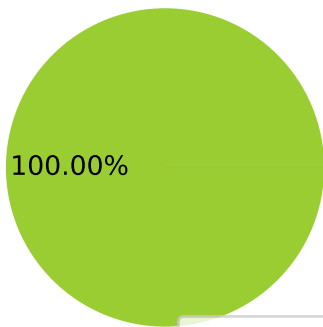
puce 0



in

out

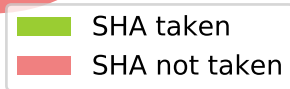
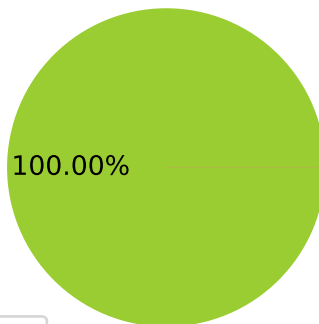
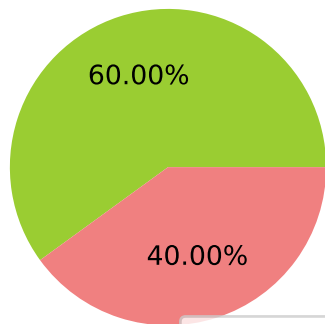
puce 5



in

out

puce 402

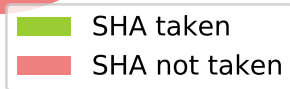
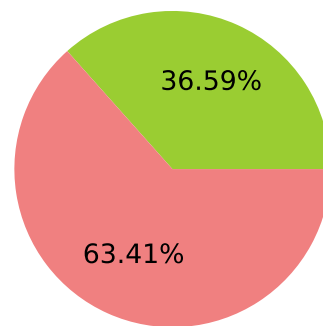
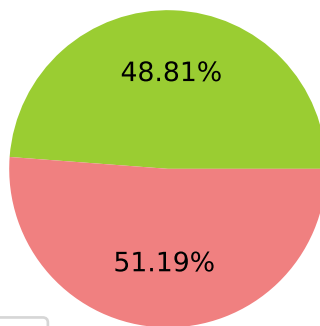
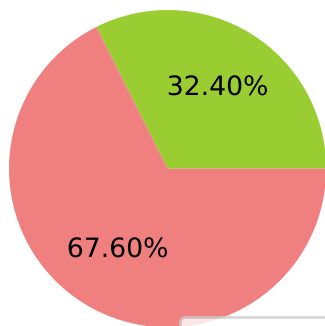


in

out

inout

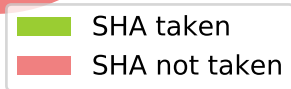
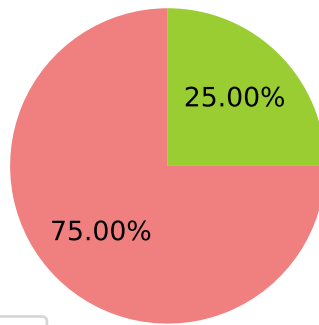
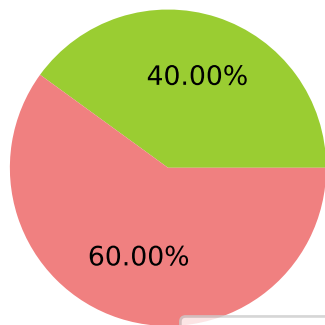
puce 404



in

out

puc 412

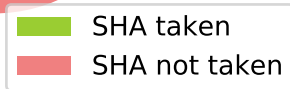
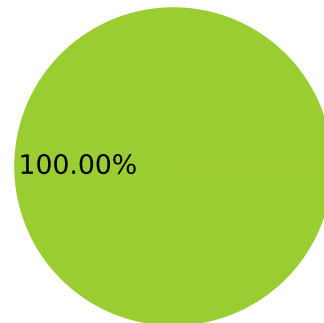
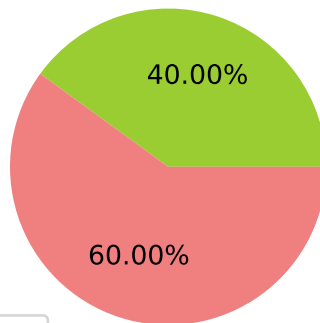
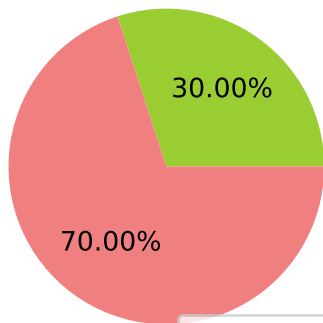


in

out

inout

puce 425

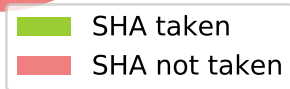
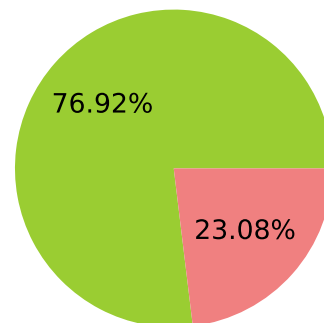
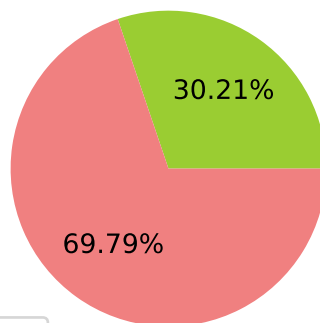
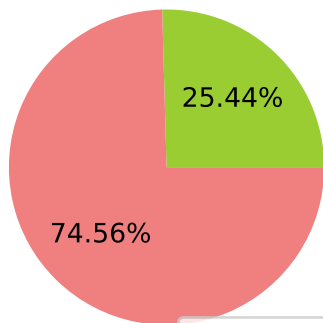


in

out

inout

puce 435

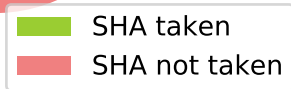
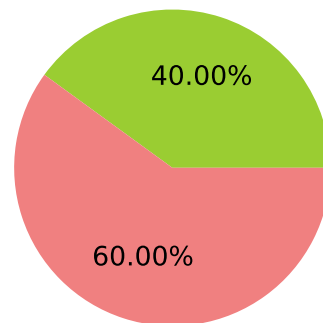
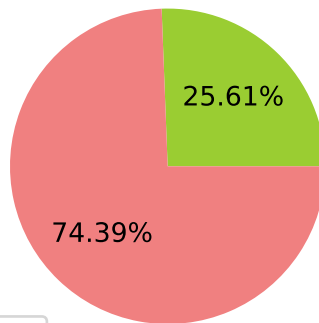
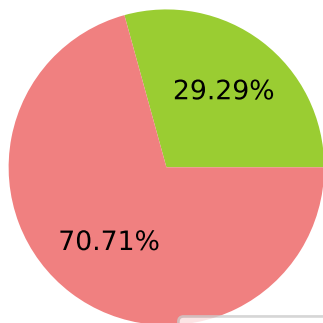


in

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inout

puce 446

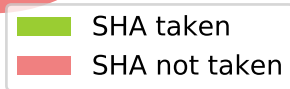
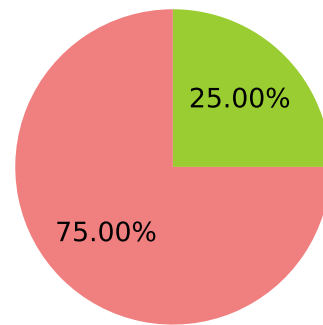
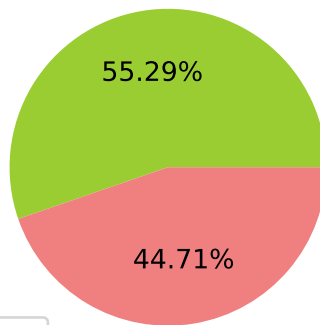
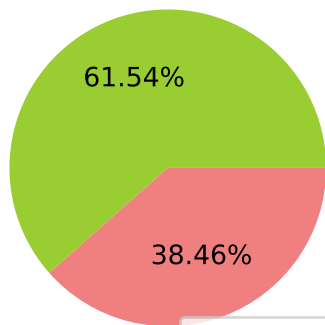


in

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inout

puce 501

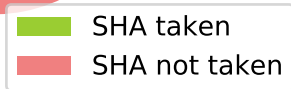
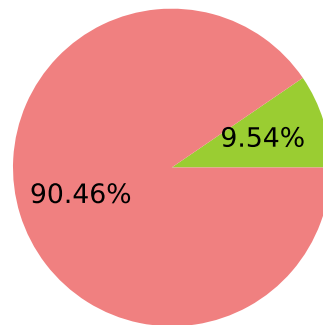
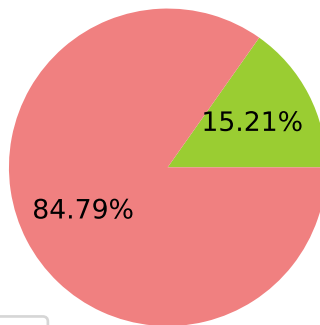
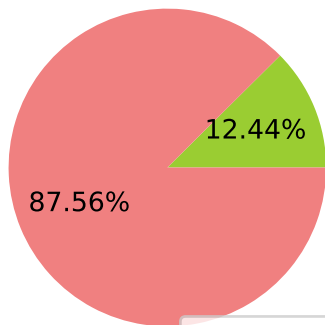


in

out

inout

puce 530

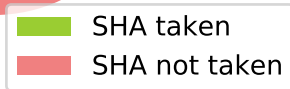
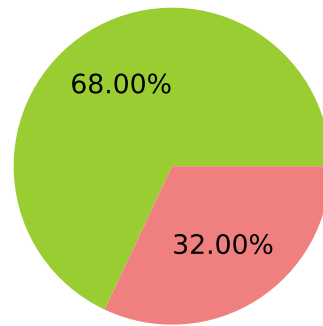
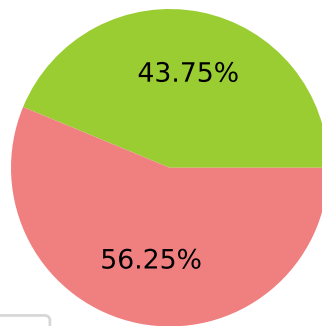
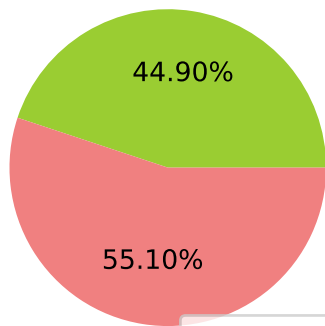


in

out

inout

puce 534

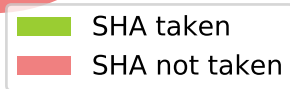
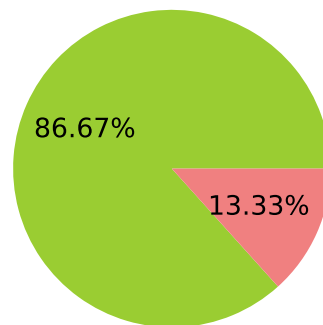
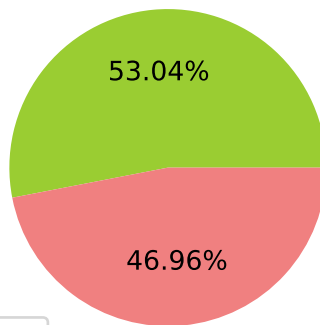
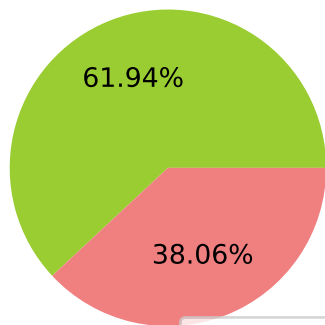


in

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inout

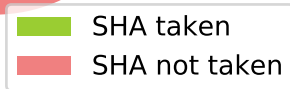
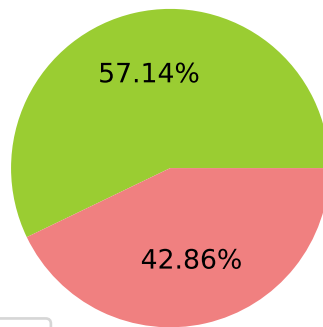
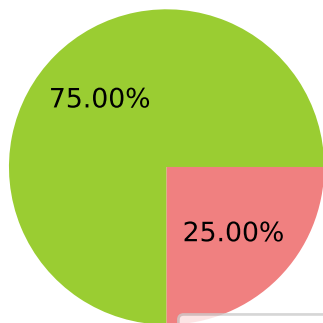
puce 581



in

out

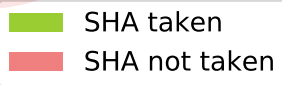
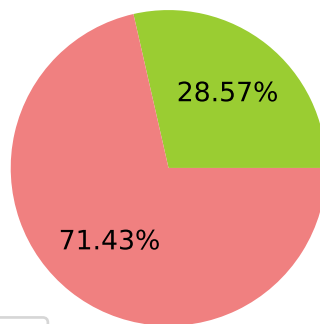
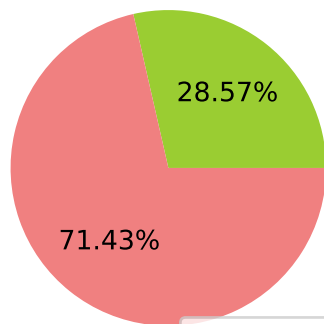
puce 598

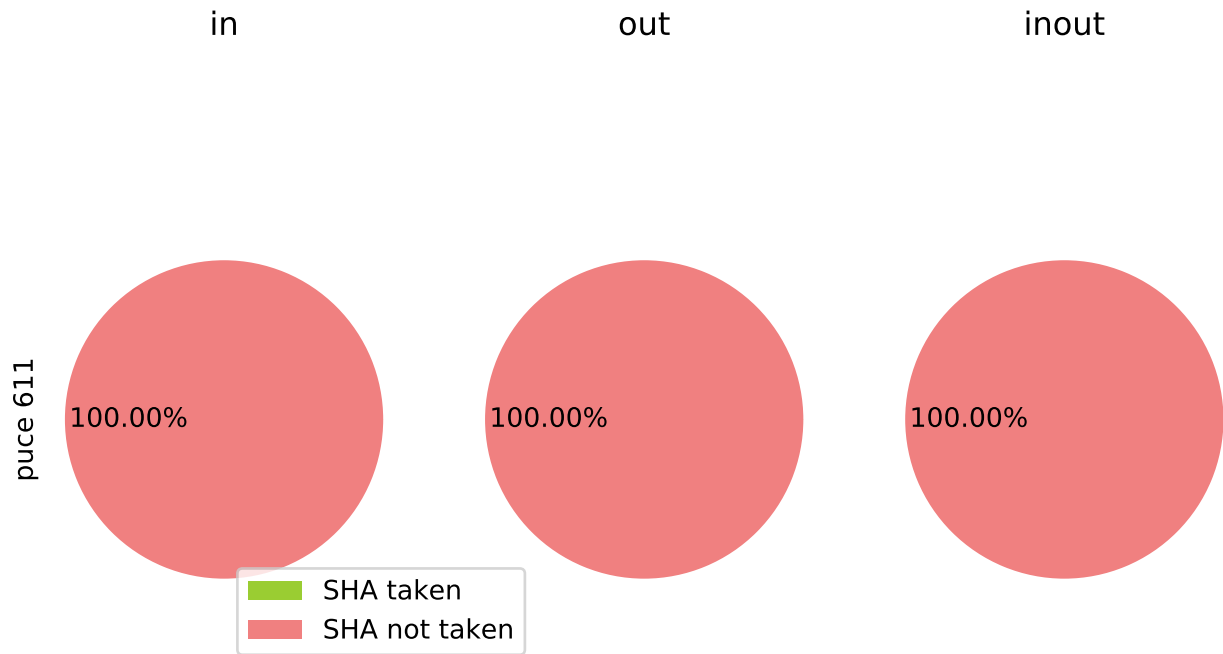


in

out

puce 605



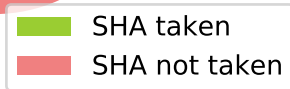
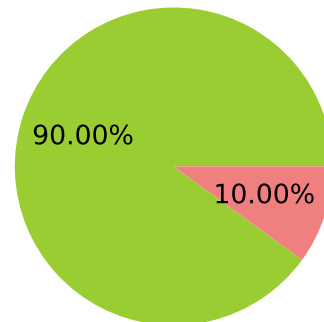
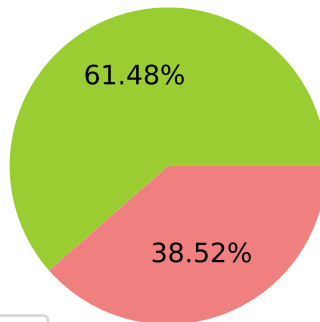
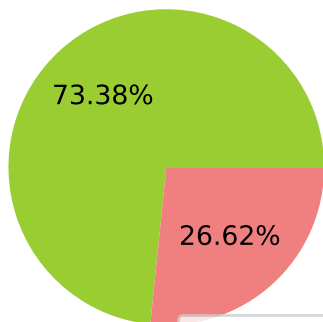


in

out

inout

puce 620

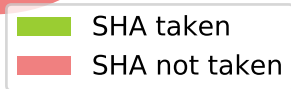
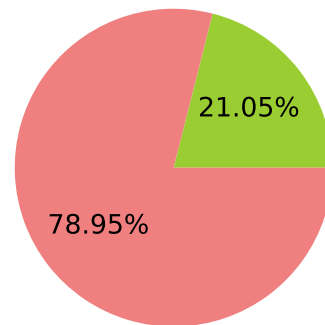
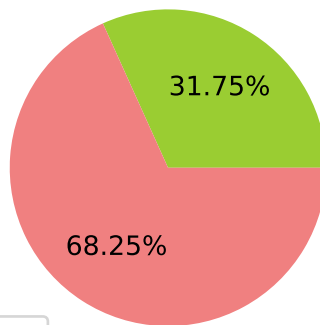
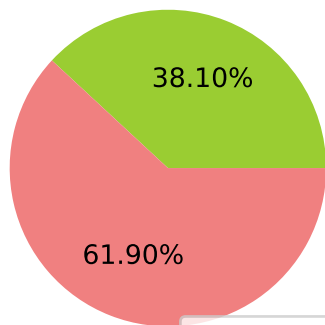


in

out

inout

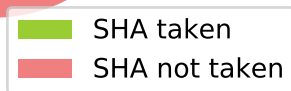
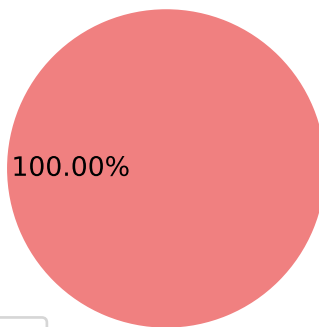
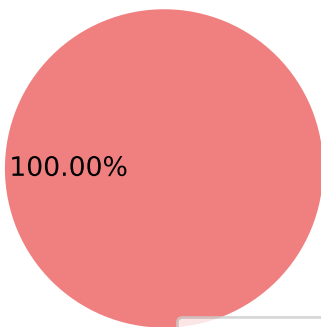
puce 685



in

out

puce 688



in

out

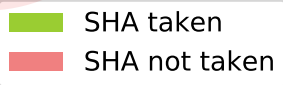
inout

puce 694

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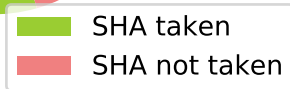
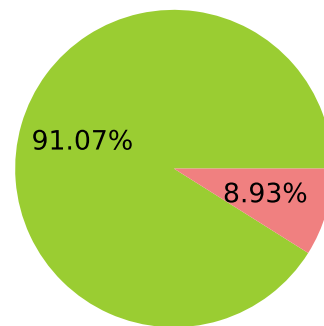
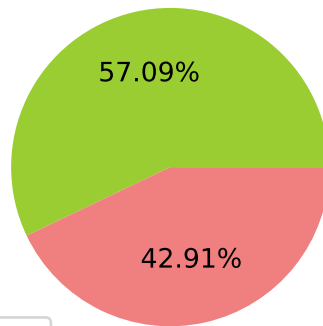
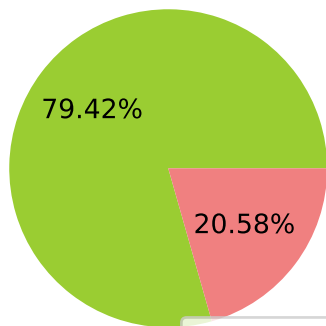


in

out

inout

puce 697

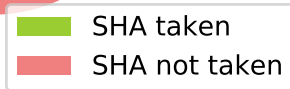
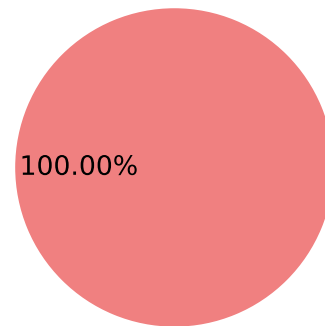
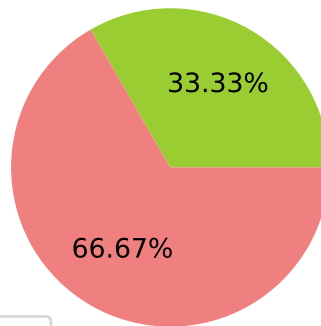
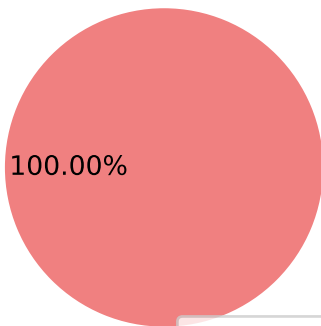


in

out

inout

puce 698

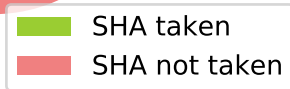
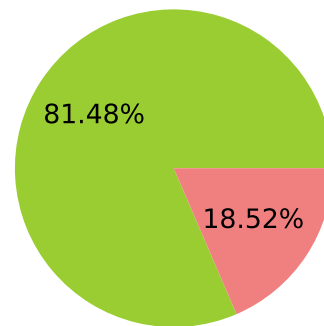
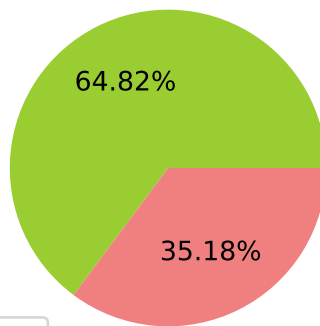
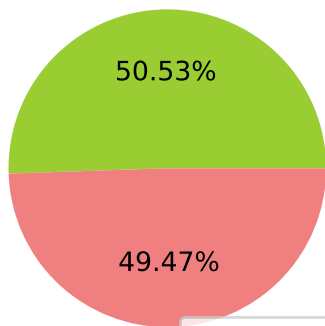


in

out

inout

puce 699

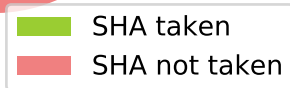
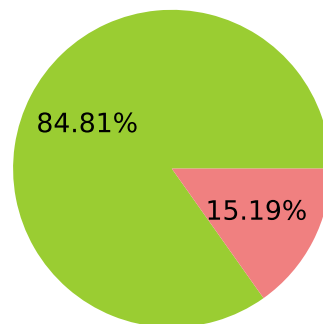
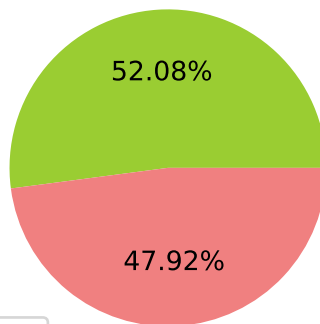
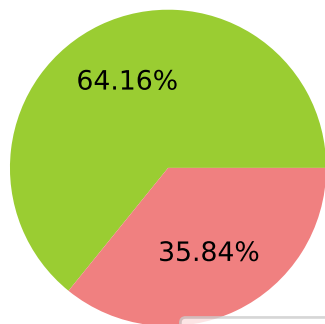


in

out

inout

puce 702

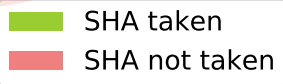
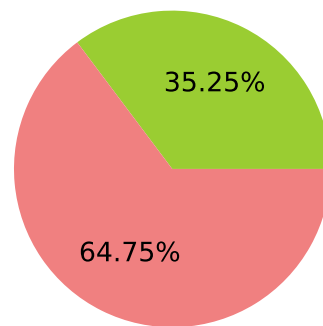
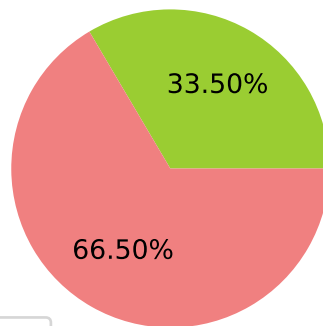
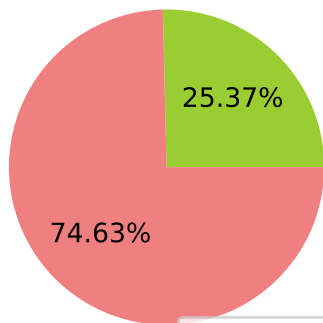


in

out

inout

puce 708

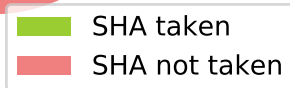
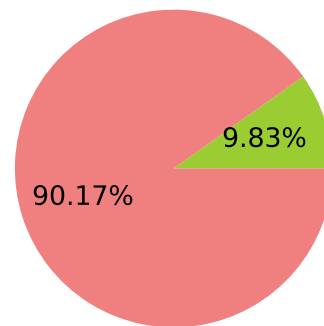
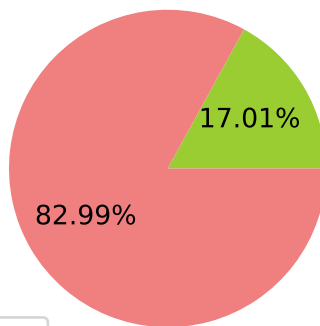
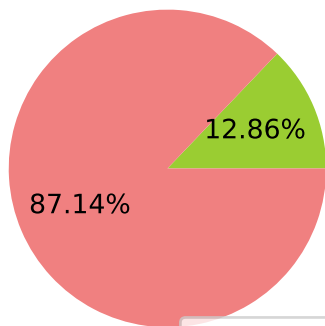


in

out

inout

puce 712

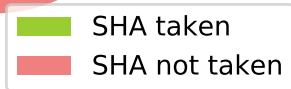
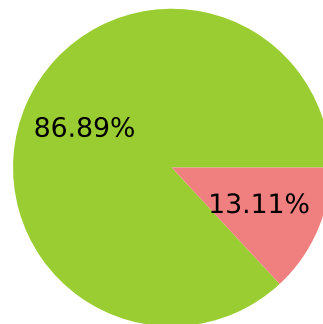
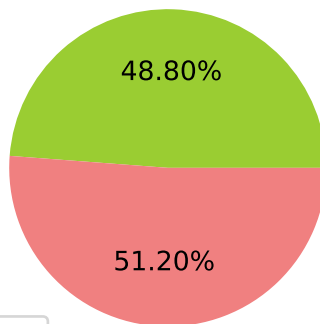
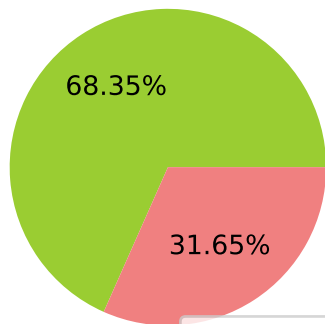


in

out

inout

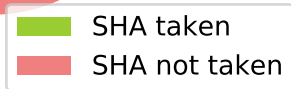
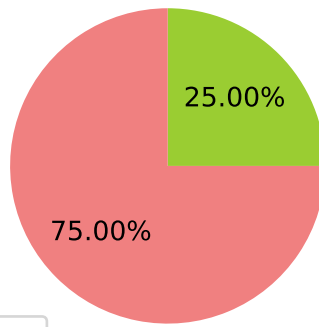
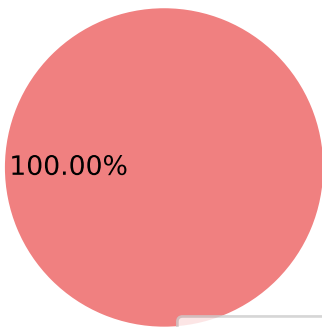
puce 714



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puce 716

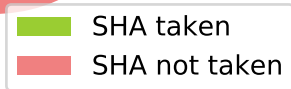
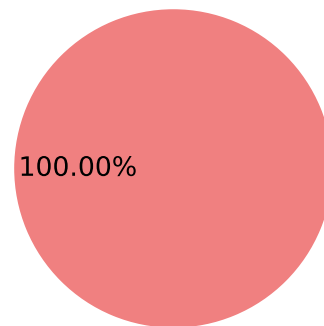
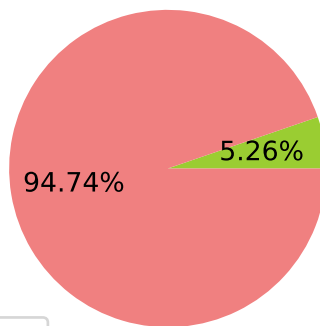
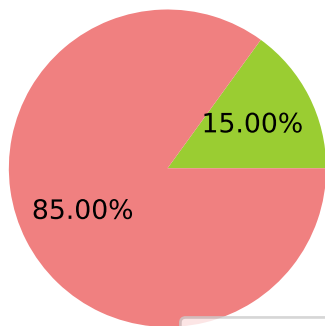


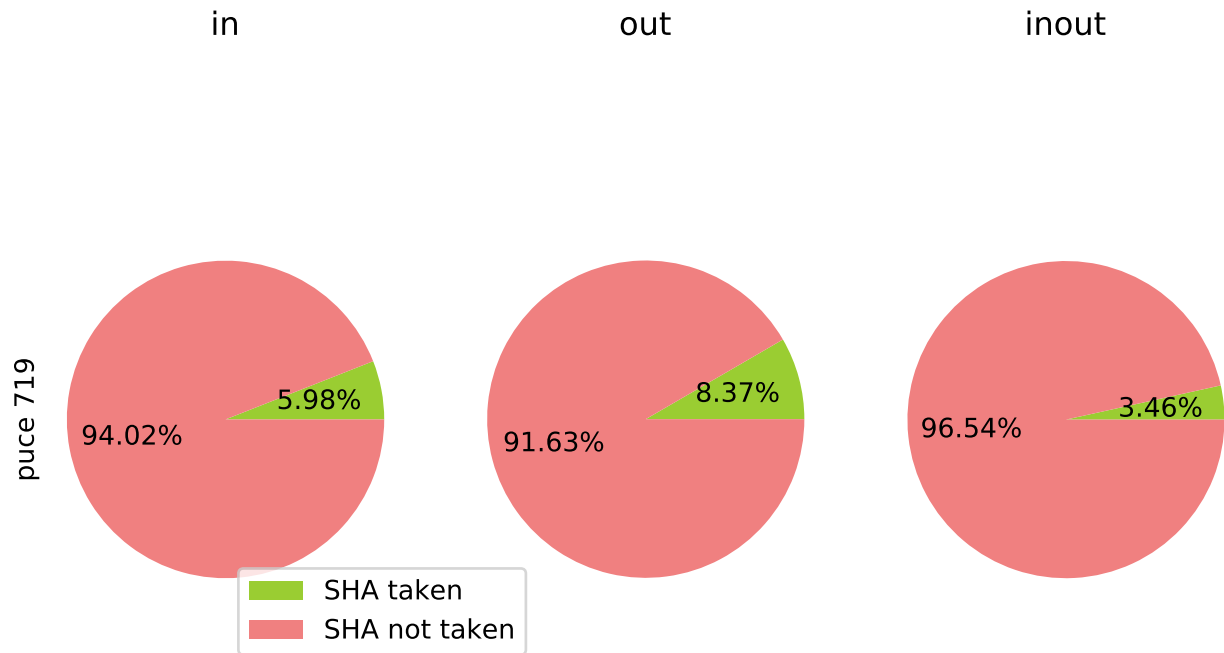
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puce 717



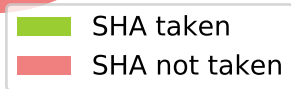
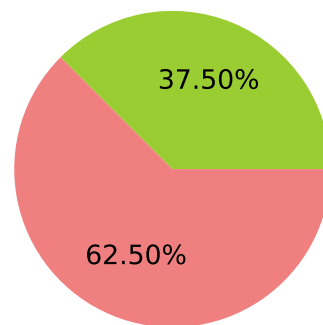
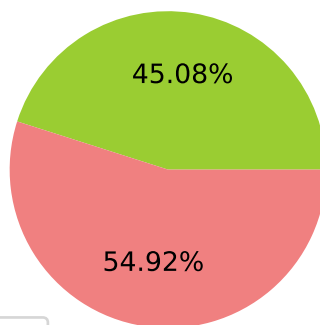
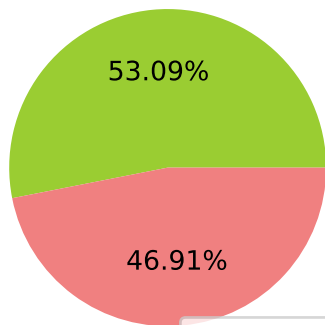


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puce 721



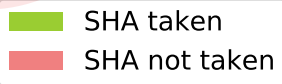
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puce 723

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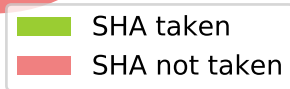
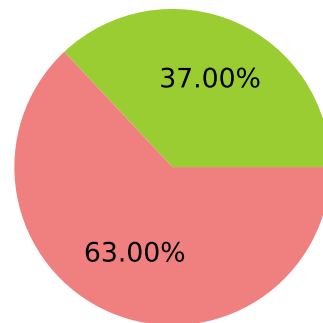
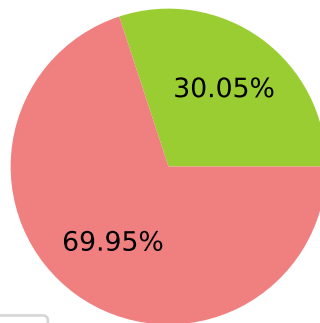
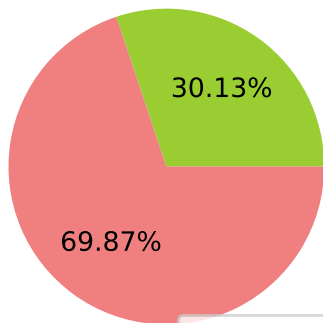


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puce 725

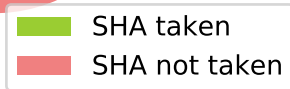
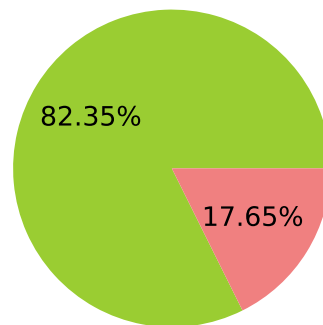
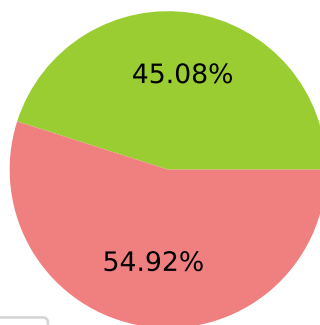
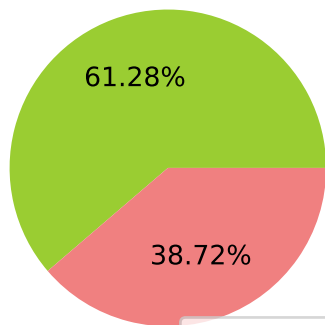


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puce 726

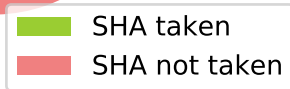
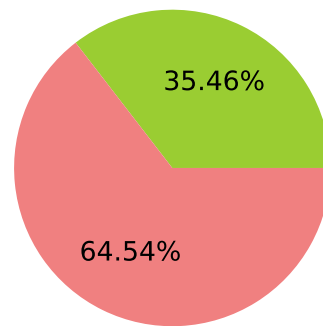
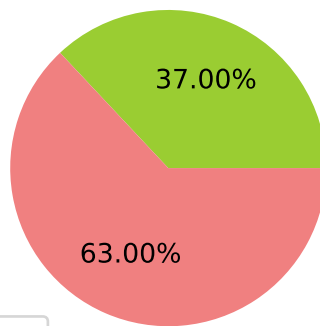
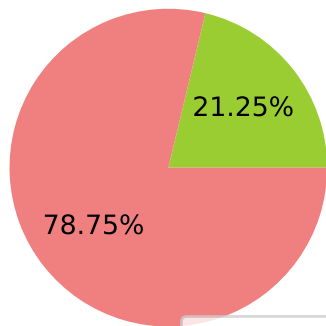


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puce 727

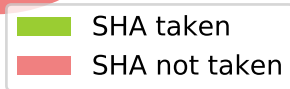
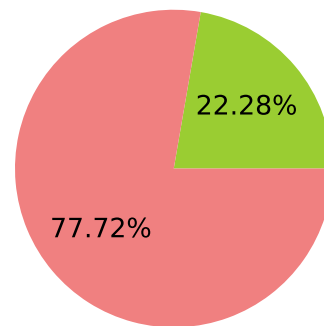
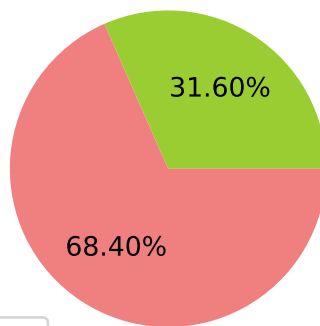
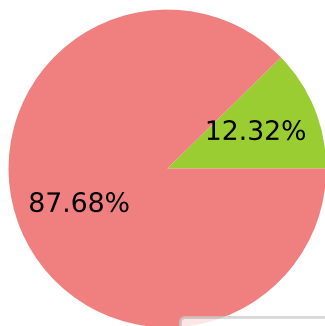


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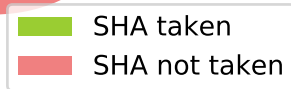
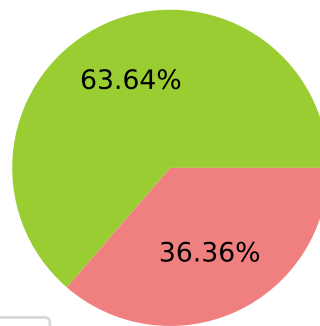
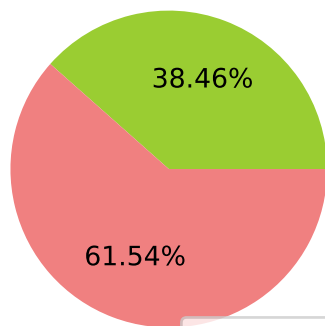
puce 729



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puce 730

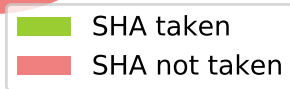
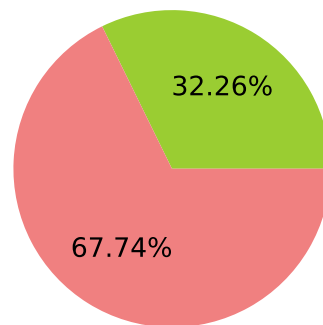
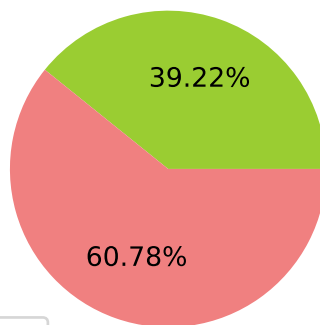
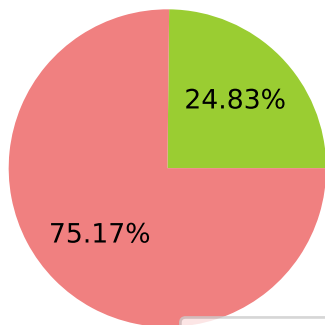


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puce 731

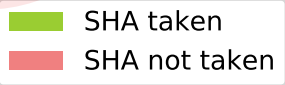
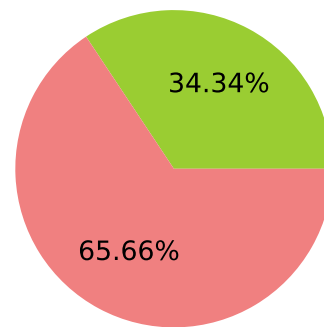
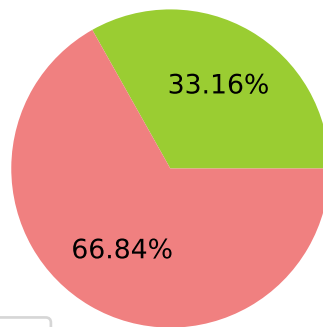
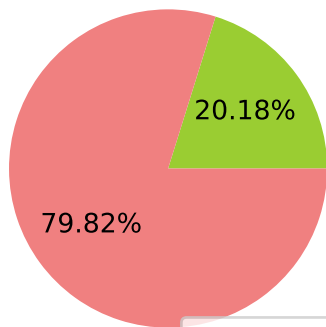


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puce 732

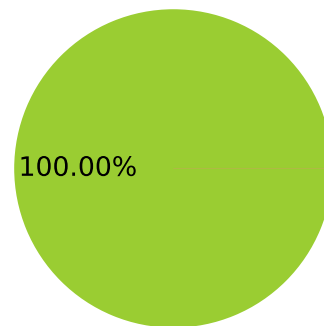
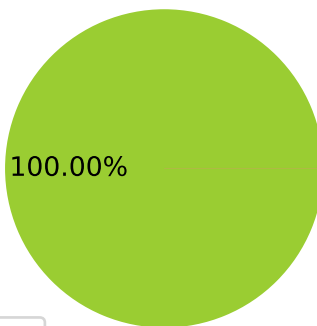
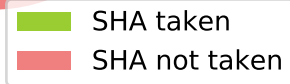
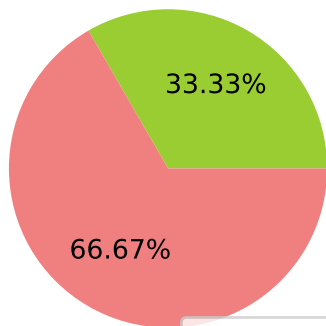


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puce 733

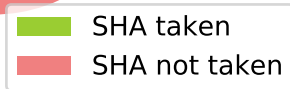
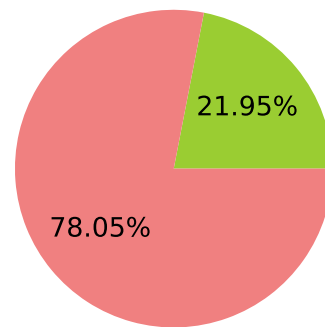
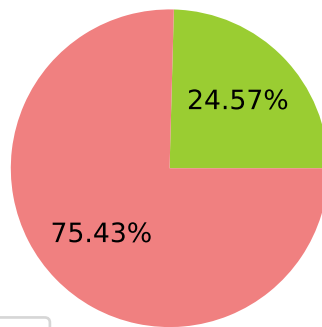
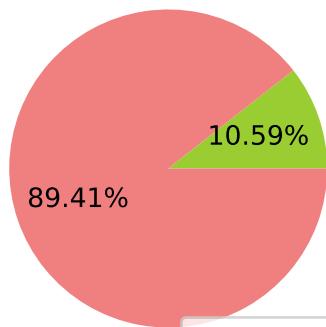


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puce 736

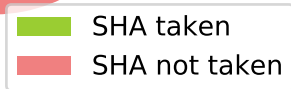
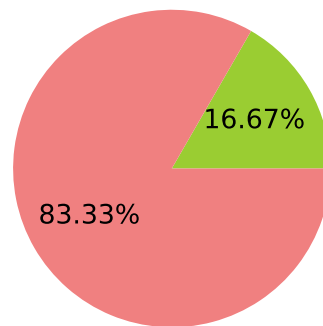
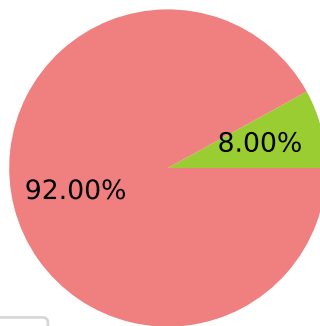
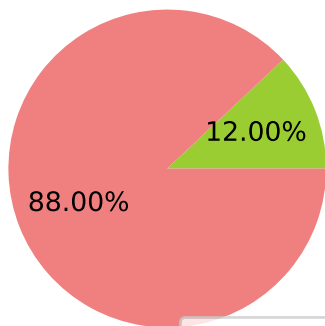


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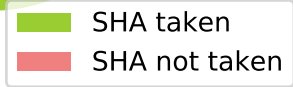
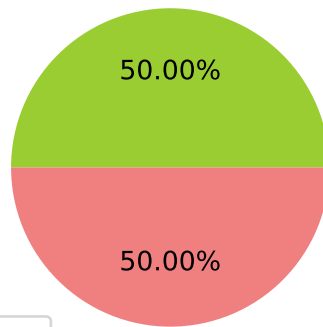
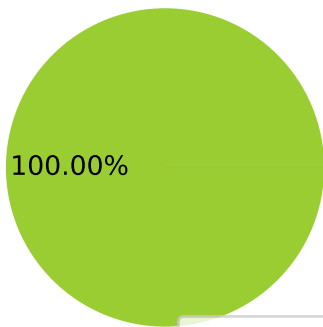
puce 747



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puce 751

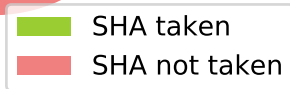
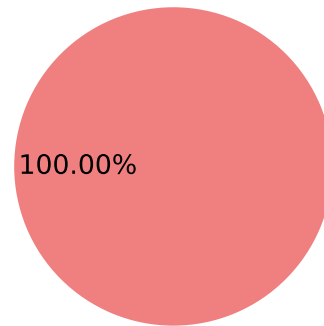
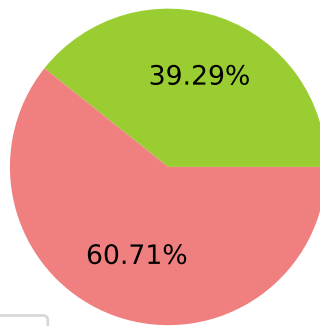
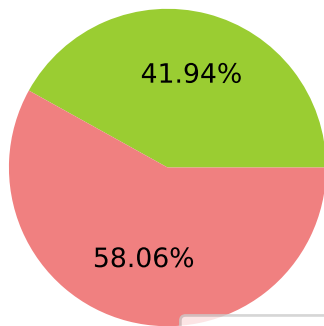


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puce 753

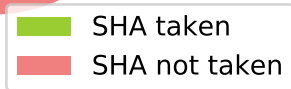
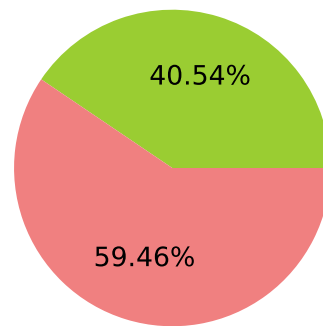
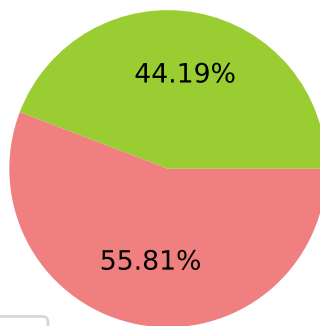
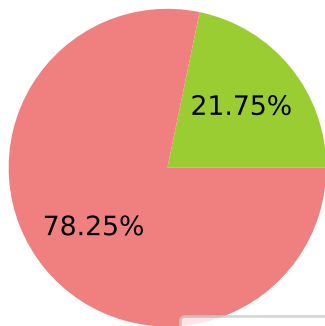


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puce 754

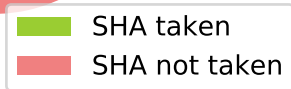
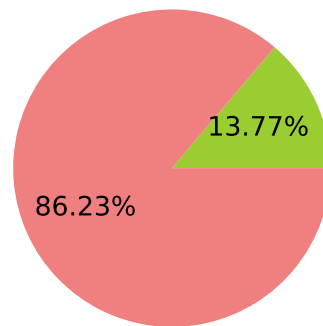
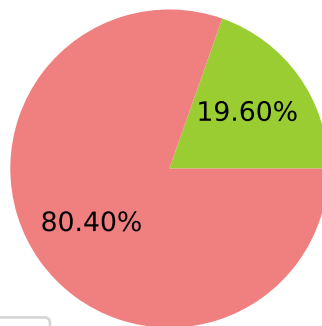
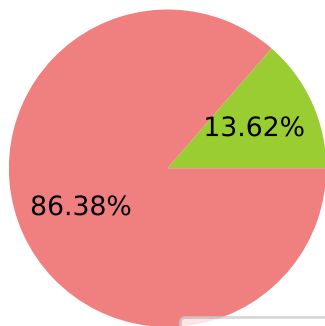


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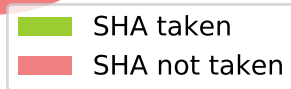
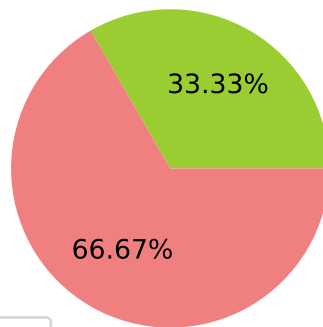
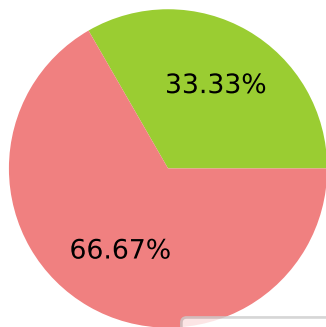
puce 755



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puce 756

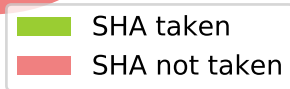
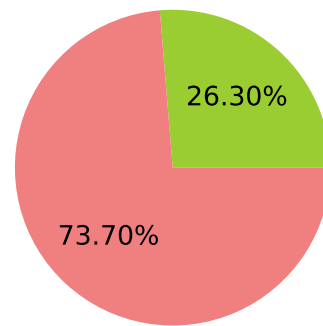
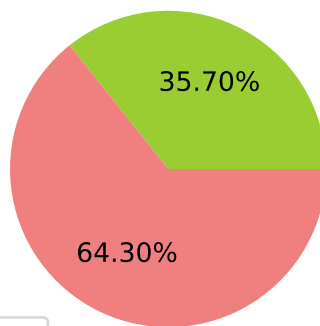
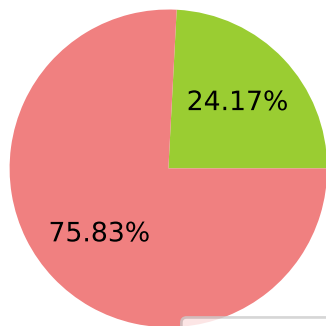


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puce 761



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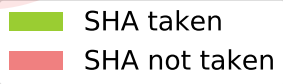
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puce 763

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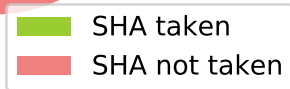
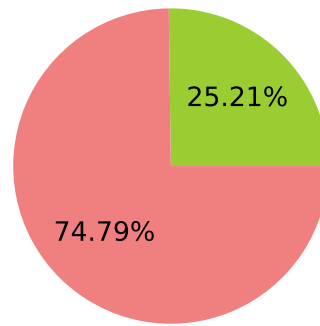
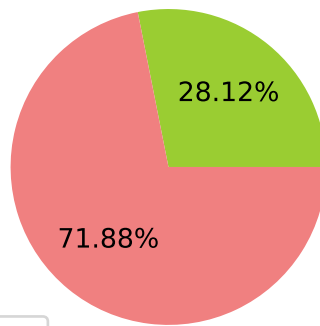
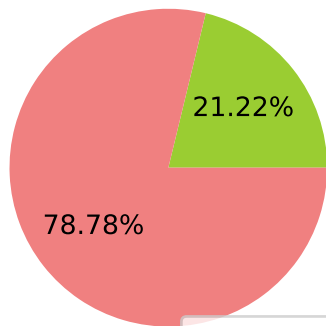


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puce 804

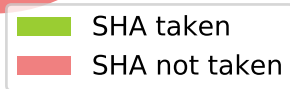
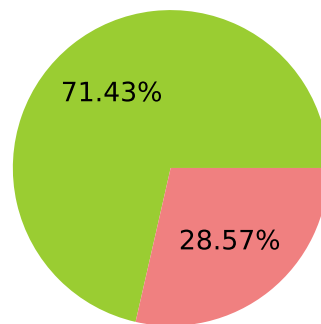
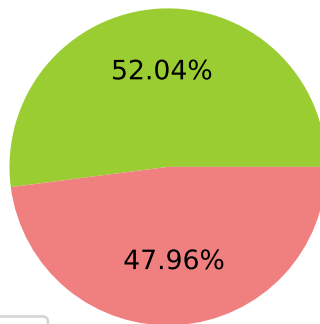
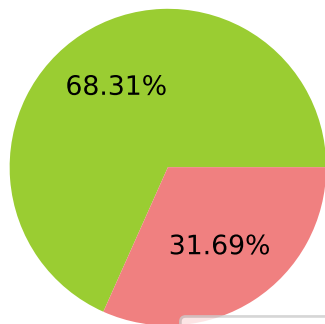


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puce 811

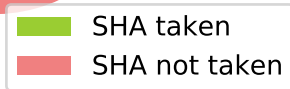
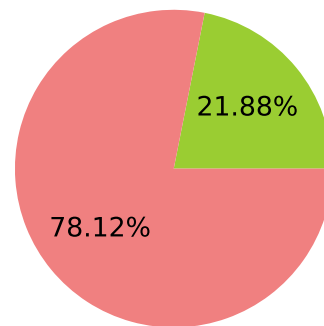
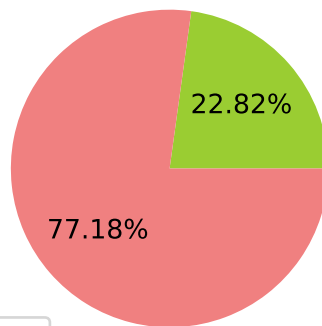
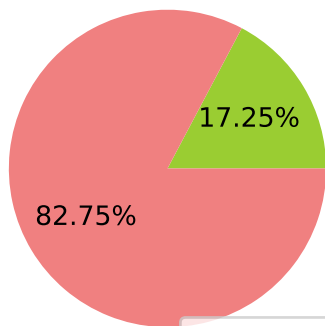


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puce 812

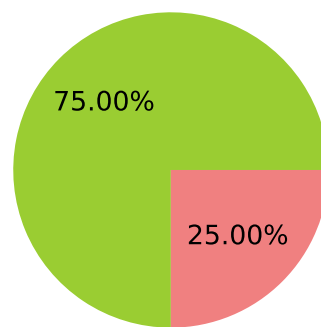
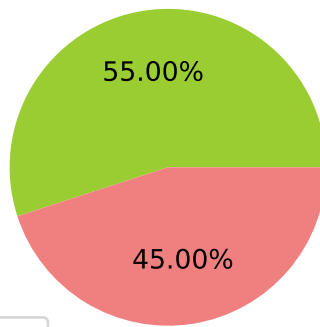
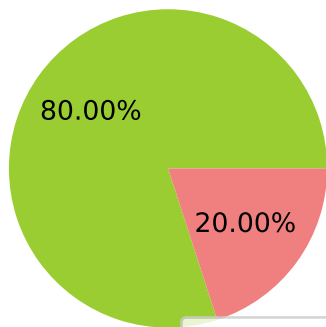


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puce 813



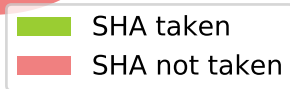
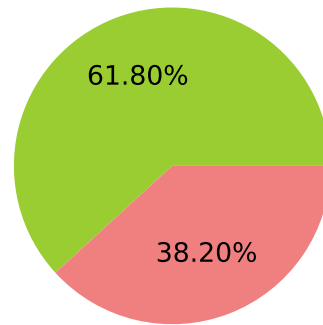
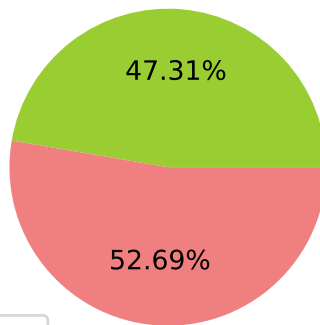
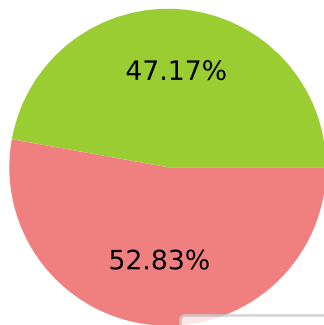
SHA taken
SHA not taken

in

out

inout

puce 814

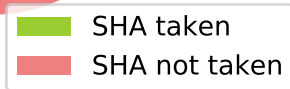
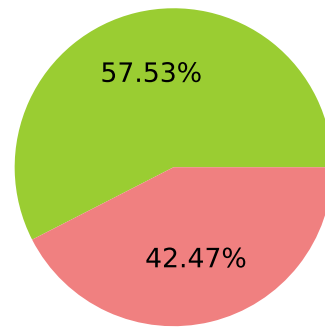
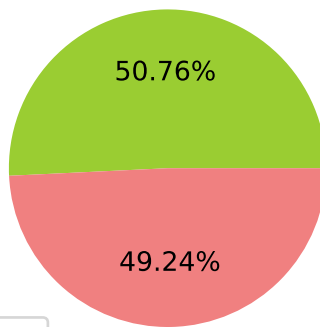
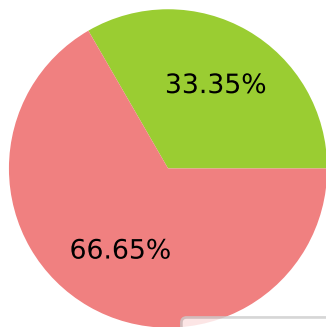


in

out

inout

puce 816

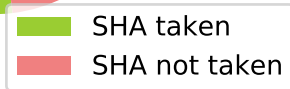
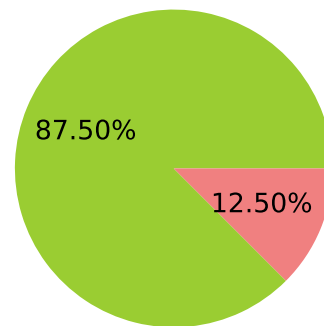
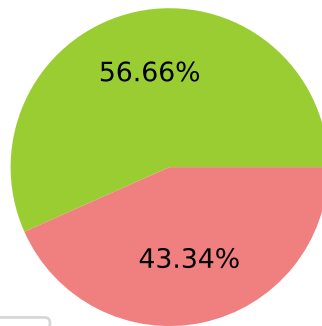
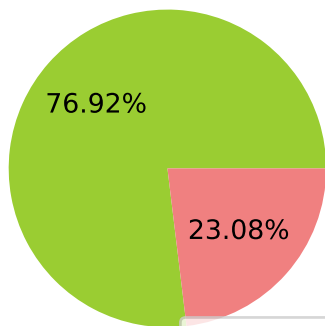


in

out

inout

puce 817

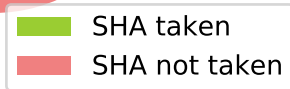
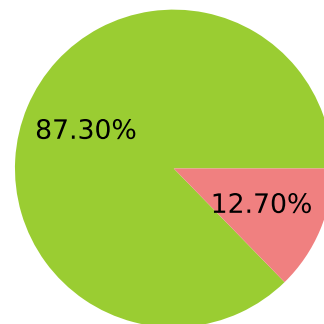
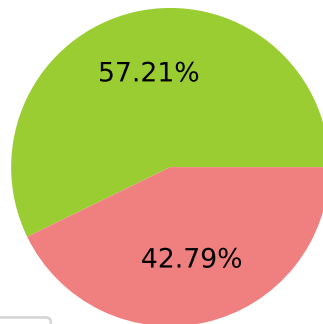
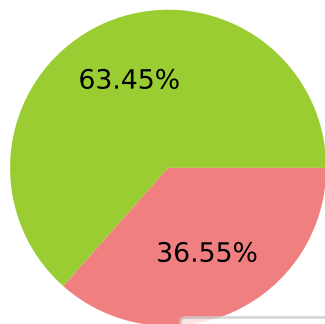


in

out

inout

puce 818

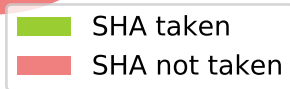
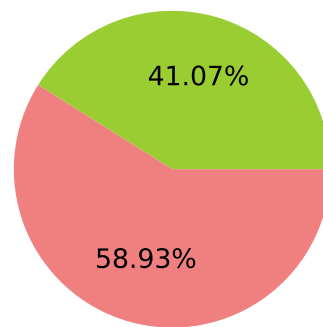
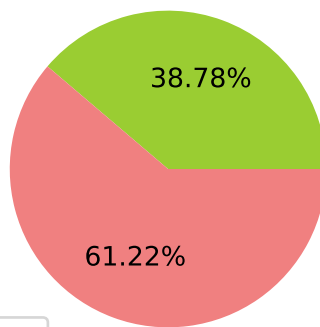
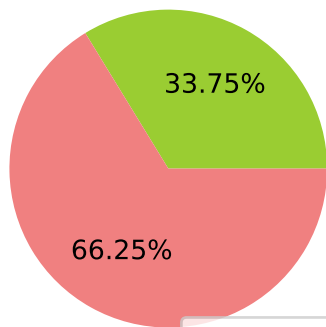


in

out

inout

puce 819

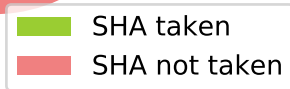
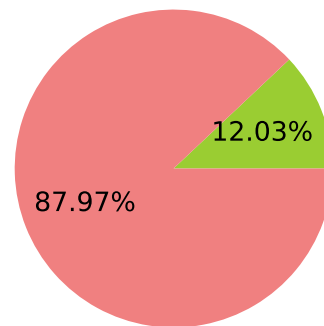
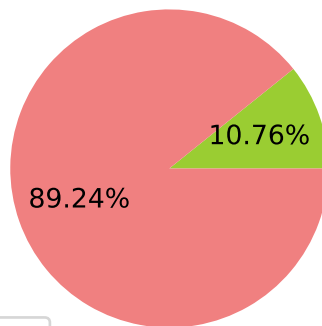
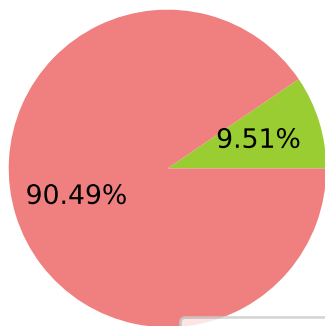


in

out

inout

puce 820

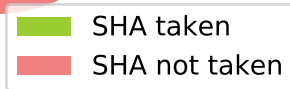
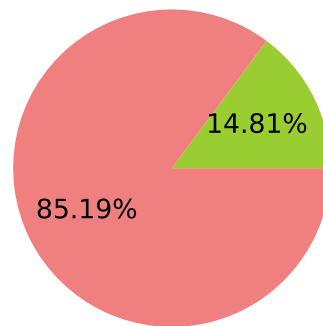
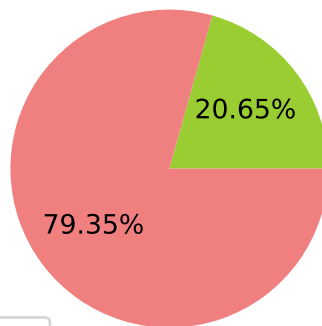
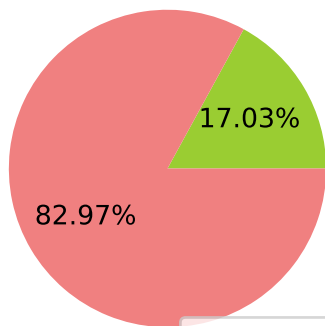


in

out

inout

puce 821

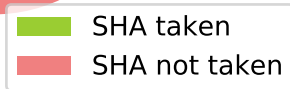
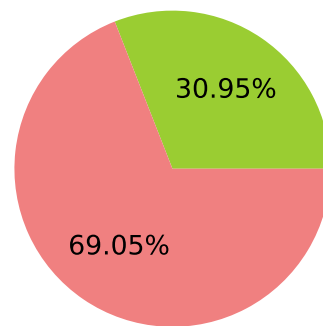
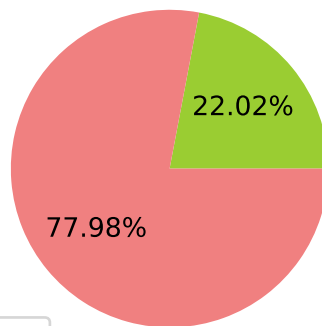
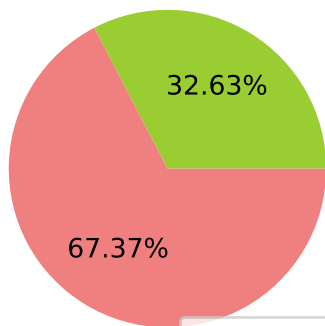


in

out

inout

puce 822

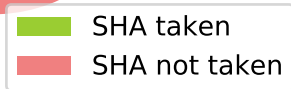
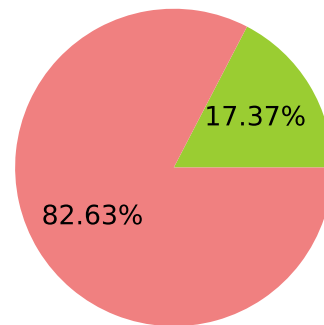
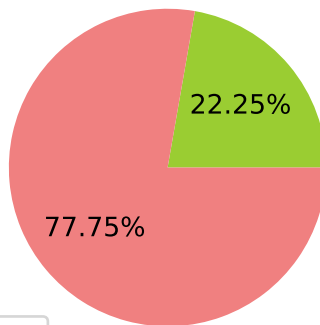
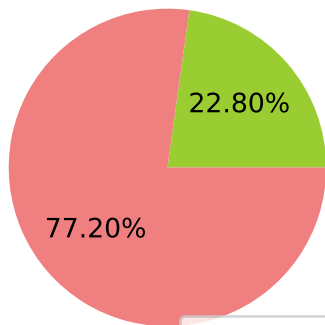


in

out

inout

puce 823

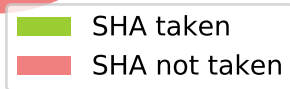
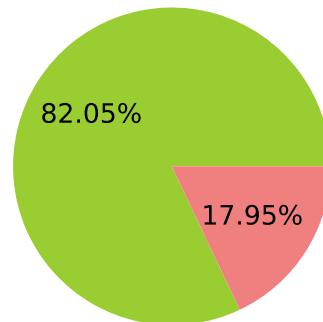
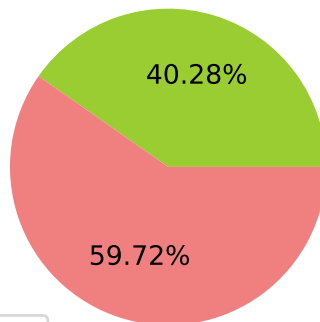
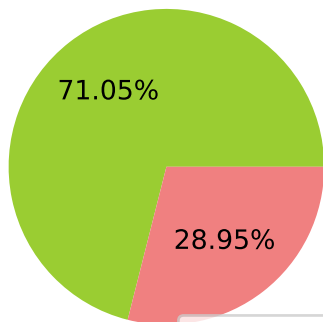


in

out

inout

puce 824

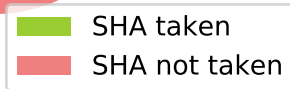
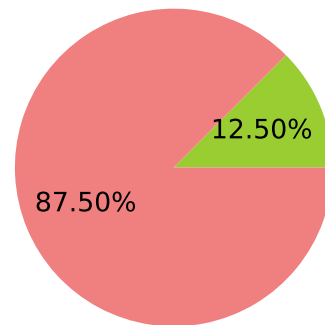
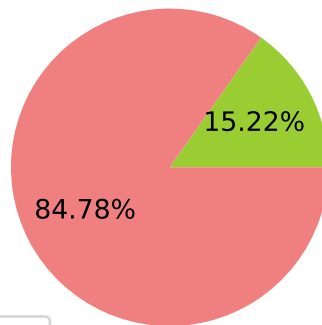
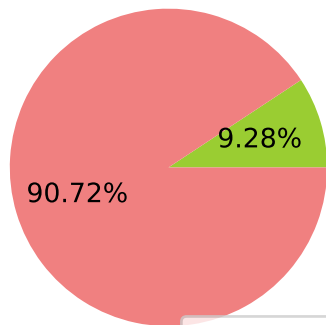


in

out

inout

puce 828

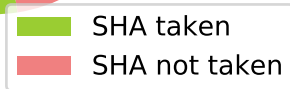
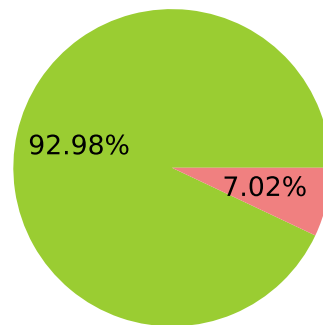
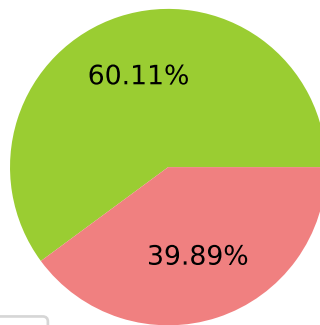
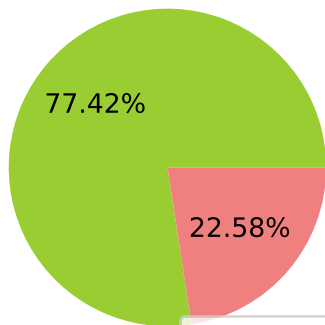


in

out

inout

puce 849

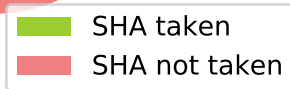
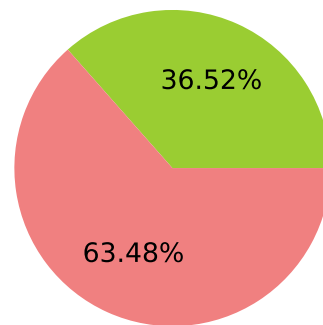
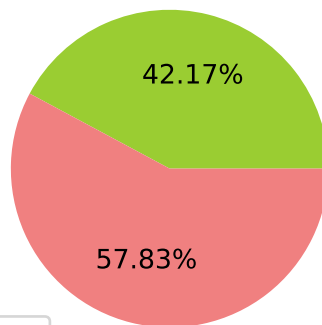
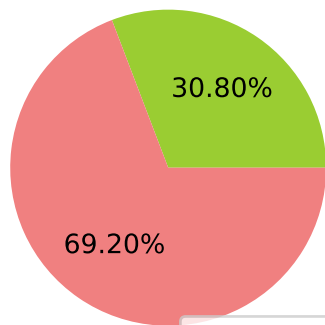


in

out

inout

puce 853

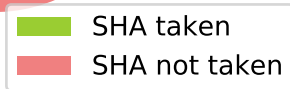
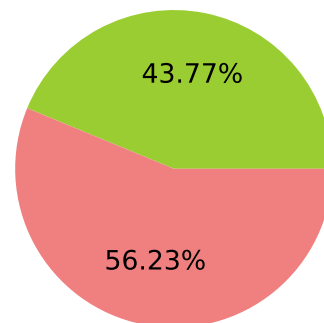
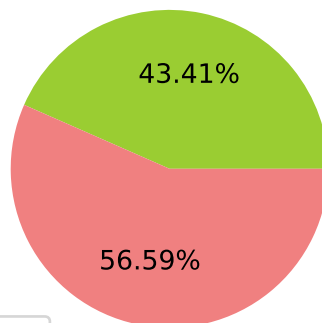
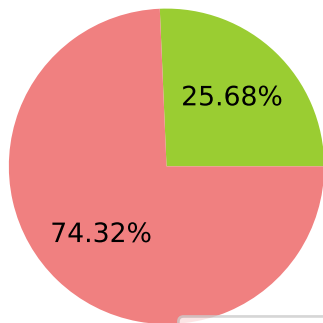


in

out

inout

puce 857

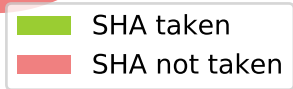
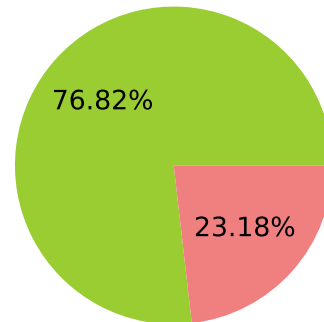
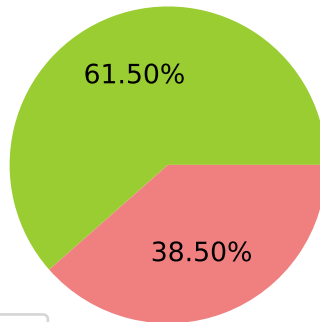
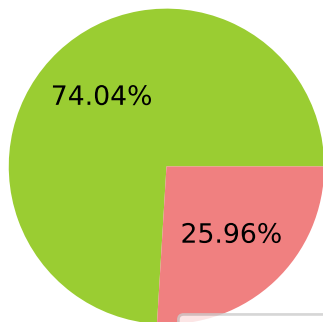


in

out

inout

puce 864

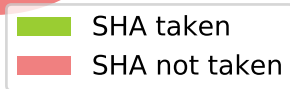
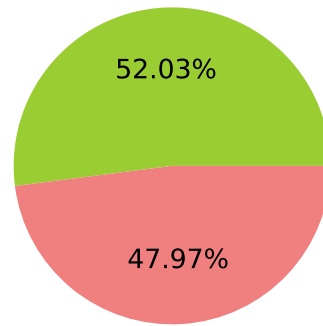
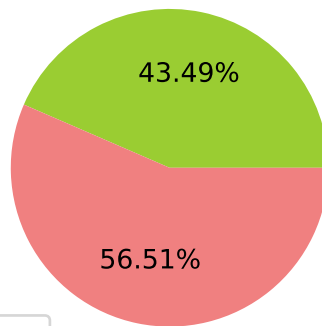
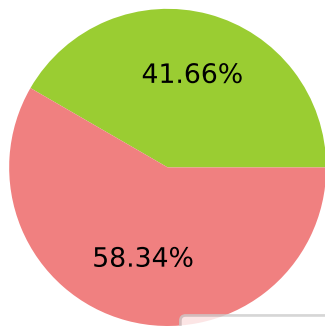


in

out

inout

puce 871

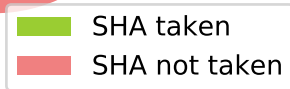
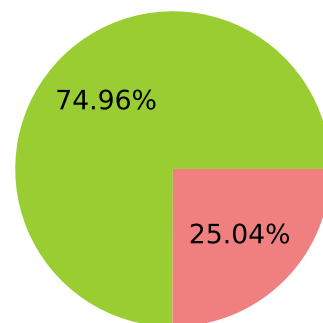
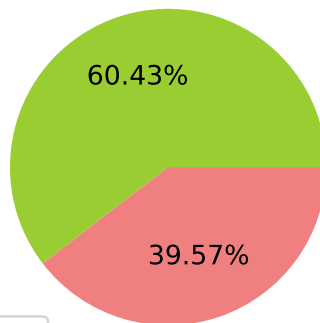
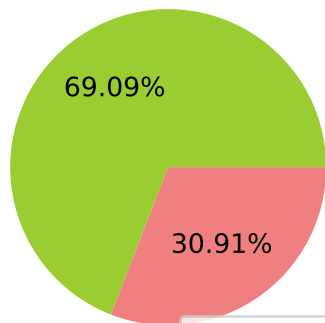


in

out

inout

puce 900

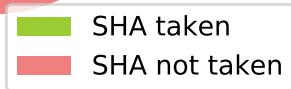
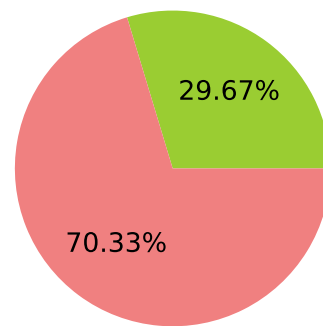
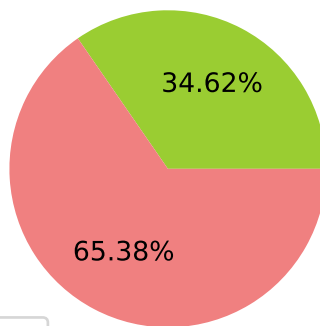
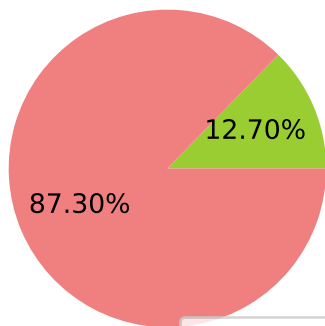


in

out

inout

puce 902

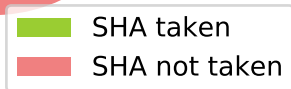
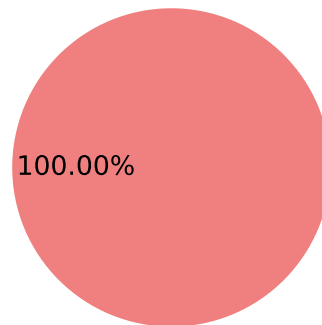
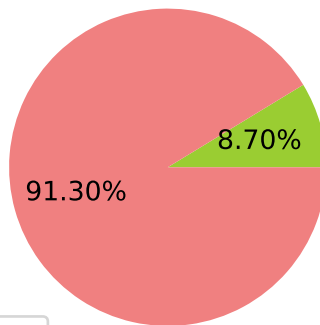
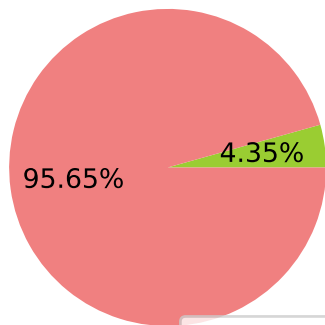


in

out

inout

puce 903

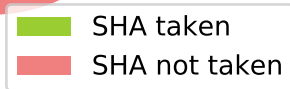
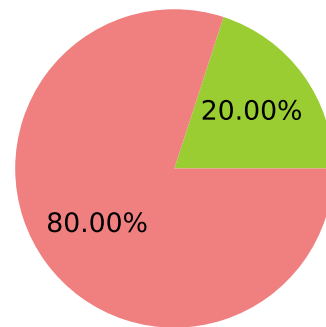
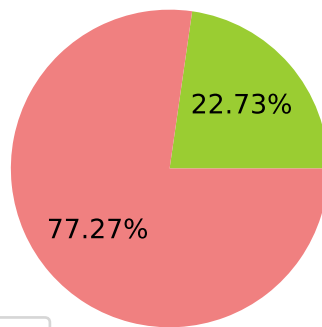
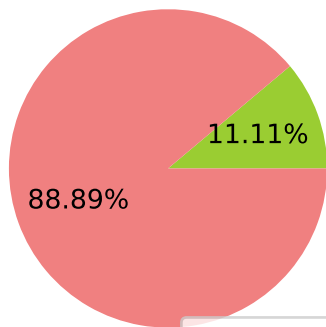


in

out

inout

puce 904

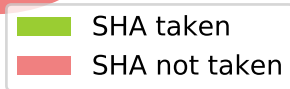
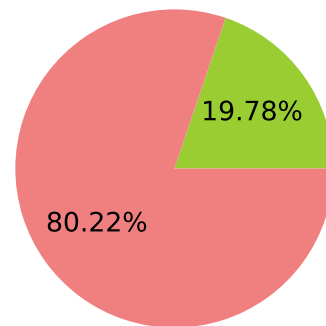
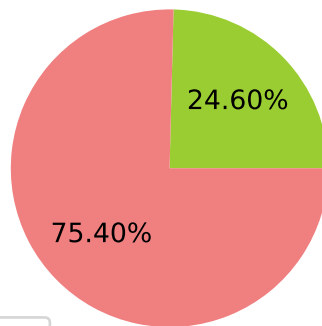
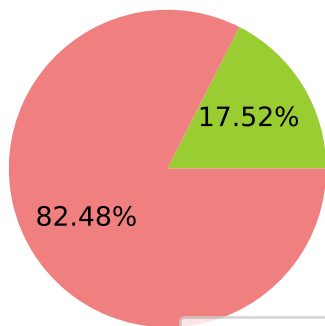


in

out

inout

puce 905

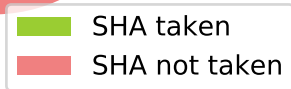
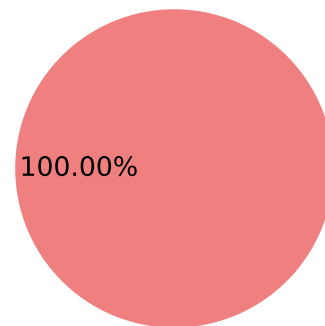
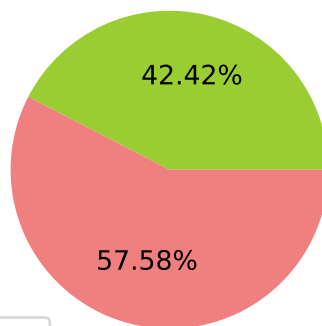
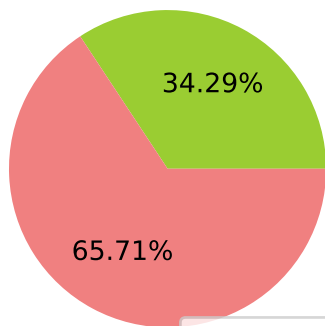


in

out

inout

puce 906

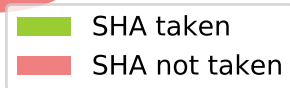
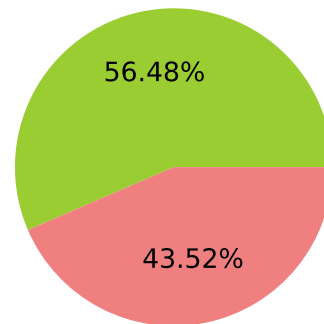
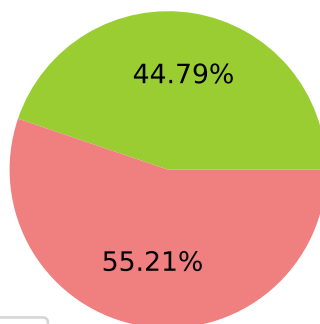
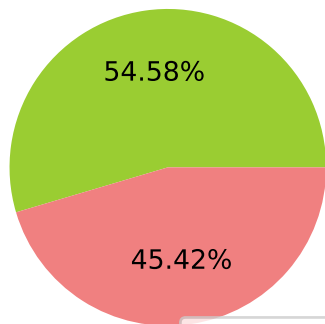


in

out

inout

puce 907

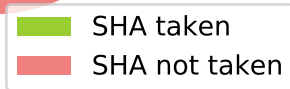
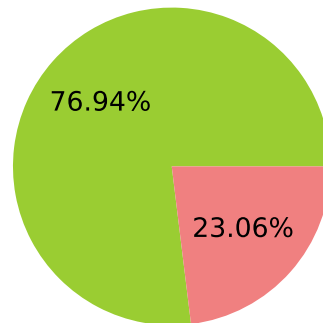
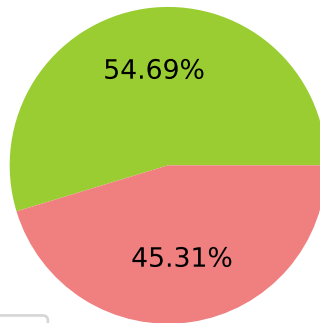
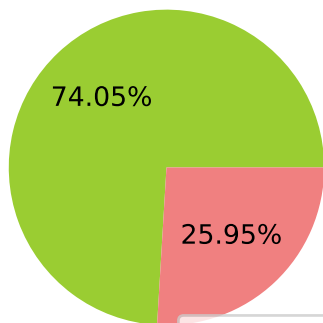


in

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puce 908

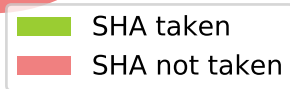
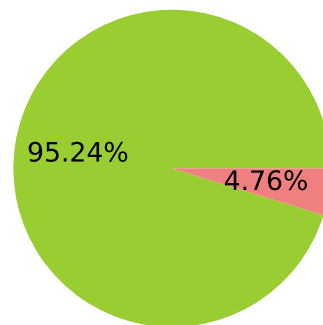
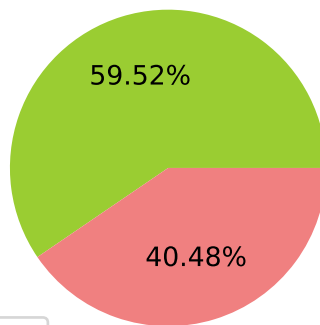
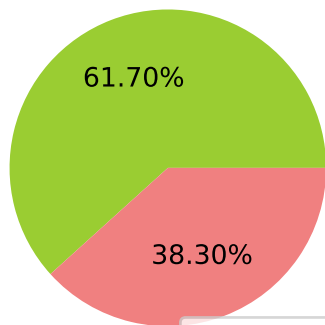


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puce 909

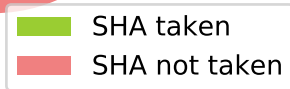
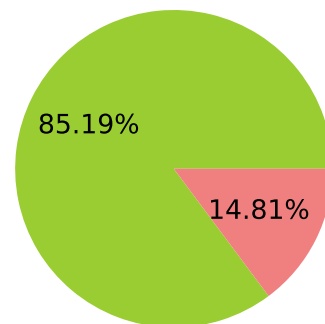
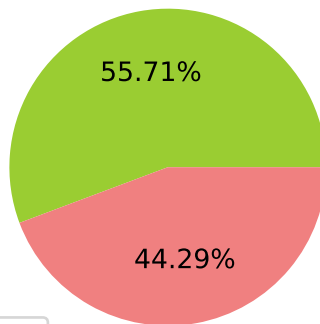
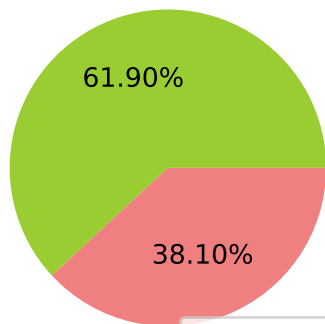


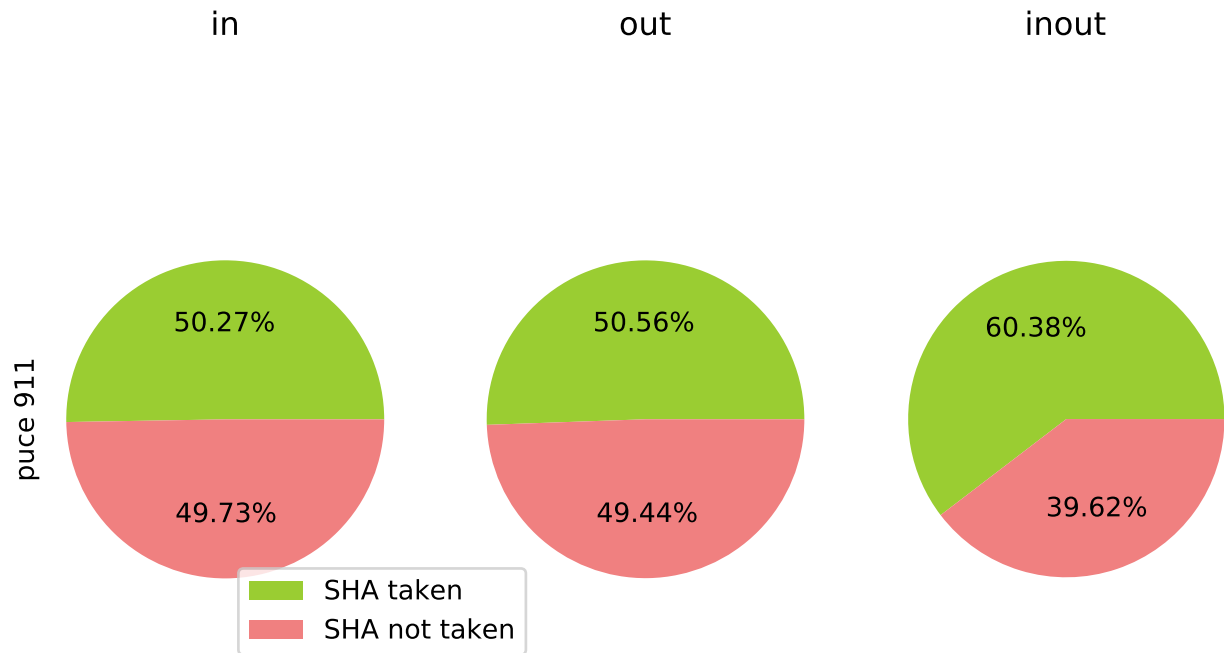
in

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puce 910



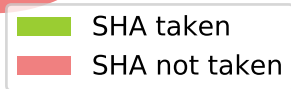
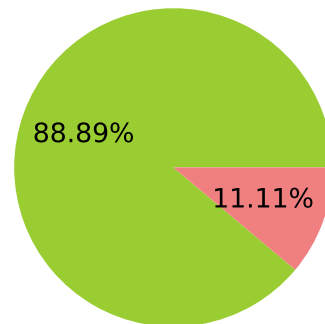
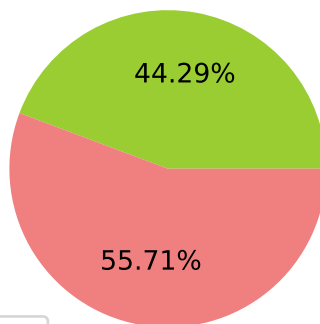
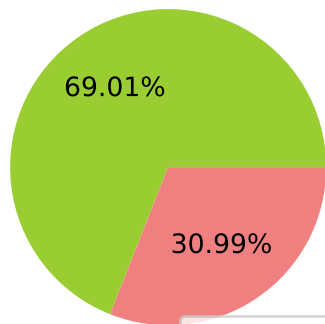


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puce 912

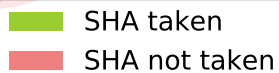
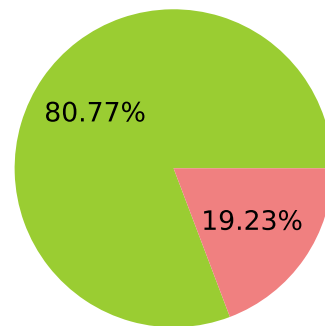
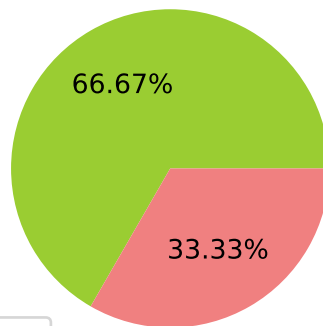
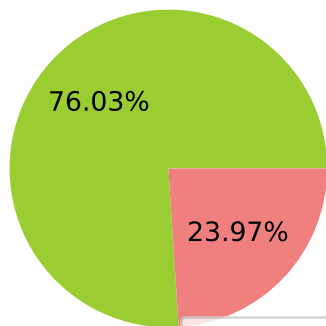


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puce 913

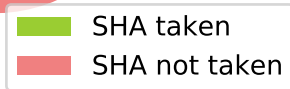
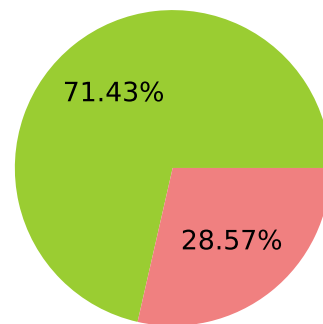
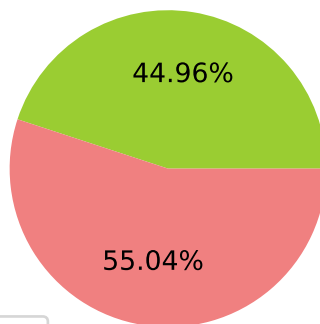
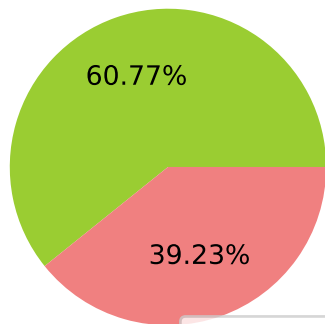


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puce 914

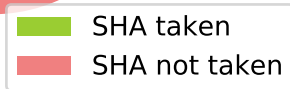
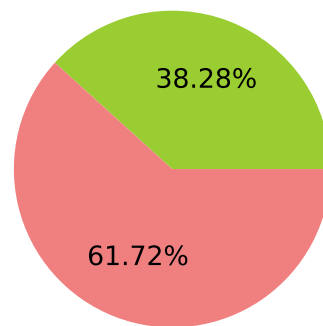
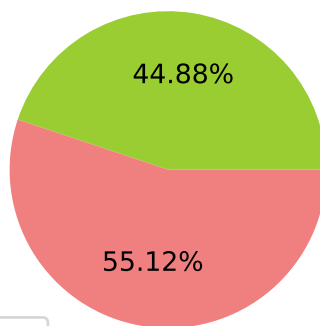
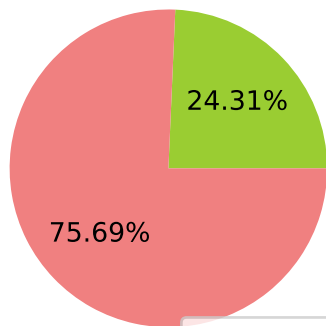


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puce 915

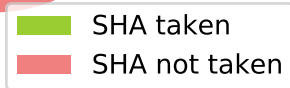
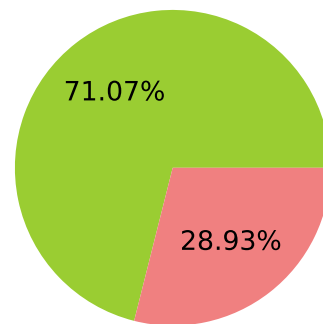
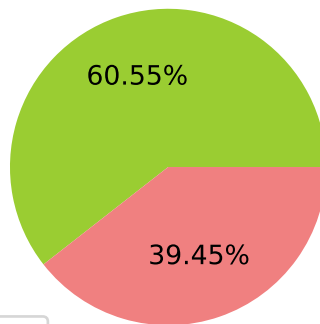
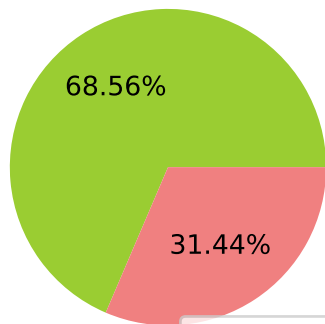


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puce 917

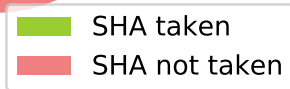
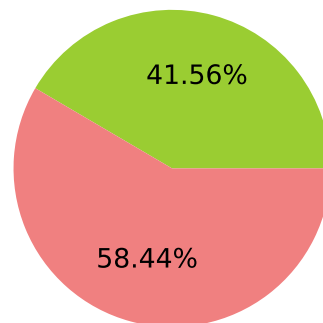
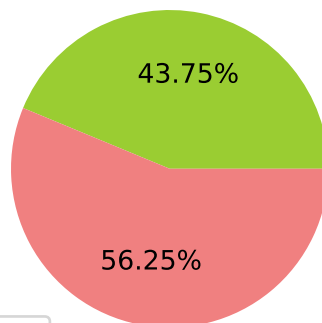
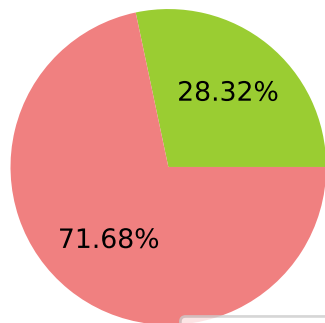


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puce 918

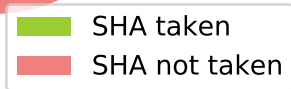
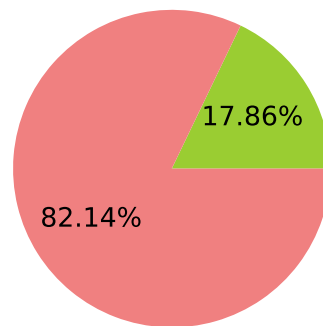
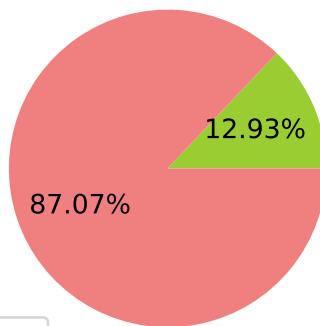
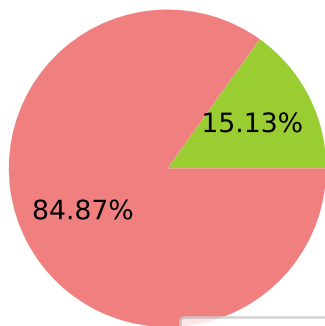


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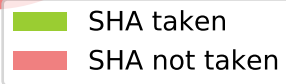
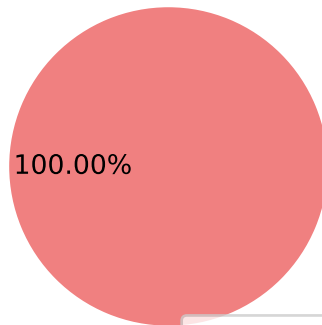
inout

puce 919



inout

puce 920

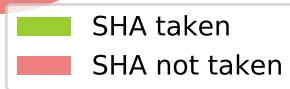
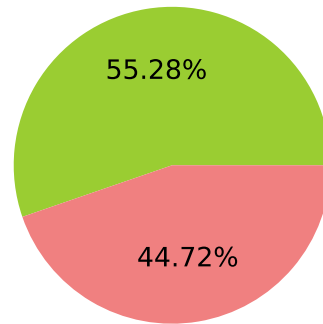
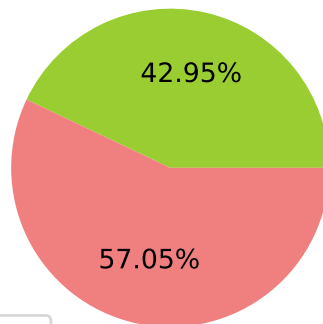
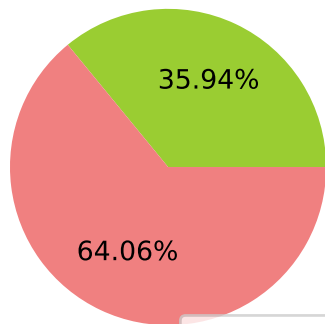


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puce 921

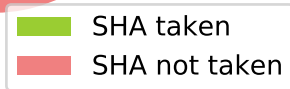
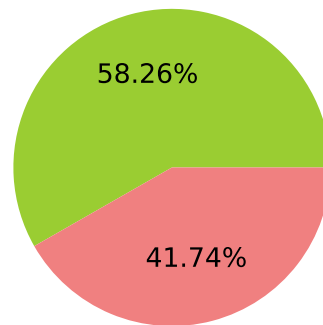
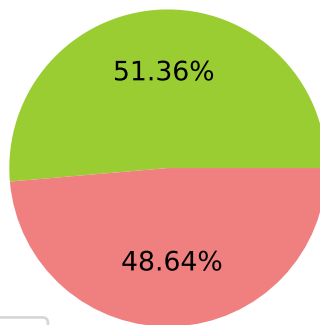
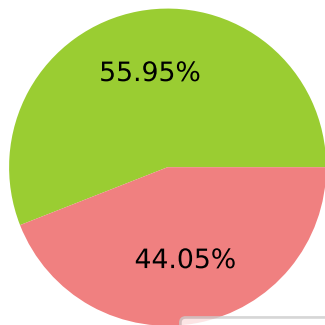


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puce 922

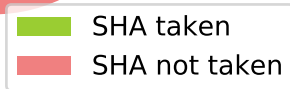
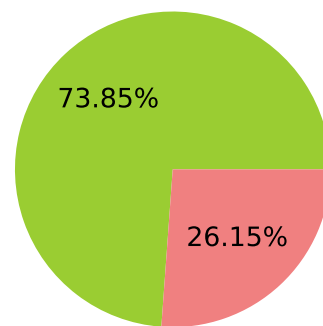
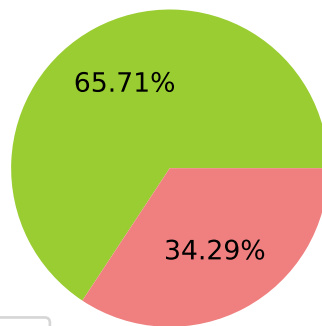
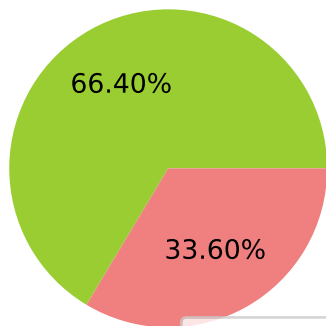


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puce 924

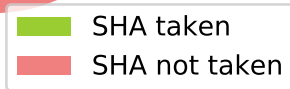
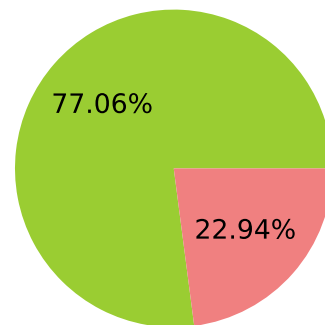
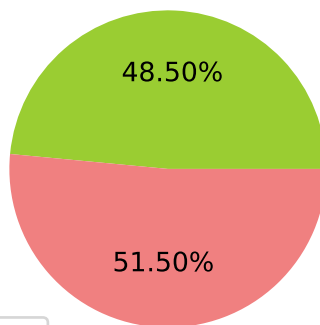
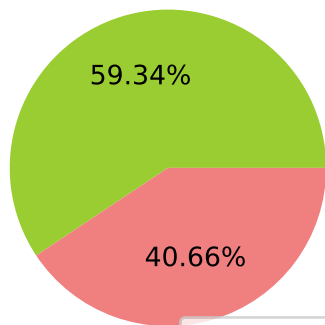


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puce 925

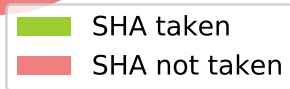
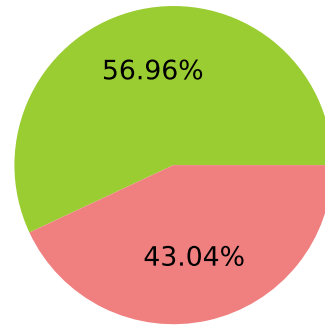
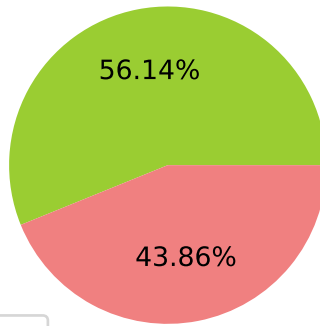
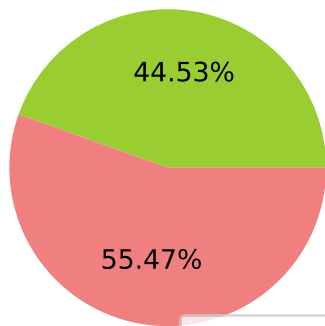


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puce 926

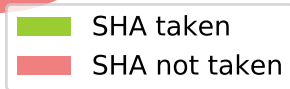
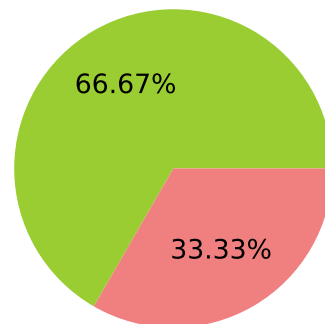
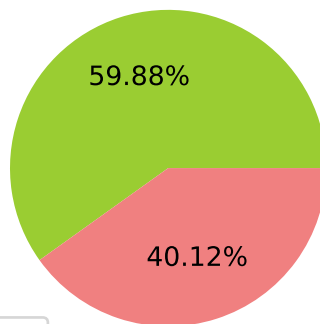
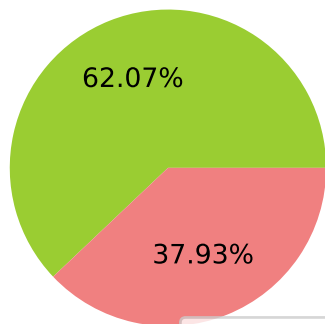


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puce 927

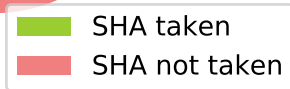
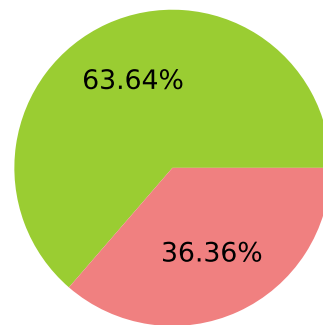
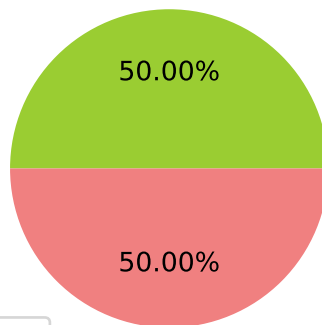
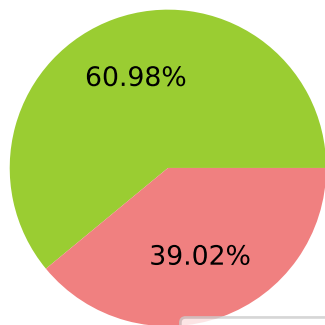


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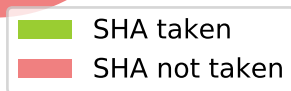
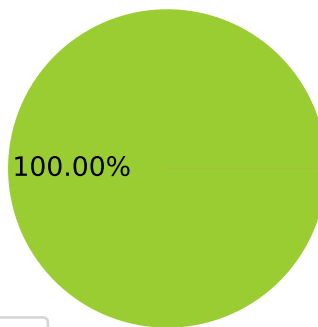
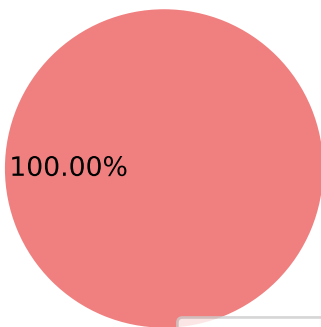
puce 929



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puce 932



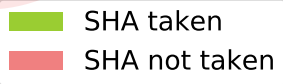
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puce 936

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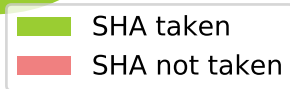
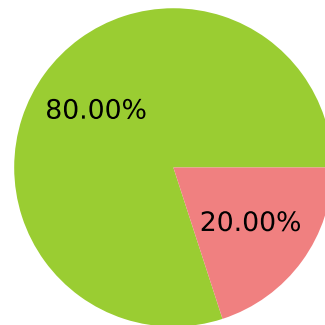
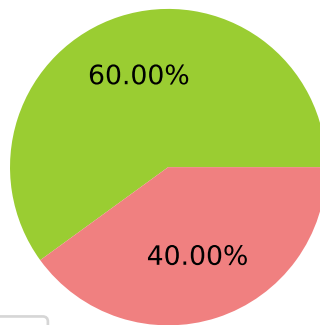
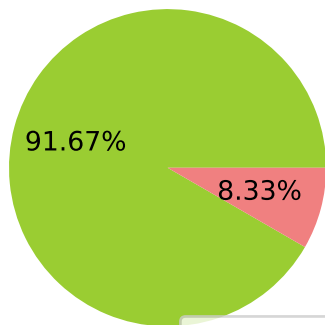


in

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puce 938

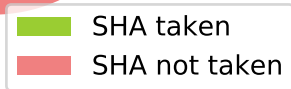
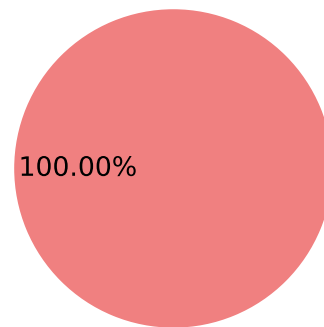
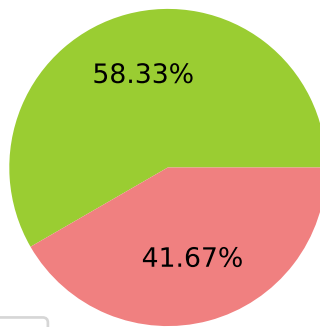
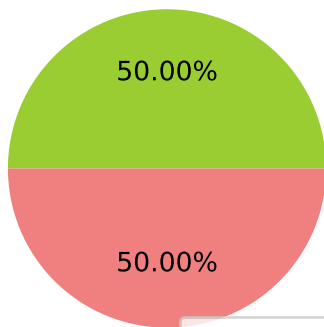


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puce 940

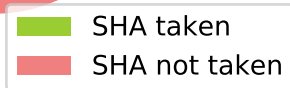
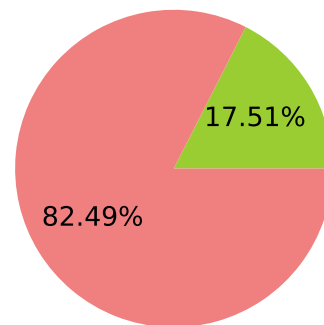
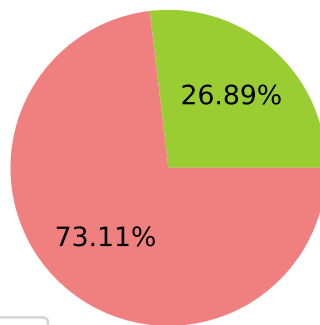
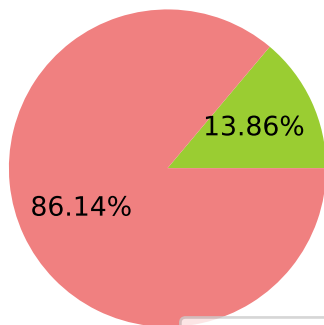


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puce 945

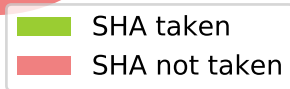
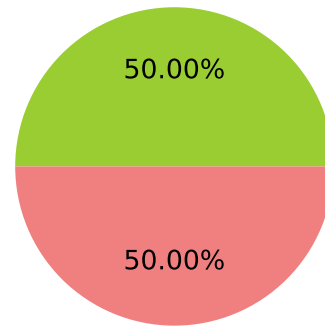
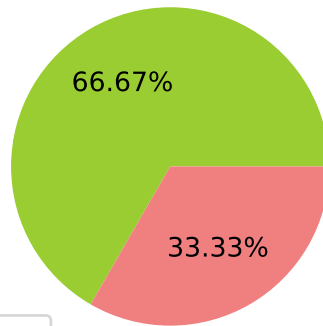
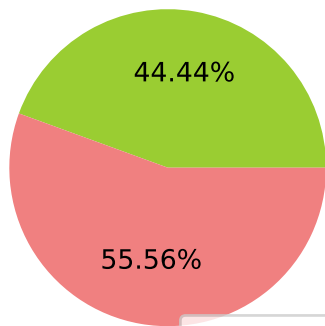


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puce 953

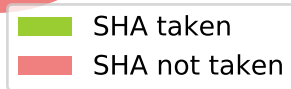
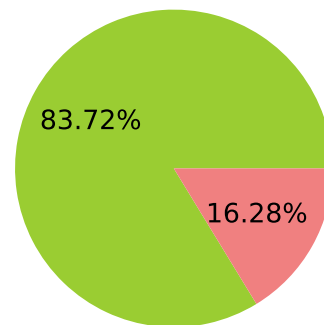
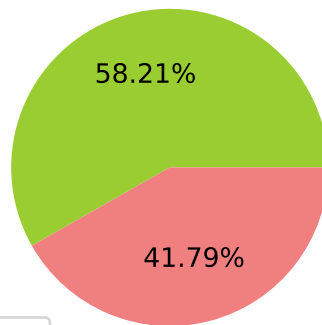
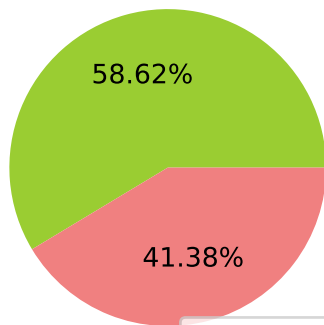


in

out

inout

puce 954

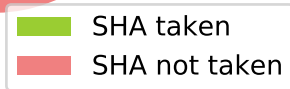
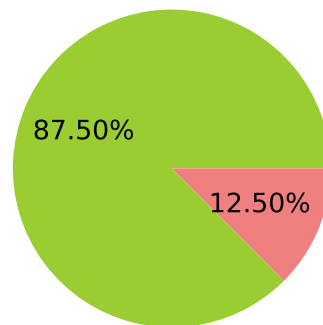
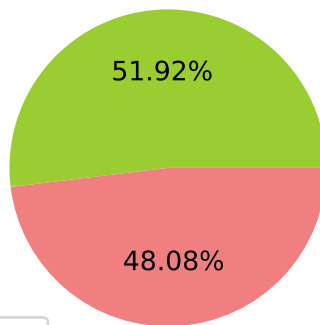
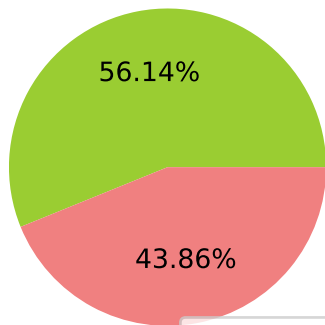


in

out

inout

puce 955

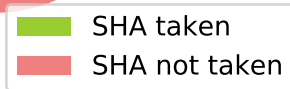
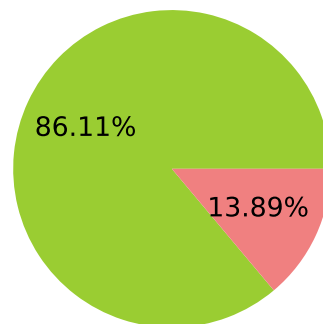
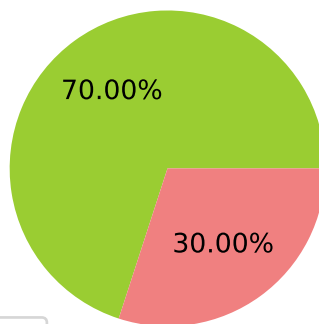
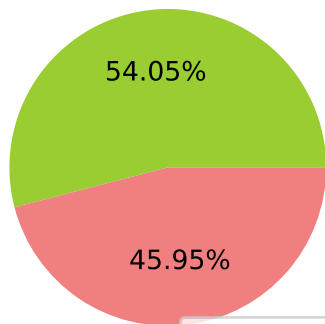


in

out

inout

puce 956

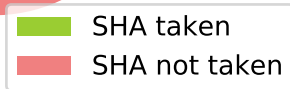
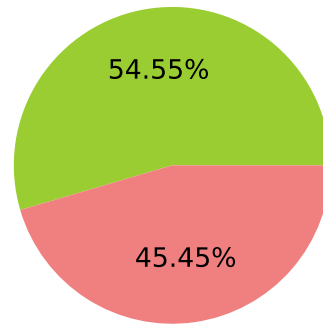
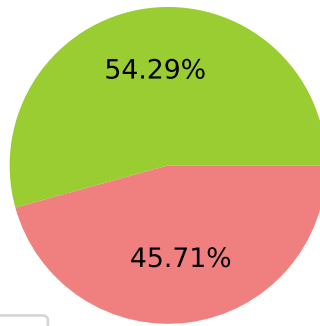
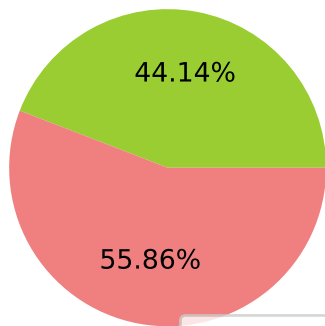


in

out

inout

puce 957

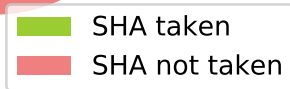
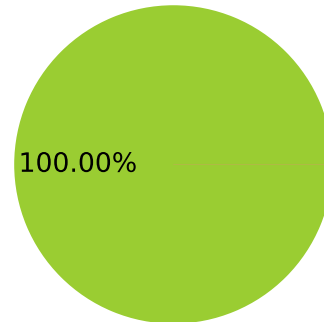
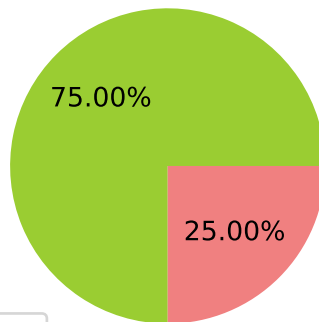
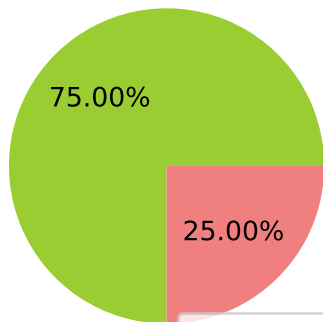


in

out

inout

puce 958

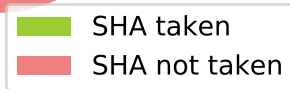
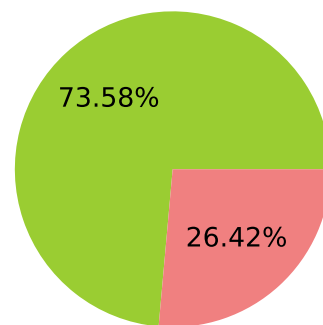
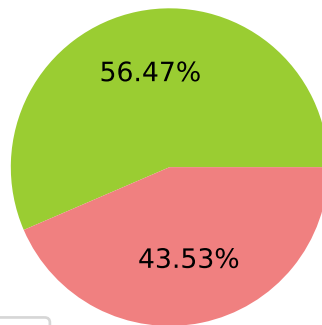
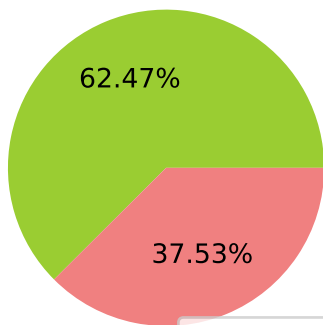


in

out

inout

puce 2379



in

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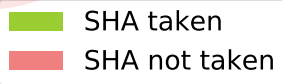
inout

puce 4341

100.00%

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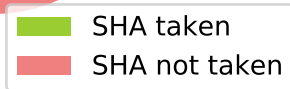
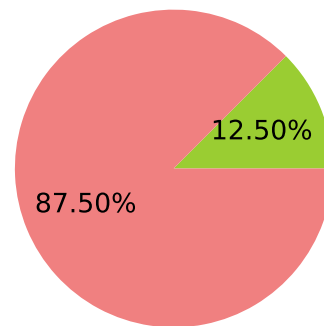
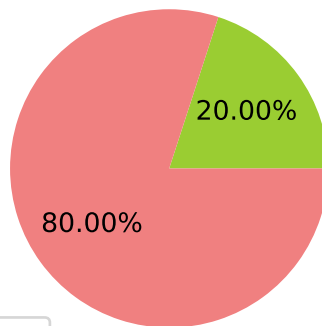
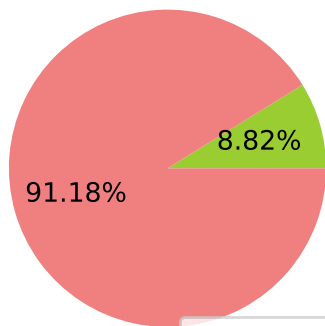


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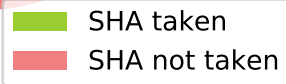
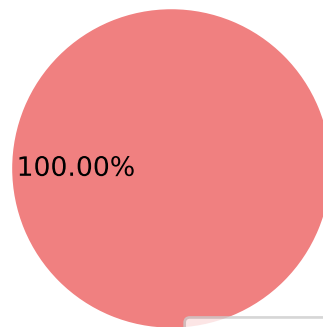
inout

puce 16384



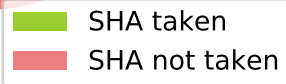
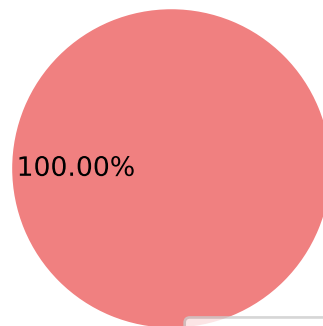
inout

puce 16640



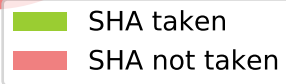
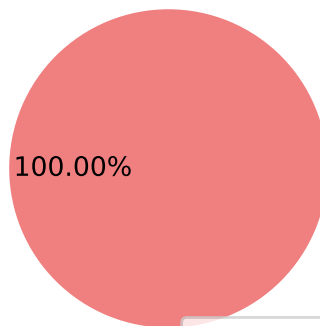
inout

puce 16896



inout

puce 20480



in

out

inout

puce 30004

