

# AD for pap566 : Towards Optimal Caching with Reuse Distance Prediction via Long Short-Term Memory

## 1. Hardware

*All the hardware used in this paper is the corporate resources of Alibaba. According to the corporate security policy, there is no way to vpn-access the hardware resources from outside at this moment. But, we would like to give the detailed machine architectures and configurations as below.*

All deep learning tasks were performed on a Nvidia Tesla V100 GPU card of the recent Volta architecture, which has 5120 streaming cores, 640 tensor cores and 32GB memory capacity. The CPU host is Intel Xeon CPU 8163 2.50GHz, running Linux kernel 5.0.

## 2. Software and Code

*The code used in this paper is partially running in the production at Alibaba. According to the corporate security policy, there is no way to open-source the code at this moment. But, we would like to describe some basics of the code structures and the instructions to run.*

### 2.1 For trace pre-processing

- `extract_msr.py`: convert MSR original traces into the format of memory access requests.
- `count_diff.py`: count the number of different data blocks in a trace.
- `preprocess.py`: merge partial traces into one complete trace.
- `global_state.py`: generate the first 6 global features of each data access, and partition the trace into uniform trunks for faster file readings in later experiments.

How to run:

- `python extract_msr.py trace_name volume_number`
  - output: trace name volume number trace.tr
- `python count_diff.py trace_name volume_number`
  - output: the number of different data blocks in trace name volume number trace.tr (denoted as key  $i$  for trace name  $i$  trace.tr, when the volume number is equal to  $i$ .)
- `python preprocess.py trace_name total_volume_number key_0 ... key_n`
  - output: trace.csv (assuming total volume number =  $n + 1$ )
- `python global_state.py trace_name trunk_size`

- output: trace st 0.csv, trace st 1.csv, . . .

## 2.2 For feature engineering

- feature\_trace.h feature\_trace.cc
  - Compute features from the converted traces above.
- feature\_to\_examples.h feature\_to\_examples.cc
  - Convert the computed features to training data sets and testing data sets, stored in .tfrecord data format.

### How to build:

- We have a BUILD file for all C++ programs.
- >> bazel build -c opt :feature\_trace
- >> bazel build -c opt: feature\_to\_examples

### How to run:

- >> ./bazel-bin/feature\_trace --trace\_path <trace> --last\_k=<a number>
  - Output: a feature vector for each access
- >> ./bazel-bin/feature\_to\_examples --dataset\_path <trace\_feature\_file> --num\_examples=<a number>
  - Output: a training data set or test data set in the .tfrecord data format.

## 2.3. For model training and inference

- seq\_stacked\_predictor.py
  - Train model and make predictions for given test data sets.

### How to run:

- >> python3 -W ignore predictors/seq\_stacked\_predictor.py --epochs=100 \ --seq\_length=1024 \ --batch\_size=64 --data\_path "data\_sets/data\_set\_web\_pred" \ --out\_path "data\_sets/data\_set\_web\_pred"

## 2.4 For pOPT cache

- popt.h popt.cc
  - Run Algorithm 1 with the predicted forward reuse distance

### How to build:

- We have a BUILD file for all C++ programs.
- >> bazel build -c opt :popt

How to run:

- >> ./bazel-bin/popt --trace\_path <trace> --forward\_reuse\_distance\_path <rd\_file> --cache\_size=<a number> --out\_dir <out directory>
  - Output: cache miss rate given the cache size.

## 2.5 For baselines

We implemented LRU, ARC, DDRIP, OPT by ourselves based on the existing references or their definitions.

The 2Q implementation refers to the version used in lease cache work[28].

Parrot implementation refers to

[https://github.com/google-research/google-research/tree/master/cache\\_replacement](https://github.com/google-research/google-research/tree/master/cache_replacement)

## 3. Workloads

In our experiment, we considered the traces from Microsoft cloud storage workloads, which can be downloaded from <http://iota.snia.org/traces/388>.

We ignore the difference between reads and writes, and simplify them as indiscriminate memory access requests. Traces from volumes of the same domain are combined as one complete trace, since they access the same type of data.

## 4. Tools

- Bazel version: 3.5.0
- Python: 3.7.4