

TDC

This module computes the time between the arrival of the external trigger and the Sbits.

Addressing

Module ID 15
Address 0x4F0000YY
 0b0100 1111 0000 0000 0000 0000 000Y YYYY

Y register	Mode	Function
Global		
0	R/W	Status/command register - bit 0 : reset the module - bit 1 : calibration of the TDC (write sends the command, read gives the in-progress status)
1-2	R/W	Window mask for the Sbits shift-register
3	R/W	Calibration LUT (circular buffer) - 512 values to read - write resets the address
Time differences		
8-31	Read only	FIFOs for each VFAT