|  |  |  |  |
| --- | --- | --- | --- |
| ns | V1 | V2 | V3 |
| basic | 268433240 | 184658440 | 51136900 |
| DPA | 205509890 | 117846460 | 47204740 |

|  |  |  |  |
| --- | --- | --- | --- |
|  | V1 | V2 | V3 |
| basic | 18272 | 6475 | 7940 |
| DPA | 17051 | 6766 | 6762 |

|  |  |  |  |
| --- | --- | --- | --- |
| ns | V1 | V2 | V3 |
| basic | 6.2-6.3 | 4.2-4.3 | 1.0-1.1 |
| DPA | 4.7-4.8 | 2.6-2.7 | 0.9-1 |

TLM clock period(ns)

0.9

Simulated time == 45432396 ns

1

Simulated time == 49620280 ns

1.1

Simulated time == 53808164 ns

2

Simulated time == 91499120 ns

2.4

Simulated time == 108250656 ns

2.5

Simulated time == 112438540 ns

2.6

Simulated time == 116626424 ns

2.7

Simulated time == 120814308 ns

4.2

Simulated time == 183632568 ns

4.3

Simulated time == 187820452 ns

4.5

Simulated time == 196196220 ns

4.6

Simulated time == 200384104 ns

4.7

Simulated time == 204571988 ns

4.8

Simulated time == 208759872 ns

6.1

Simulated time == 263202364 ns

6.2

Simulated time == 267390248 ns

6.3

Simulated time == 271578132 ns