

# Symmetric Techniques for Advanced Protocols: What *\*are\** They?

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Inria, Paris

14th of March 2025



## Trendy topics

MPC-friendly?

Arithmetization-Oriented?

Verification efficiency?

Algebraic attacks?

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Symmetric crypto **for the blockchain...**

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Symmetric crypto **for the blockchain...**

... **for neural networks???**

The conclusion of today: **symmetric cryptography** has always had to deal with specific **implementation criteria**, but the **new ones** are indeed a bit **stranger than before**.

# Outline

- 1 What is the Purpose of a Symmetric Primitive
- 2 “Advanced” Protocols
- 3 Symmetric Primitives for Advanced Protocols
- 4 Conclusion

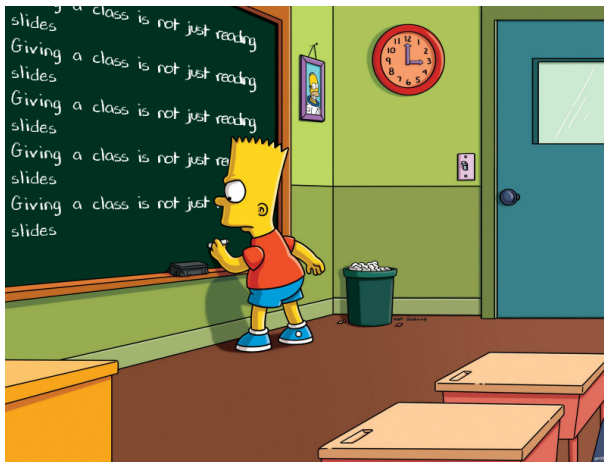
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  - Let's look at primitives we all know
  - A Small Cog in a Big Machine
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## Why do we need symmetric primitives?





## Unstable Definitions

### What is "efficient" varies

- What are the operations that we **can** use?
- What are the associated **costs**?

How to get the best security for a given price?

### What is "secure" varies

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- Should the primitive work in many context? Modularity vs. Single use
- Do we care about nonce-misuse? Robustness vs. "not our problem"

How do we define the **security** that the primitive must provide?

What are the relevant forms of cryptanalysis?

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## Web Encryption

Application

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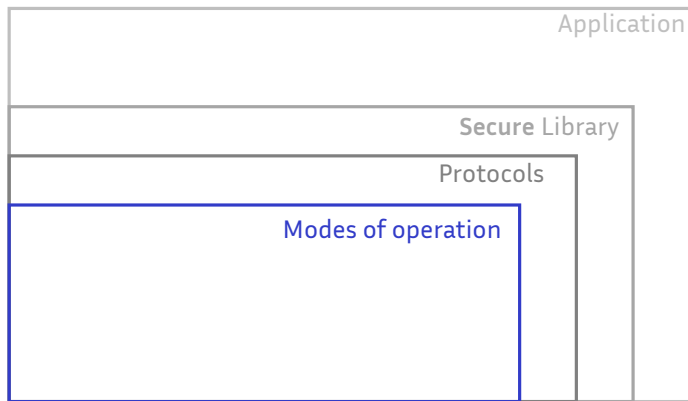


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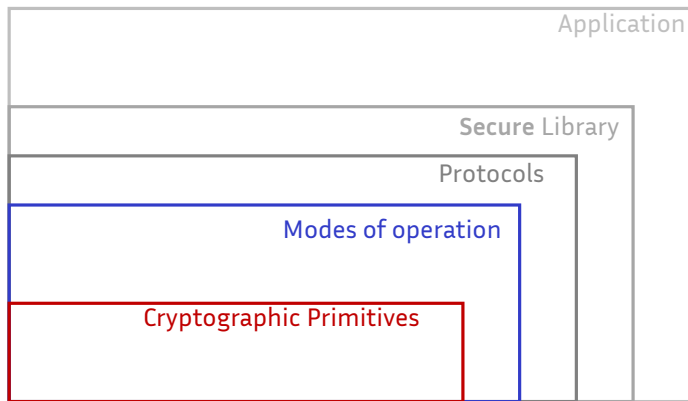




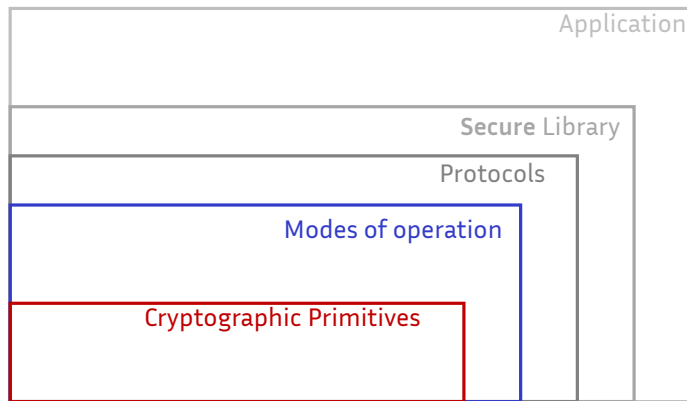
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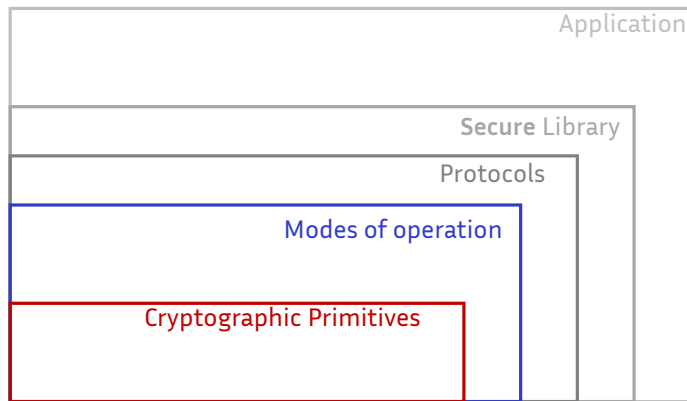


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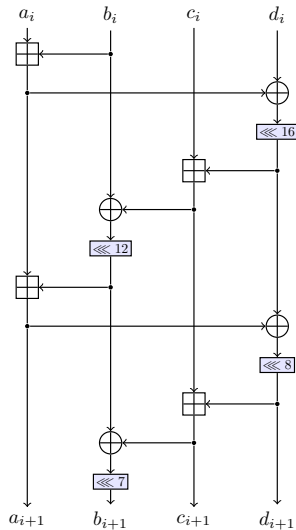
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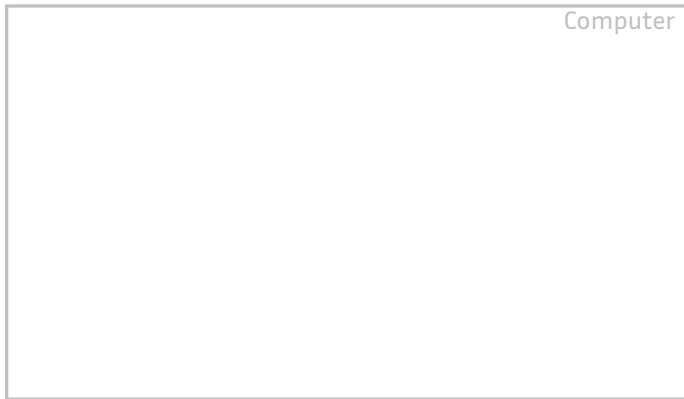
- We want **software efficient** (computer and smartphone but not micro-controllers) efficient **AEAD** for packets of a few tens to a few billion bytes.
- AES-GCM; Chacha-poly1305.

## What Chacha looks like



- Addition / Rotation / XOR
- 256-bit key
- 512-bit state
- Defined over 32-bit words

## RAM Encryption



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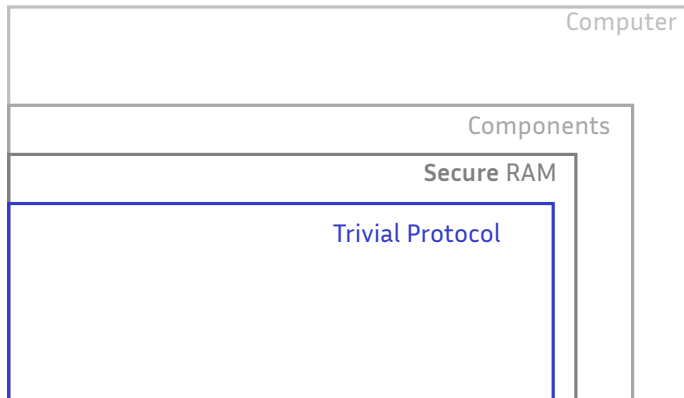


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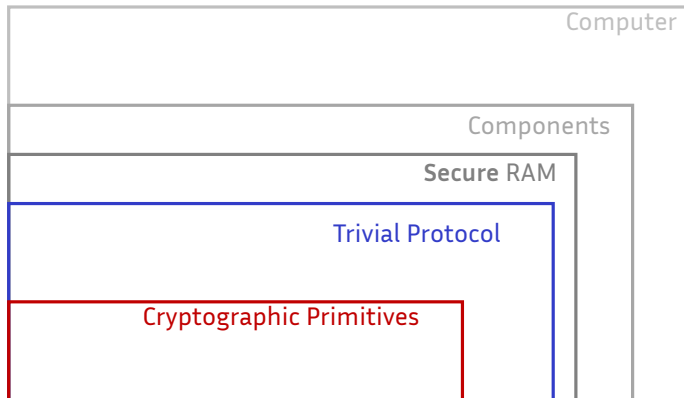




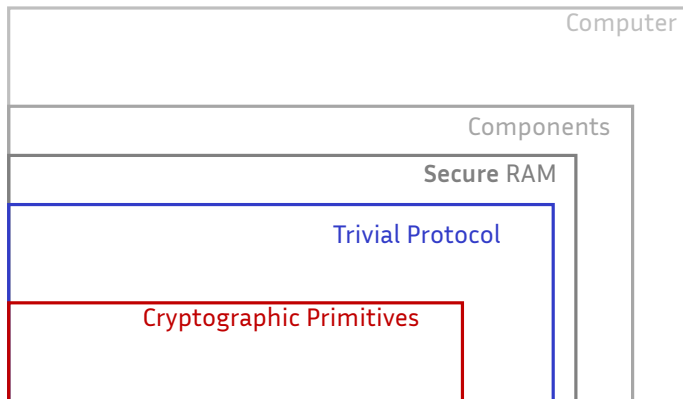
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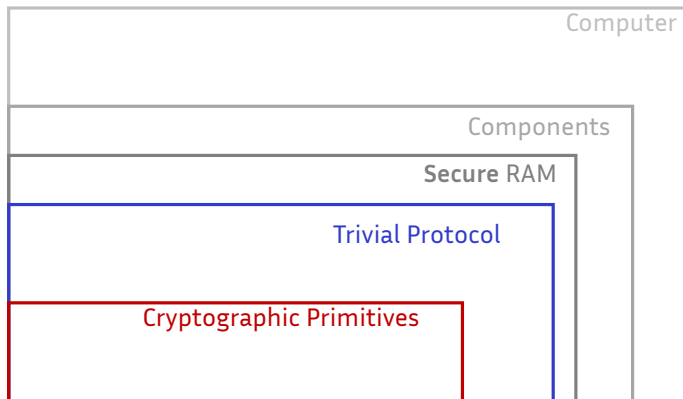


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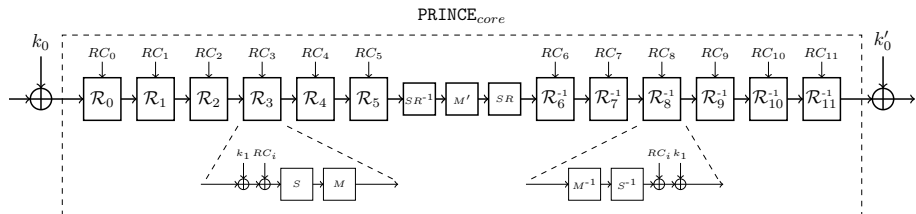
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- PRINCE? QARMA? not so clear at this stage.

## What PRINCE looks like



- 4-bit S-box optimized for hardware
- 2 different matrices
- FX construction
- "α-reflexion"
- inverse rounds used in the second half

## Some Constants

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- A symmetric primitive is a very **small** (but crucial) cog in a very big machine,
- there are many **different** "big machines", and
- this has a **huge influence** on what the primitive looks like.



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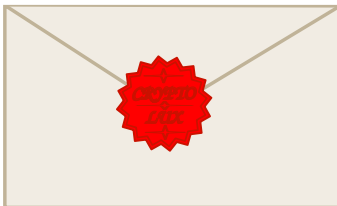
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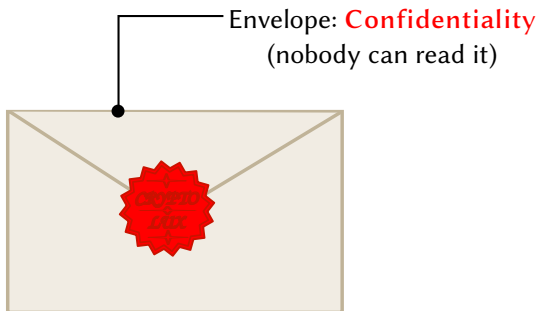
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Usually, we secure **data** (at rest or in transit).



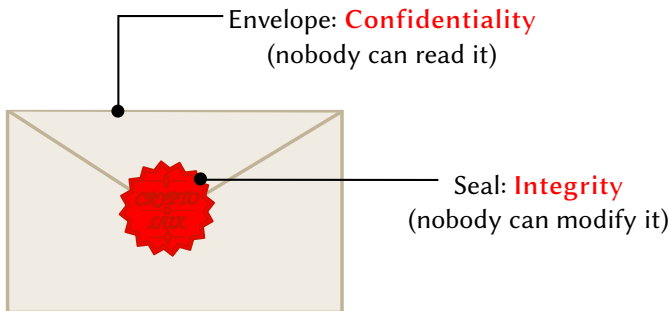
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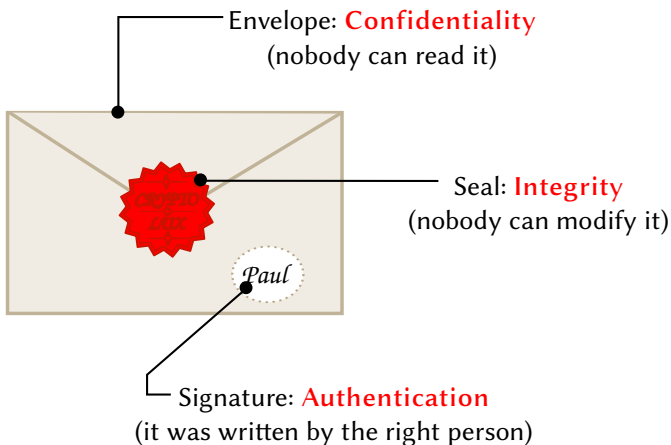
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## Securing Computation

More and more protocols intend to secure **computations**.

**FHE** Fully Homomorphic Encryption

**MPC** Multi Party Computations

**ZK-\*** Zero Knowledge- [ proof, argument... ]

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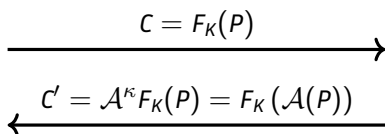
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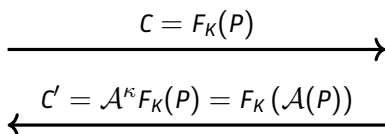
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## An example of (not F)HE

XOR-ing a constant to a ciphertext obtained using a stream cipher XORs the same constant in the plaintext:

$$C \oplus t = (P \oplus K) \oplus t = (P \oplus t) \oplus K$$

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## Goal

Allow multiple parties to evaluate a function together even if some parties are not trustworthy.

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## Applications

- Masking (the side-channel attack counter-measure)
- MPC-in-the-head paradigm (e.g. for Picnic signatures)
- ...

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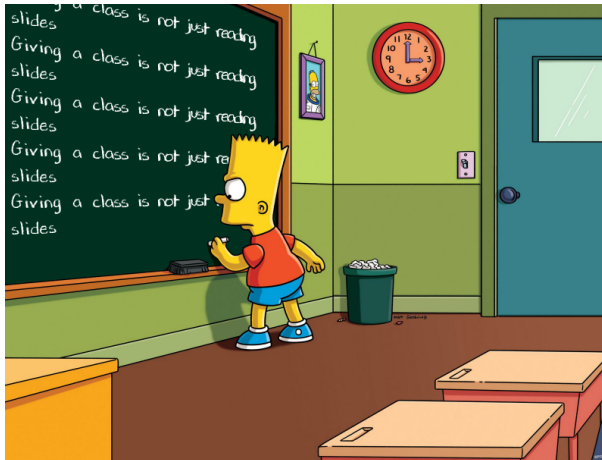
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- BLOCKCHAIN!!!

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## Arithmetization: General Principle



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Verifying if  $y = c(ax + b)^{10} + x$  in R1CS

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$$2 \quad t_1 = t_0 + b$$

$$3 \quad t_2 = t_1 \times t_1$$

$$4 \quad t_3 = t_2 \times t_2$$

$$5 \quad t_4 = t_3 \times t_3$$

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- How to turn a computation into an arithmetic circuit depends on the operations allowed
- Its cost is also arithmetization-dependent—**though low degree is usually welcome!**

## A not basic at all example of arithmetization

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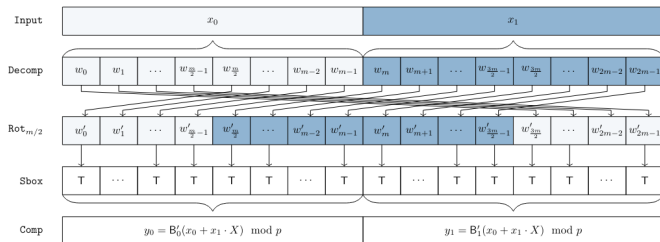
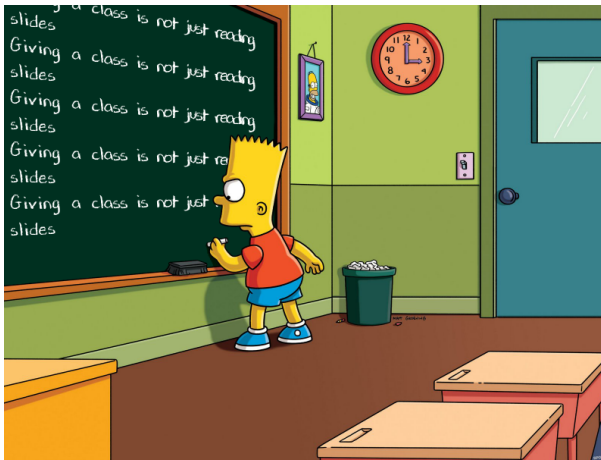


Figure 3: The Bar layer  $B' : \mathbb{F}_{p^n} \rightarrow \mathbb{F}_{p^n}$  for  $n = 2$  in detail, including the decomposition, the rotation, the S-box, and the composition.

source: *Skyscraper: Fast Hashing on Big Primes*,  
<https://eprint.iacr.org/2025/058.pdf>

## "Arithmetization-Oriented"? Evaluation vs. Verification

(the term was coined in [AAB<sup>+</sup>20])



## Symmetric Techniques for Advanced Protocols

MPC

FHE

ZK

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FHE

ZK

Masking

BGV

R1CS

MPC-in-the-head  
(signatures...)

FV

AIR  
...

PCF

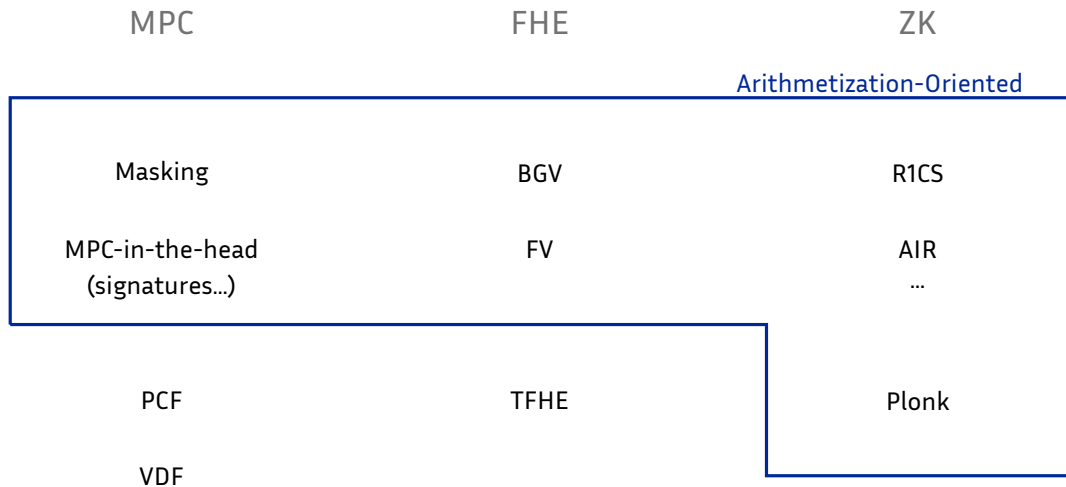
TFHE

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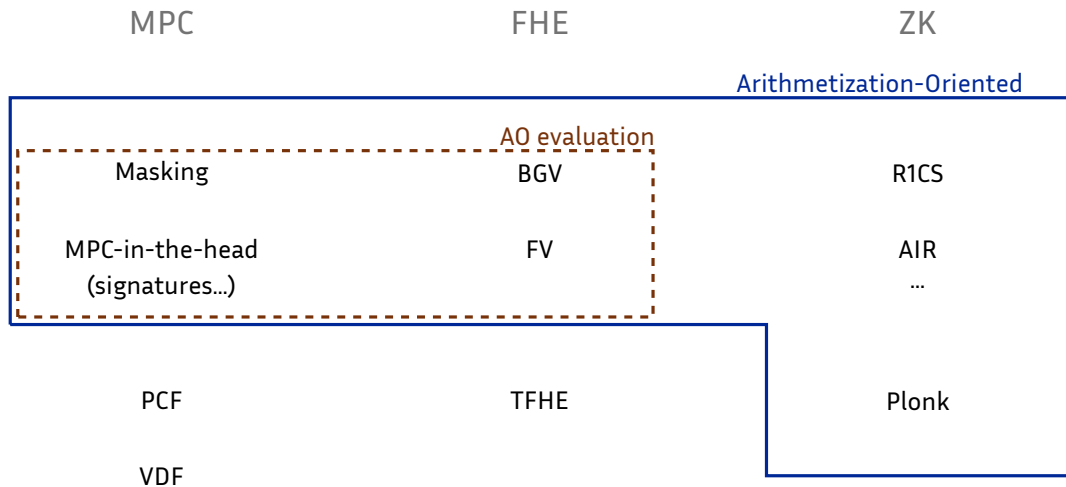
VDF



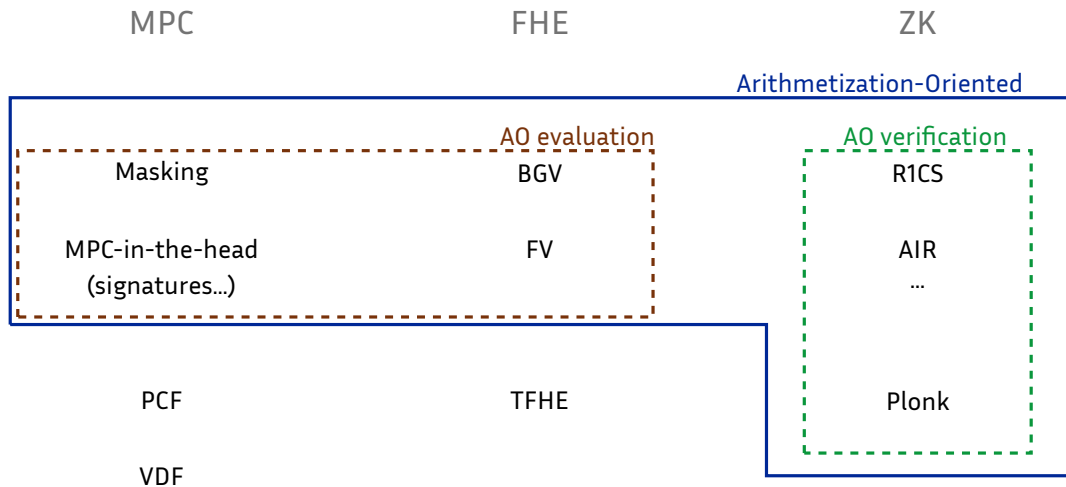
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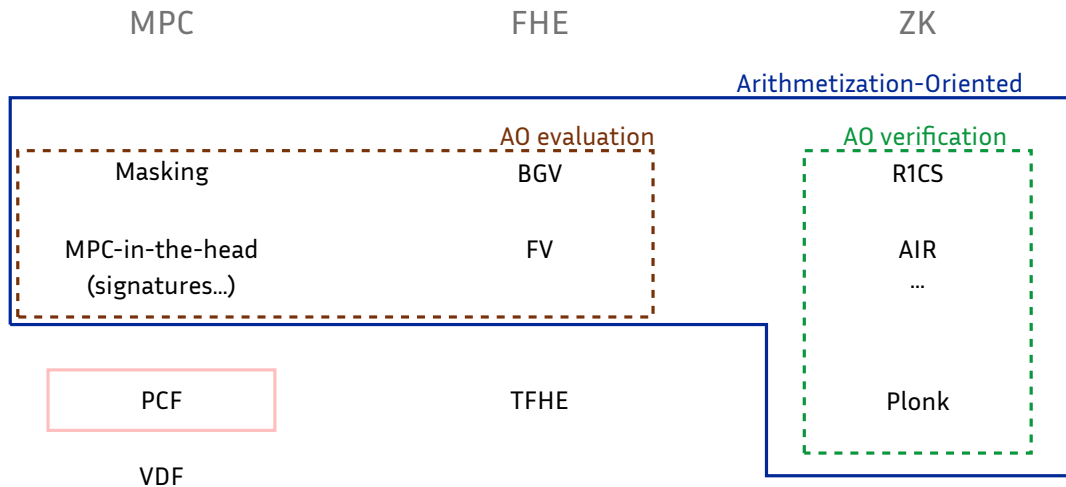
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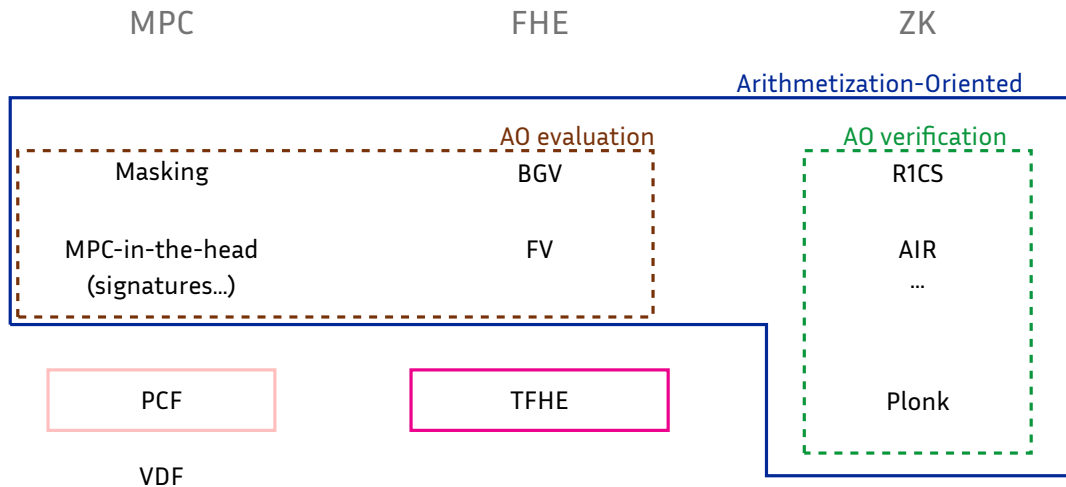
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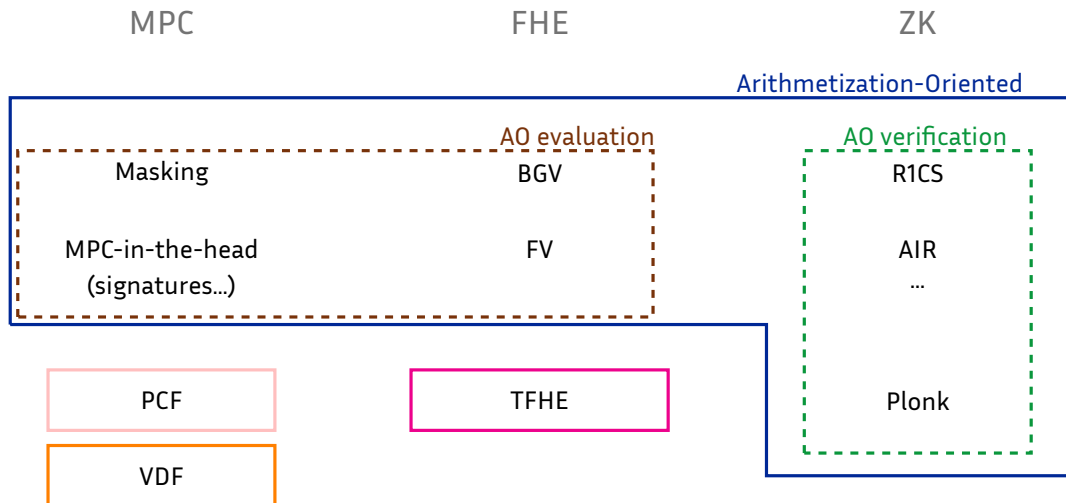
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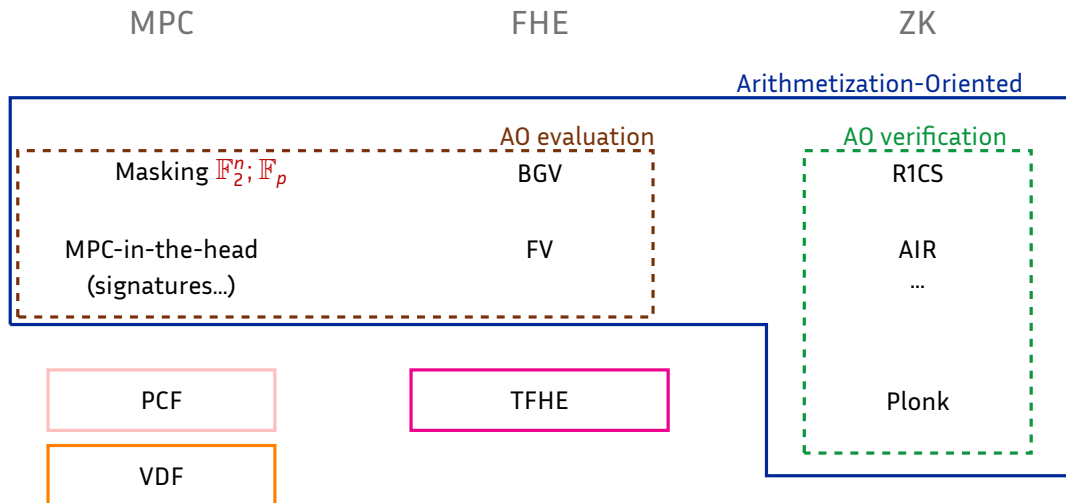
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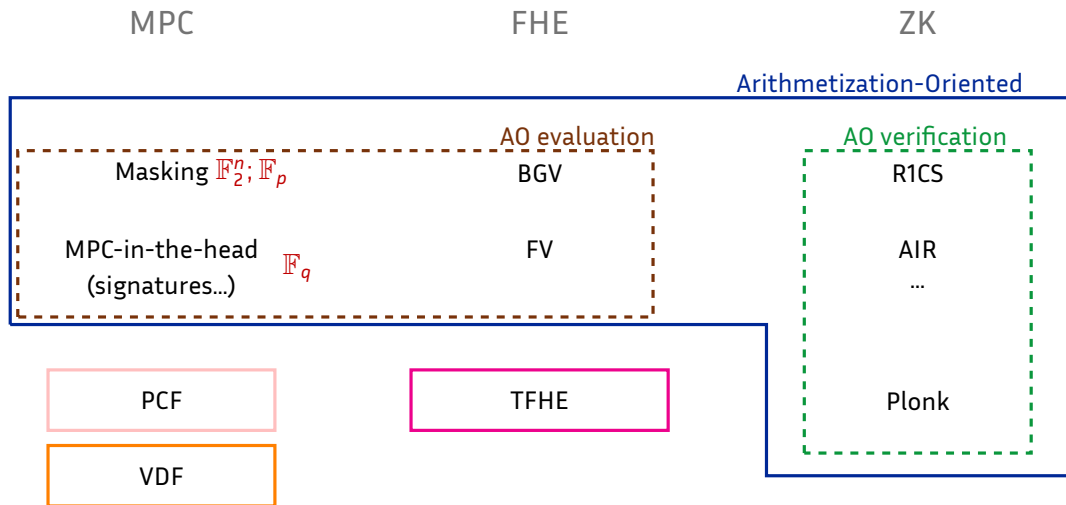
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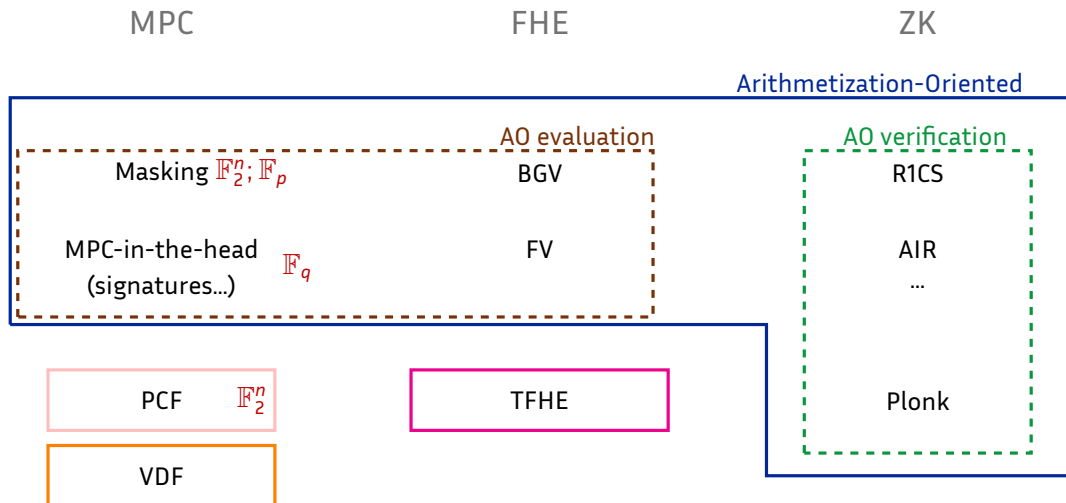


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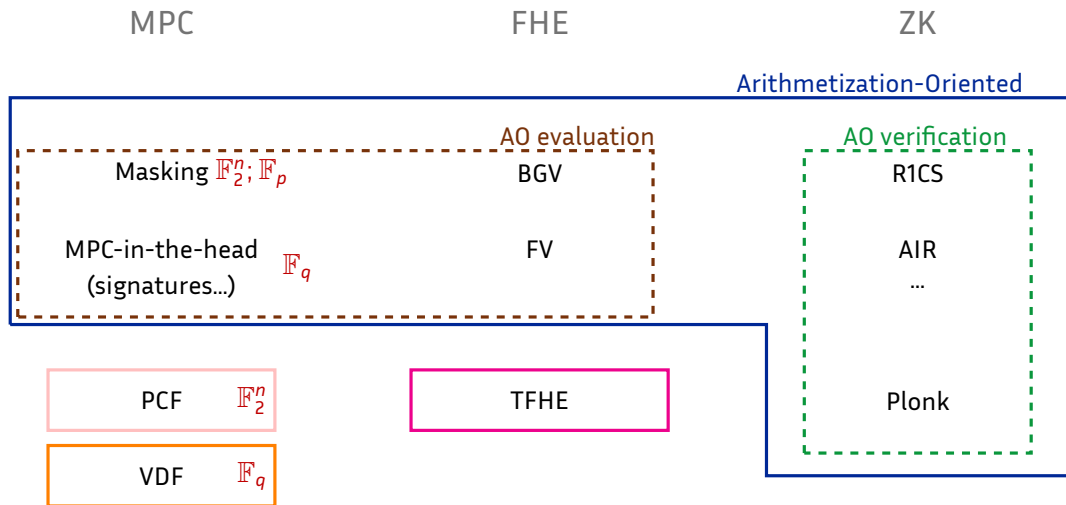




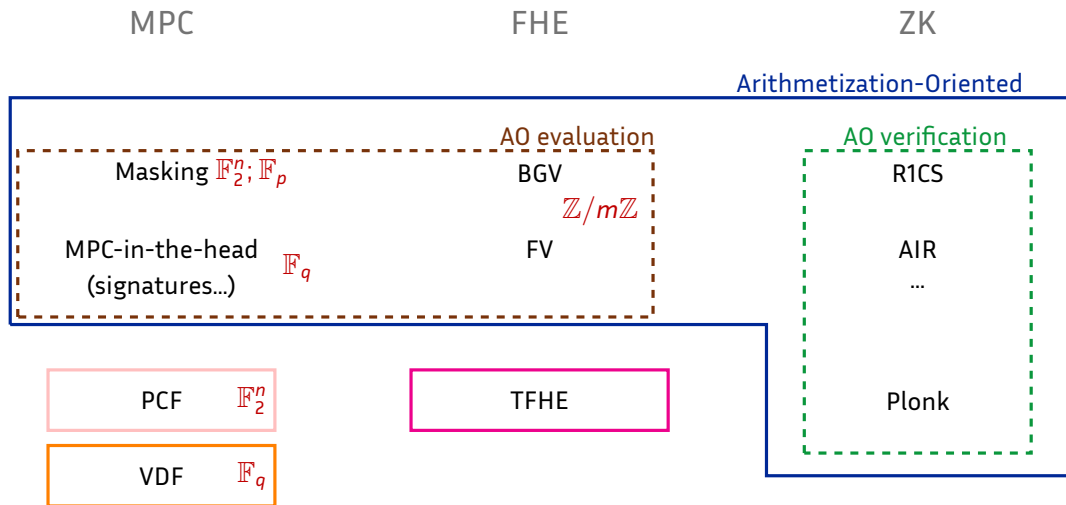
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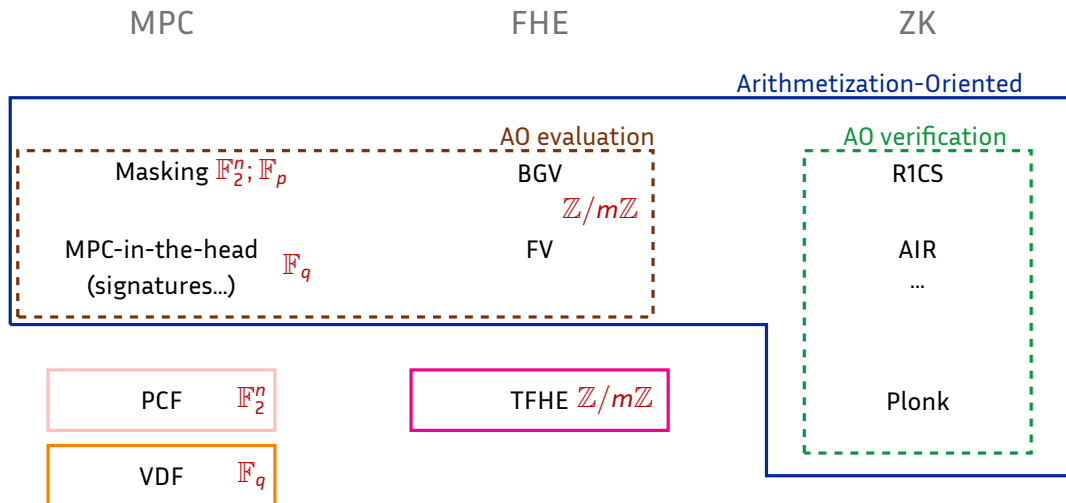
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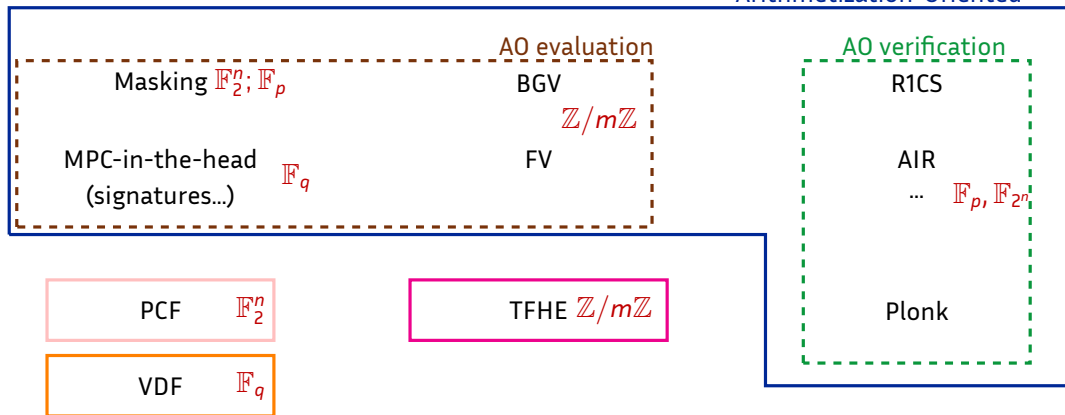
# Symmetric Techniques for Advanced Protocols

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Arithmetization-Oriented



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**beware of algebraic attacks!**
- **However** good design approaches are *inherently* good design approaches

Working over  $\mathbb{F}_q$  (especially if low degree arithmetizations are needed) introduces new challenges, **but solutions will rely on tried and true methods**.

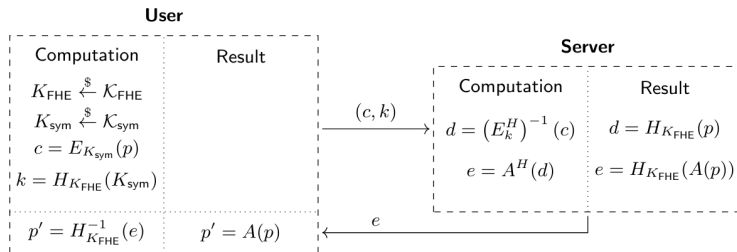
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  - FHE: Stream ciphers for transciphering
  - MPC: low multiplicative depth, and PCF
  - ZK: Hash function with AO verification
- 4 Conclusion

# Transciphering



**Fig. 1:** The principle of transciphering, where  $E$  is a symmetric cipher (with secret key  $K_{\text{sym}}$  sampled from the space  $\mathcal{K}_{\text{sym}}$ ),  $H$  is a fully homomorphic cipher (with private key  $K_{\text{FHE}}$  sampled from the space  $\mathcal{K}_{\text{FHE}}$ ),  $E^H$  is a homomorphic evaluation of  $E$ ,  $A$  corresponds to some arbitrary operations, and  $A^H$  to their homomorphic evaluation.

source: *Transistor: a TFHE-friendly Stream Cipher*

<https://eprint.iacr.org/2025/282>

## The case of TFHE

Operates on  $\mathbb{Z}/m\mathbb{Z}$ , where  $m$  can be anything, though: more efficient if  $m$  is smaller.

### Operations allowed

**Linear Combinations**  $\sum_i \alpha_i x_i$ , where the  $\alpha_i$  are constant while  $x_i$  is input/key dependent.

- Costs almost nothing in terms of time/communication complexity...
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**PBS** (Programmable BootStrap)  $y \leftarrow S(x)$

- Very time consuming...
- But resets the noise to a **base level**



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## The case of TFHE

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- If the ring size is even, it is better if it is **nega-cyclic** ( $S(x + 2^{n-1}) = -S(x)$ )

## TFHE: corresponding stream ciphers

Elisabeth-4 [CHMS22] ;  $q = 2^4$

Uses a constant key register on which index-dependent non-linear functions are applied.

Can be linearized [GBJR23]

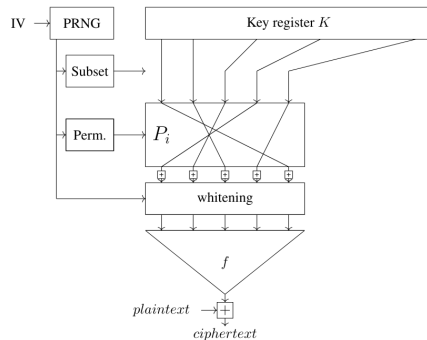


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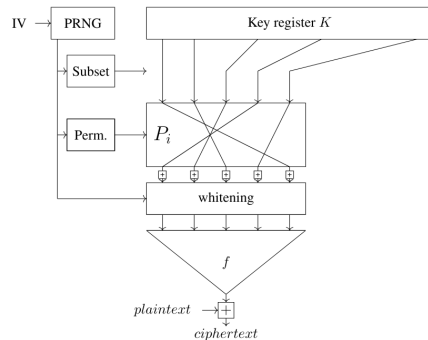


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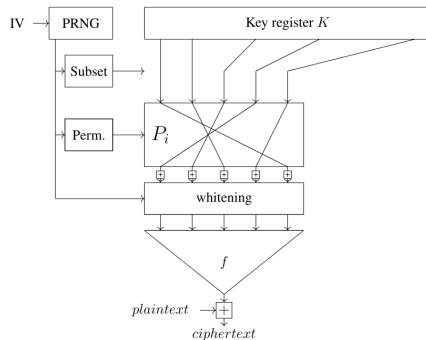


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**Transistor** [BBB<sup>+</sup>25] ;  $q = 2^4 + 1$   
SNOW-like round structure  
See you at Anne's invited talk :D

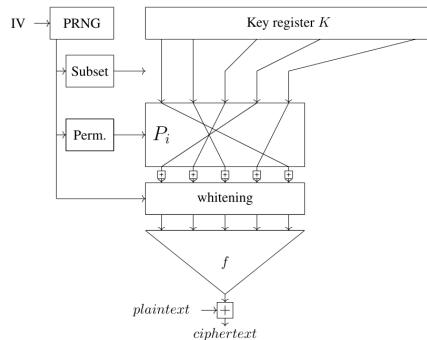


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Use very few rounds with a low degree.  
Rely on large, randomly generated, nonce-dependent matrices.

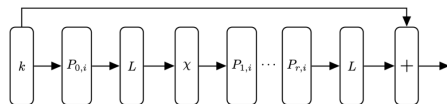


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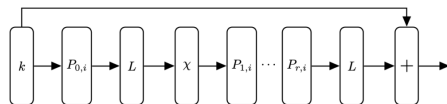


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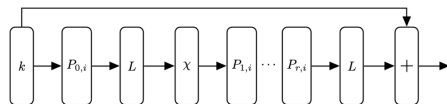


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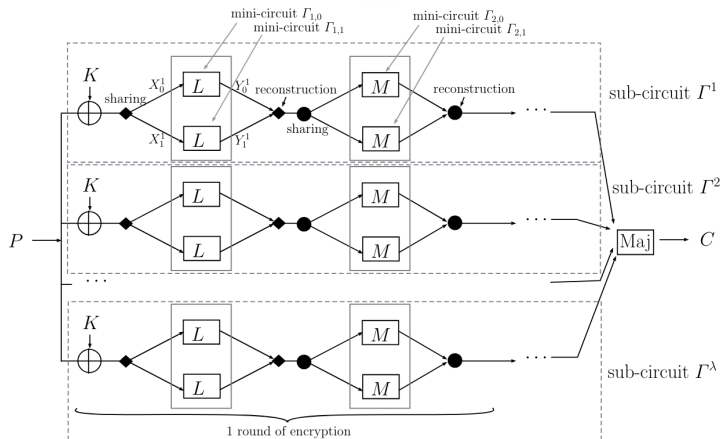
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## Trojan Resilience



source: *MOE: Multiplication Operated Encryption with Trojan Resilience*  
<https://tosc.iacr.org/index.php/ToSC/article/view/8834>

## MPC-Friendly Encryption

**LowMC** [ARS<sup>+</sup>15]  $q = 2$

SPN with partial layer of quadratic S-boxes.

Rely on large, randomly generated matrices.

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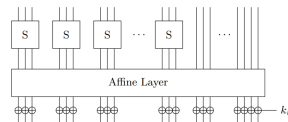


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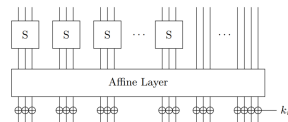


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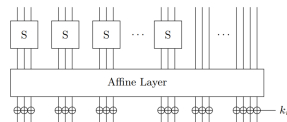


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**MOE** [BFL<sup>+</sup>21]  $q = 2^{128}$ ,  $m = 2^{128}$

Dedicated structure with linear operations in  $\mathbb{F}_q$  and  
 $\mathbb{Z}/q\mathbb{Z}$ . Intended for hardware trojan resilience.

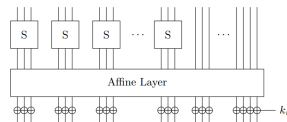


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**VDLPN** [BCG<sup>+</sup>20]

$$f_k(x) = \bigoplus_{i=1}^D \bigoplus_{j=1}^w \bigwedge_{\ell=1}^i (x_{i,j,\ell} \oplus k_{i,j,\ell}),$$

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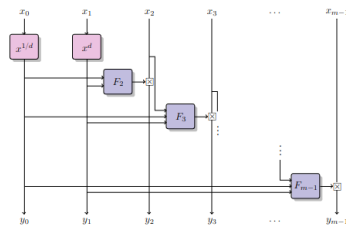
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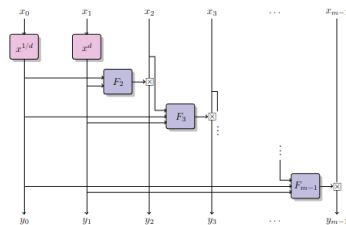
source: *Cryptanalysis and design of symmetric primitives defined over large finite fields*, PhD thesis of C.

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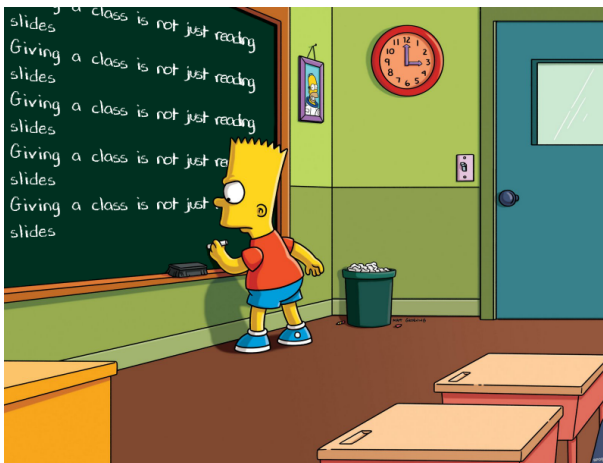
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- Anemoi** [BBC<sup>+</sup>23]  $q = 2^n$  or large prime  
 Uses the “Flystel”, a high degree S-box  
 CCZ-equivalent to a function of low degree.



source: *Cryptanalysis and design of symmetric primitives defined over large finite fields*, PhD thesis of C. Bouvier

## Arithmetization-Oriented Verification: CCZ-equivalence?



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**Coffee Break!**

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