	Title: *			Sheet: 21 of 27
	Model: EM1	Document Number: 21		Iss-Rev: *
	Author: *	Checked by: *		Date: *
	Approved by: *			
	File name: LD39050PU33R.SchDoc			

A

A

B

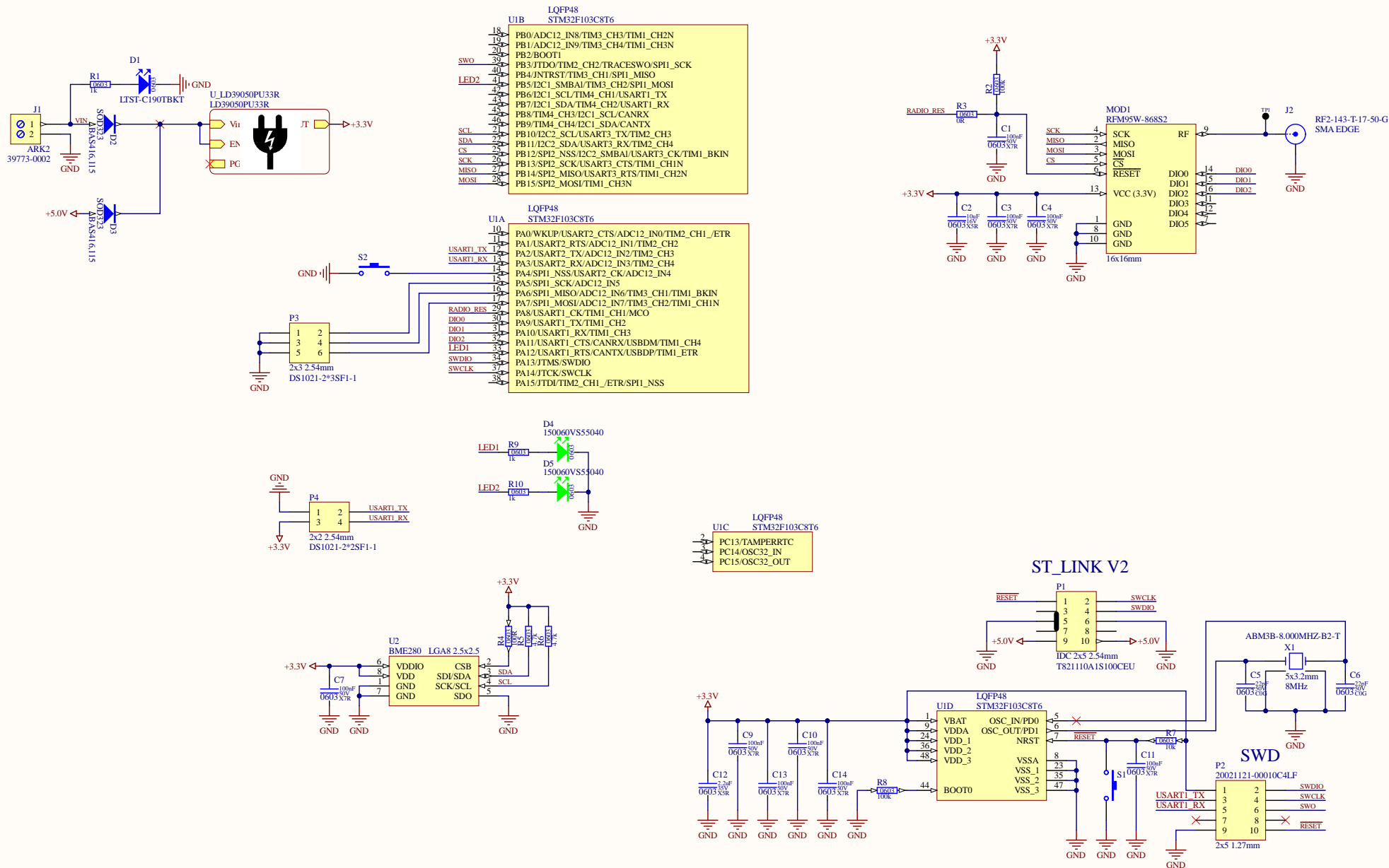
B

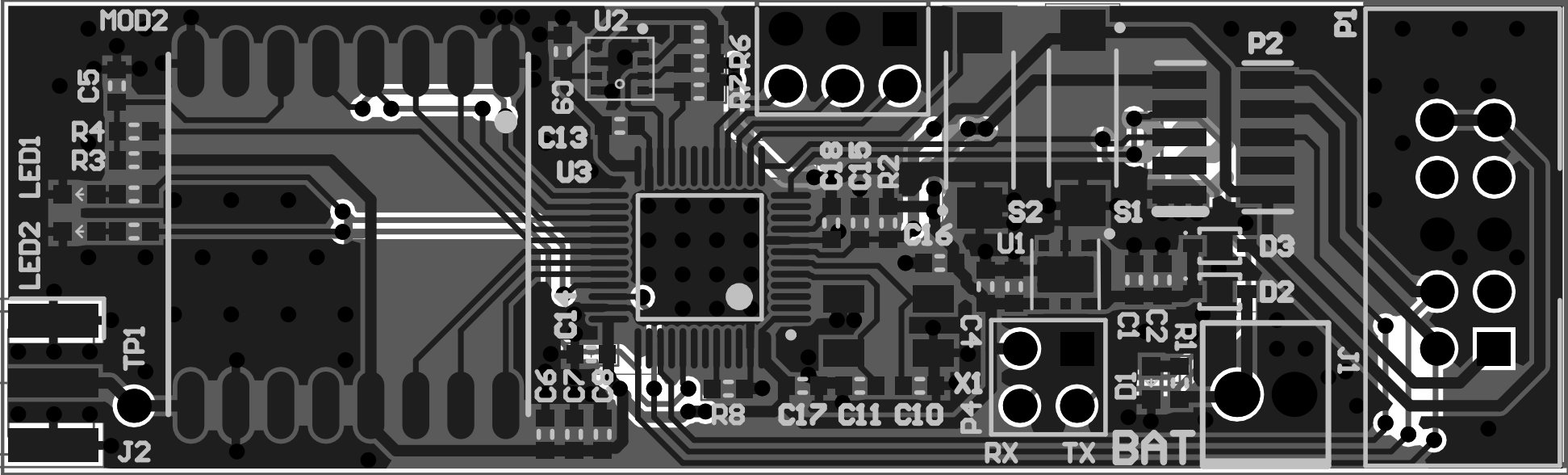
C

C

D

D





Comment	Description	Designator	Footprint	LibRef	Quantity
Ceramic X7R	Capacitor	C1, C2, C3, C4, C5, C7, C8, C9, C13, C14, C15, C17, C18	C_0603_1608	C_1uF_25V_X7R_0603_10, C_100nF_50V_X7R_0603_5	13
Ceramic X5R	Capacitor	C6, C16	C_0603_1608	C_10uF_16V_X5R_0603_20, C_2.2uF_35V_X5R_0603_10	2
Ceramic COG	Capacitor	C10, C11	C_0603_1608	C_22pF_50V_COG_0603_5	2
Blue Led	LED_BLUE	D1	OL_0603_1608	OL_B_0603	1
BAS416		D2, D3	SOD323	D_BAS416_0A5_75V_SOD323	2
Green LED 0603	LED GREEN	D4, D5	OL_0603_1608	OL_G_0603_150060VS55040	2
ARK2	ARK Screw terminal, 2-Pin	J1	J_ARK2_2.54	J_39773-0002	1
SMA EDGE	SMA connector	J2	J_SMA_EDGE_6.25	J_SMA_EDGE_6.25	1
RFM95(W) - Low Power Long Range Transceiver Module + LoRa	RFM95/96/97/98(W) - Low Power Long Range Transceiver Module	MOD2	MOD_RFM95	MOD_RFM95W-868S2	1
IDC	IDC Header	P1	P_IDC_2x5_2.54	P_IDC_2x5_2.54	1
Jtag	Header, 10-Pin, Dual row	P2	P_HEADER_2x5_1.27	P_HEADER_2x5_1.27	1
Goldpin male	Pin Header	P3	P_HEADER_2x3_2.54	P_HEADER_2x3_2.54	1
Goldpin male	Pin Header	P4	P_HEADER_2x2_2.54	P_HEADER_2x2_2.54	1
Resistor		R1, R2, R3, R4, R5, R6, R7, R8, R9, R10	R_0603_1608	R_1k_0603_1, R_10k_0603_1, R_100k_0603_1, R_OR_0603_5, R_100R_0603_1, R_4.7k_0603_1	10
3x6mm	Tact Switch	S1, S2	S_TACTSWITCH3x6	S_TACTSWITCH	2
Single pin	Testpoint	TP1	TP_1PIN	TP_1PIN	1
LD39050PU33R		U1	DFN6_3x3	VL_LD39050PU33R	1
Combined humidity and pressure sensor		U2	LGA8_2.5x2.5	U_BME280	1
STM32F103C8T6		U3	LQFP48_7x7	UP_STM32F103C8T6	1
8MHz		X1	X_ABM3B_5X3X11	X_ABM3B-8.000MHZ-B2-T	1