Horizontal and Vertical Opening Scatter Plot for Rate of 9.6-optimized MSP A FPGA-TX1-RX16-MSP C FPGA 100 -MSP A FPGA-TX2-RX15-MSP C FPGA MSP C FPGA-TX3-RX7-MSP A FPGA MSP C FPGA-TX4-RX6-MSP A FPGA TRP FPGA-TX5-RX5-MSP A FPGA TRP FPGA-J1-J1-TRP FPGA TRP FPGA-J3-J3-TRP FPGA 90 MSP A FPGA-IC39-IC4-TRP FPGA MSP C FPGA-IC39-IC15-TRP FPGA MSP A FPGA-TX1-RX18-MSP C FPGA Vertical Opening Percentage MSP A FPGA-TX2-RX17-MSP\_C\_FPGA MSP C FPGA-TX3-RX9-MSP A FPGA 80 -MSP C FPGA-TX4-RX8-MSP A FPGA TRP FPGA-TX5-RX14-MSP C FPGA MSP A FPGA-TX1-RX11-MSP C FPGA MSP A FPGA-TX2-RX10-MSP C FPGA MSP C FPGA-TX3-RX2-MSP A FPGA MSP C FPGA-TX4-RX1-MSP A FPGA 70 -MSP A FPGA-TX1-RX13-MSP C FPGA MSP A FPGA-TX2-RX12-MSP C FPGA MSP C FPGA-TX3-RX4-MSP A FPGA MSP C FPGA-TX4-RX3-MSP A FPGA 60 50 50 60 70 80 90 100 Horizontal Opening Percentage