Horizontal and Vertical Opening Scatter Plot for Rate of 6.4 default MSP A FPGA-TX1-RX13-MSP C FPGA 100 MSP A FPGA-TX2-RX12-MSP C FPGA MSP C FPGA-TX3-RX4-MSP A FPGA MSP C FPGA-TX4-RX3-MSP A FPGA MSP A FPGA-TX1-RX11-MSP C FPGA MSP A FPGA-TX2-RX10-MSP C FPGA MSP C FPGA-TX3-RX2-MSP A FPGA 90 MSP C FPGA-TX4-RX1-MSP A FPGA MSP A FPGA-TX1-RX16-MSP C FPGA MSP A FPGA-TX2-RX15-MSP C FPGA Vertical Opening Percentage MSP C FPGA-TX3-RX7-MSP A FPGA MSP C FPGA-TX4-RX6-MSP A FPGA 80 -MSP A FPGA-TX1-RX18-MSP C FPGA MSP A FPGA-TX2-RX17-MSP C FPGA MSP C FPGA-TX3-RX9-MSP A FPGA MSP C FPGA-TX4-RX8-MSP A FPGA TRP FPGA-TX5-RX14-MSP_C_FPGA MSP A FPGA-IC39-IC4-TRP FPGA 70 -MSP C FPGA-IC39-IC15-TRP FPGA TRP_FPGA-J1-J1-TRP_FPGA TRP FPGA-TX5-RX5-MSP_A_FPGA 60 50 50 60 70 80 90 100 Horizontal Opening Percentage