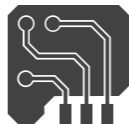


Board
netlist
files

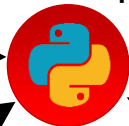


FPGA
package
files



Latex
reports

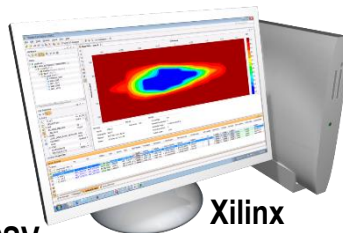
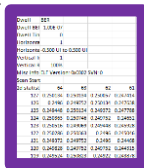
Python scripts



TCL control scripts

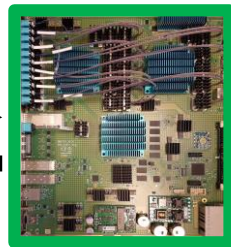


Eye CSV
files



Xilinx
Vivado

JTAG



DUT with
IBERT IP FW