

US 20240013846A1

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2024/0013846 A1

Yun et al.

Jan. 11, 2024 (43) Pub. Date:

MEMORY BUILT-IN SELF-TEST WITH **AUTOMATED REFERENCE TRIM** FEEDBACK FOR MEMORY SENSING

Applicant: Siemens Industry Software Inc.,

Plano, TX (US)

Inventors: Jongsin Yun, Portland, OR (US);

Benoit Nadeau-Dostie, Gatineau (CA); Martin Keim, Sherwood, OR (US)

Appl. No.: 17/756,963 (21)

PCT Filed: May 28, 2020 (22)

PCT No.: PCT/US2020/034860 (86)

§ 371 (c)(1),

Jun. 7, 2022 (2) Date:

Related U.S. Application Data

Provisional application No. 62/945,335, filed on Dec. 9, 2019.

Publication Classification

(51)Int. Cl. G11C 29/14 (2006.01)G11C 29/46 (2006.01)G11C 29/12 (2006.01)

U.S. Cl. (52)G11C 29/14 (2013.01); G11C 29/46

(2013.01); *G11C 29/1201* (2013.01)

ABSTRACT (57)

This application discloses a memory built-in self-test system to prompt a memory device to sense values of stored data using a reference trim during memory read operations. The memory built-in self-test system can automatically set the reference trim for the memory device. The memory built-in self-test system includes a memory built-in self-test controller to prompt the memory device to perform the memory read operations with different test values for the reference trim. The memory built-in self-test system also includes a trim feedback circuit to determine when the memory device fails to correctly sense the values of the stored data using the test values for the reference trim, and set the reference trim for the memory device based, at least in part, on the failures of the memory device to correctly sense the stored data.

