



US 20220215896A1

(19) **United States**(12) **Patent Application Publication**
Nadeau-Dostie et al.(10) **Pub. No.: US 2022/0215896 A1**(43) **Pub. Date: Jul. 7, 2022**(54) **METHOD AND APPARATUS FOR
PROCESSING MEMORY REPAIR
INFORMATION****Publication Classification**(51) **Int. Cl.***G11C 29/44* (2006.01)*G11C 29/40* (2006.01)*G11C 29/36* (2006.01)(52) **U.S. Cl.**CPC .. *G11C 29/4401* (2013.01); *G11C 29/3602*(2013.01); *G11C 29/36* (2013.01); *G11C 29/40*

(2013.01)

(71) Applicant: **Siemens Industry Software Inc**, Plano,
TX (US)(72) Inventors: **Benoit Nadeau-Dostie**, Gatineau (CA);
Luc Romain, Gatineau (CA)(21) Appl. No.: **17/604,805**(22) PCT Filed: **Aug. 27, 2019**(86) PCT No.: **PCT/US2019/048223**

§ 371 (c)(1),

(2) Date: **Oct. 19, 2021****Related U.S. Application Data**(60) Provisional application No. 62/836,100, filed on Apr.
19, 2019.

(57)

ABSTRACT

Systems and methods for repairing a memory. A method includes performing a repair analysis of the embedded memories to produce repair information. The method includes storing the repair information in the registers, where the registers are organized into groups having chains of identical length. The method includes performing collision detection between the repair information in each of the groups. The method includes merging the repair information in each of the groups. The method includes repairing the embedded memories using the merged repair information.

