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Sheet: /Power/

File: power.kicad\_sch

**Title: Clavier – Power supplies**

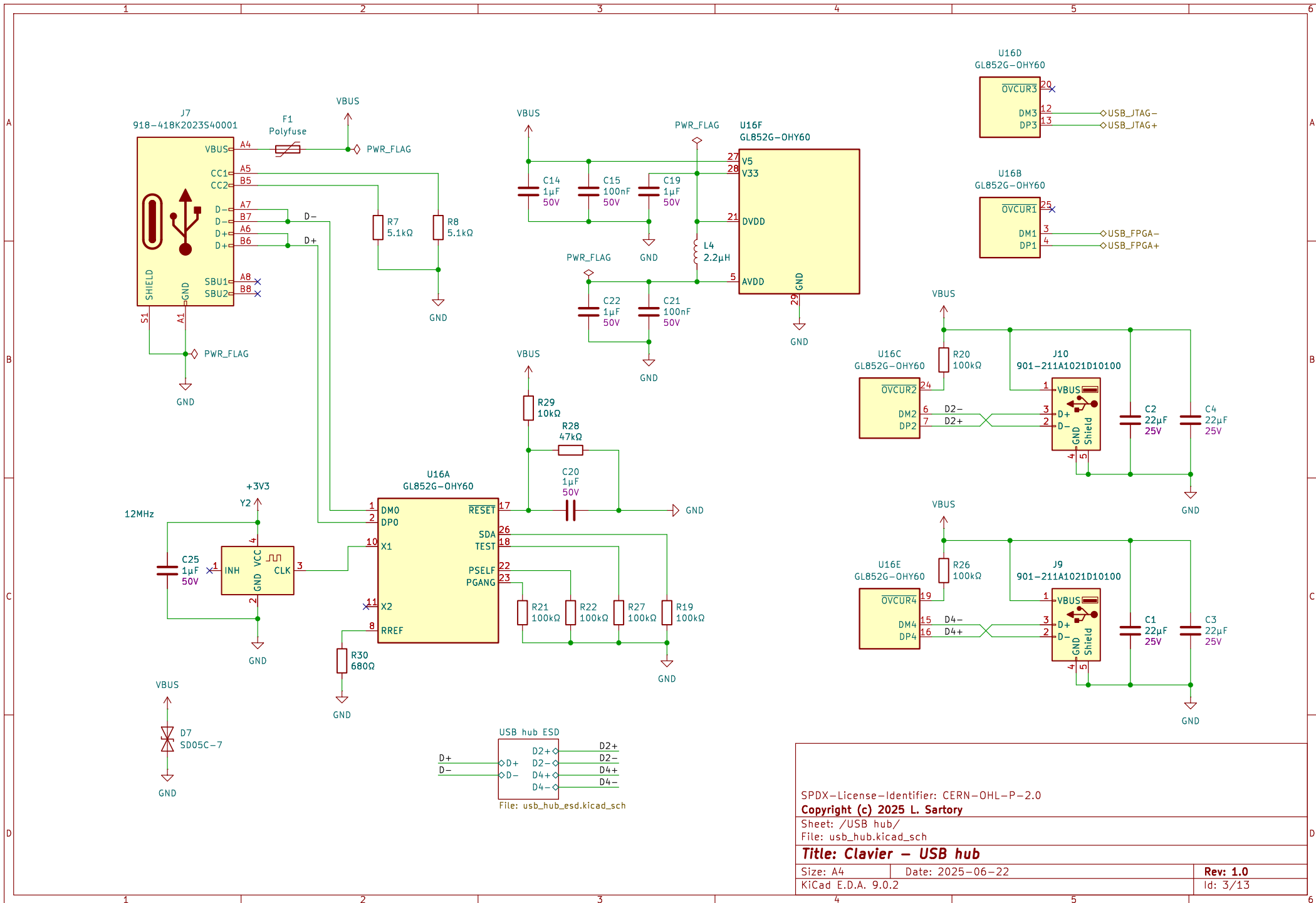
Size: A4

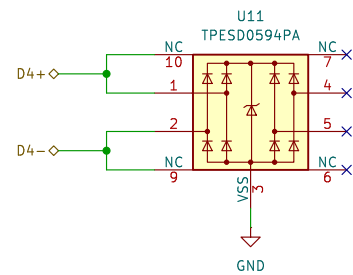
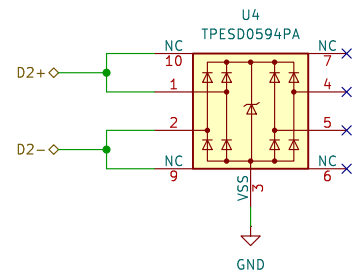
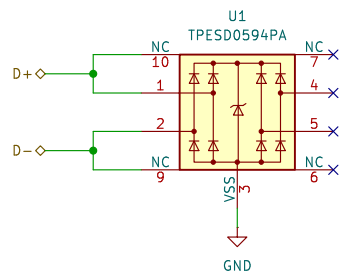
Date: 2025-06-22

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Sheet: /USB hub/USB hub ESD/

File: usb\_hub\_esd.kicad\_sch

**Title: Clavier – USB hub ESD protection**

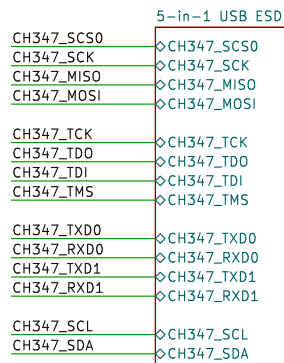
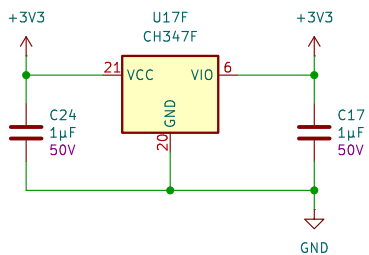
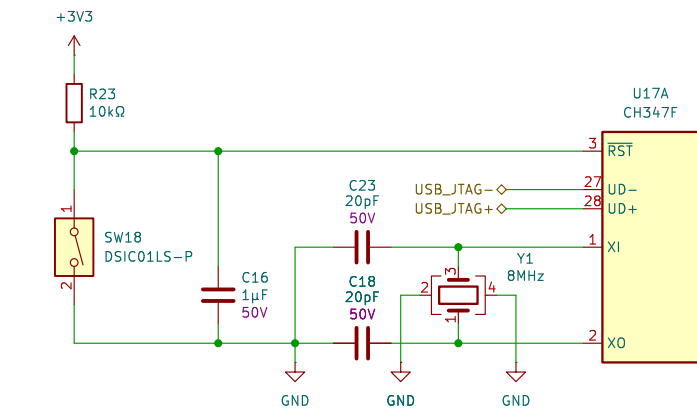
Size: A4

Date: 2025-06-22

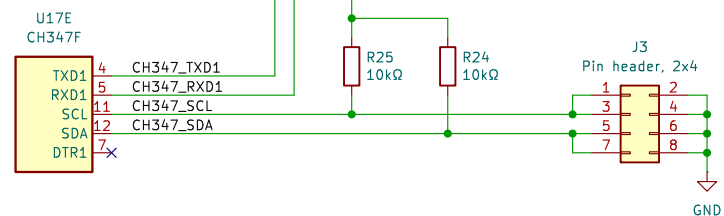
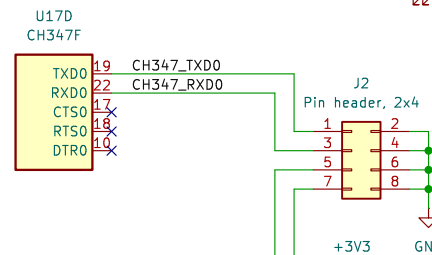
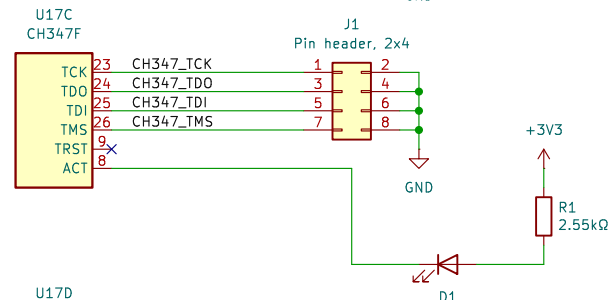
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File: usb\_jtag\_esd.kicad\_sch



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Sheet: /5-in-1 USB/

File: usb\_jtag.kicad\_sch

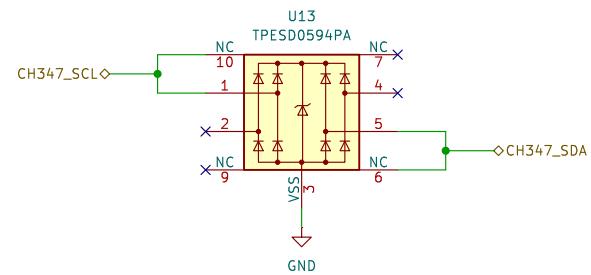
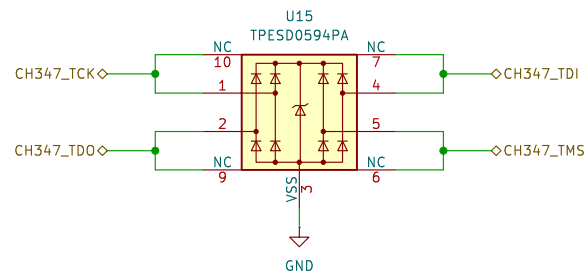
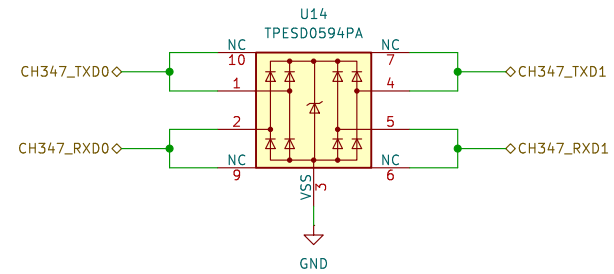
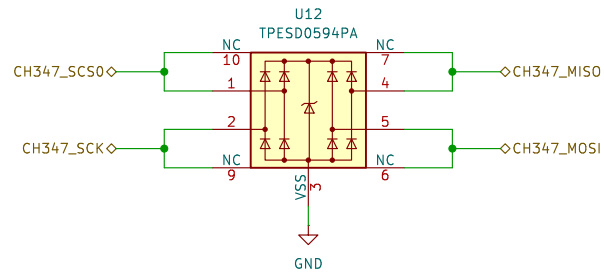
**Title: Clavier - 5-in-1 USB**

Size: A4 Date: 2025-06-22

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Sheet: /5-in-1 USB/5-in-1 USB ESD/

File: usb\_jtag\_esd.kicad\_sch

**Title: Clavier – 5-in-1 USB ESD protection**

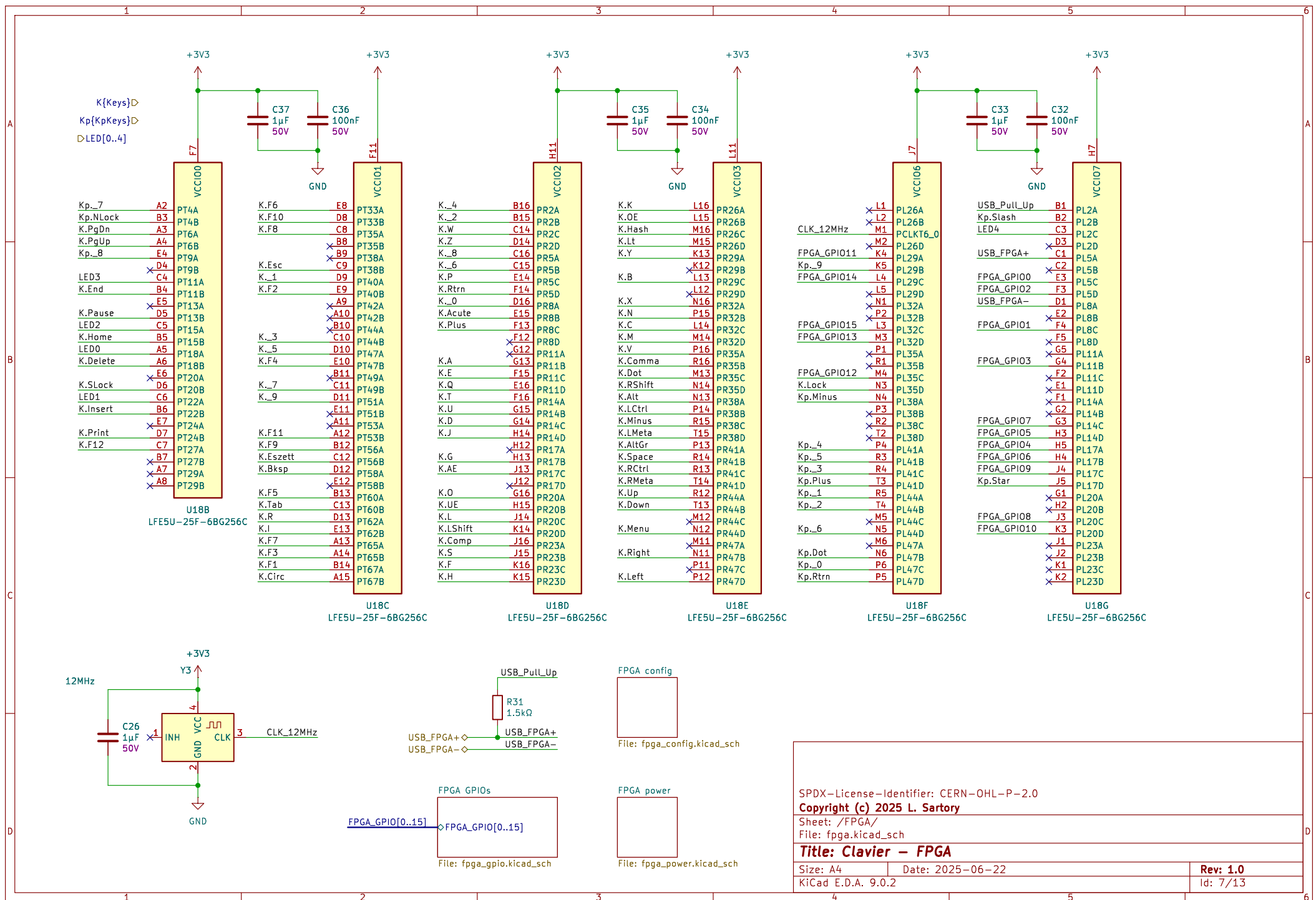
Size: A4

Date: 2025-06-22

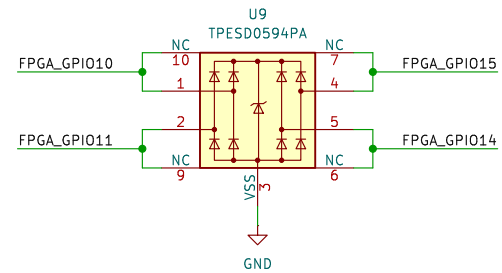
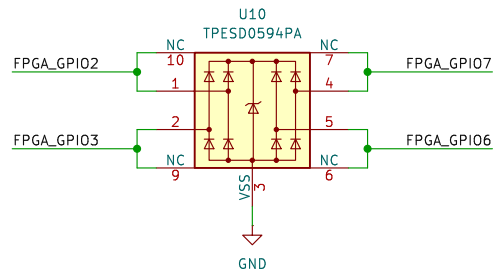
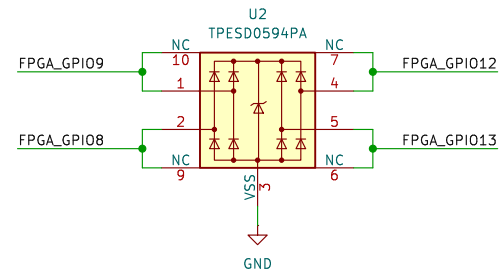
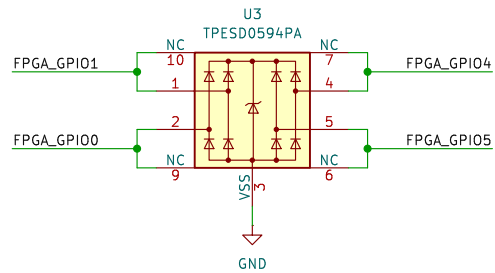
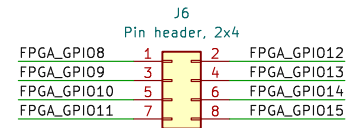
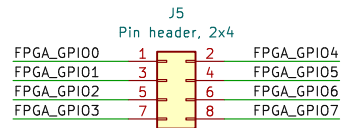
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FPGA\_GPIO[0..15]◇



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Sheet: /FPGA/FPGA GPIOs/

File: fpga\_gpio.kicad\_sch

Title: Clavier – FPGA GPIOs

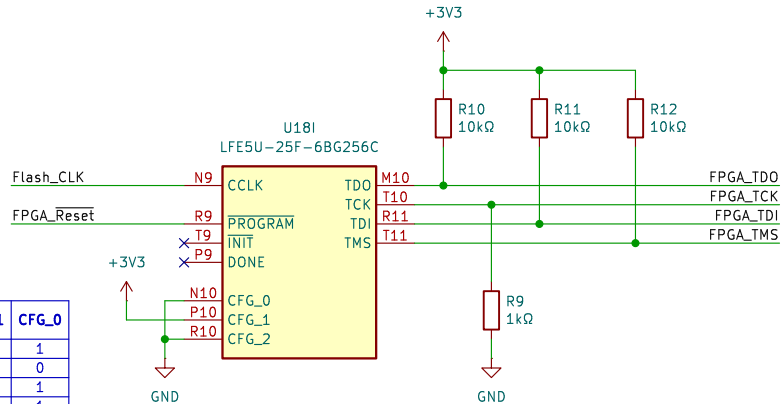
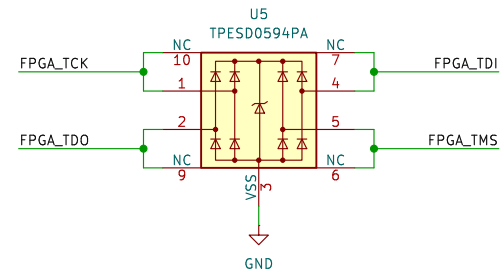
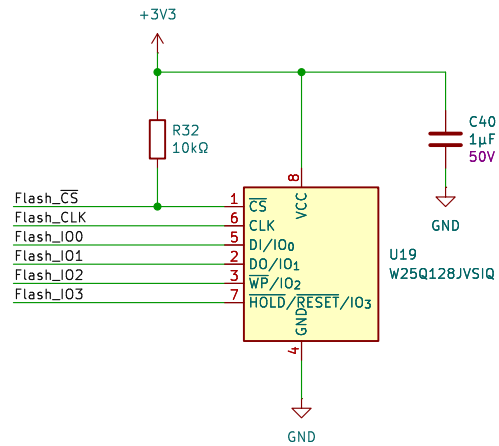
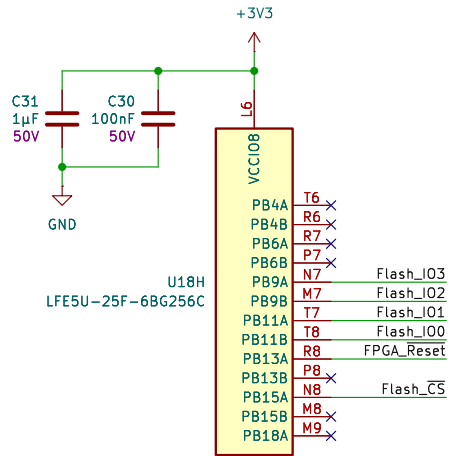
Size: A4 Date: 2025-06-22

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Configuration Mode	Clock	CFG_2	CFG_1	CFG_0
SSPI	CCLK	0	0	1
MSPI	MCLK	0	1	0
SCM	CCLK	1	0	1
SPCM	CCLK	1	1	1

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Sheet: /FPGA/FPGA config/

File: fpga\_config.kicad\_sch

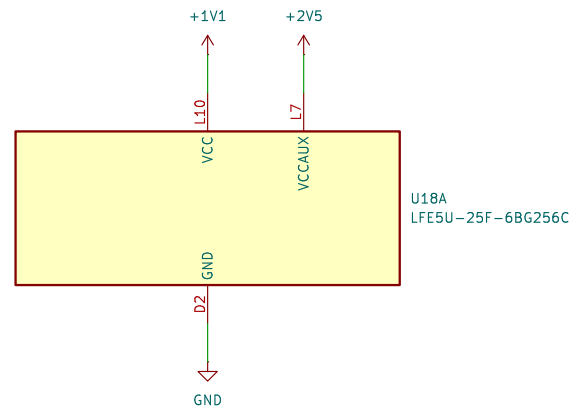
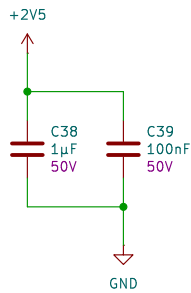
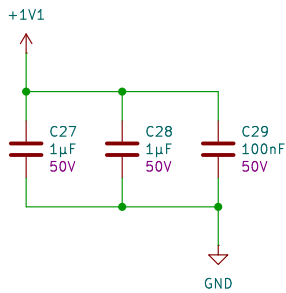
Title: Clavier – FPGA configuration

Size: A4 Date: 2025-06-22

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Sheet: /FPGA/FPGA power/

File: fpga\_power.kicad\_sch

**Title: Clavier – FPGA power**

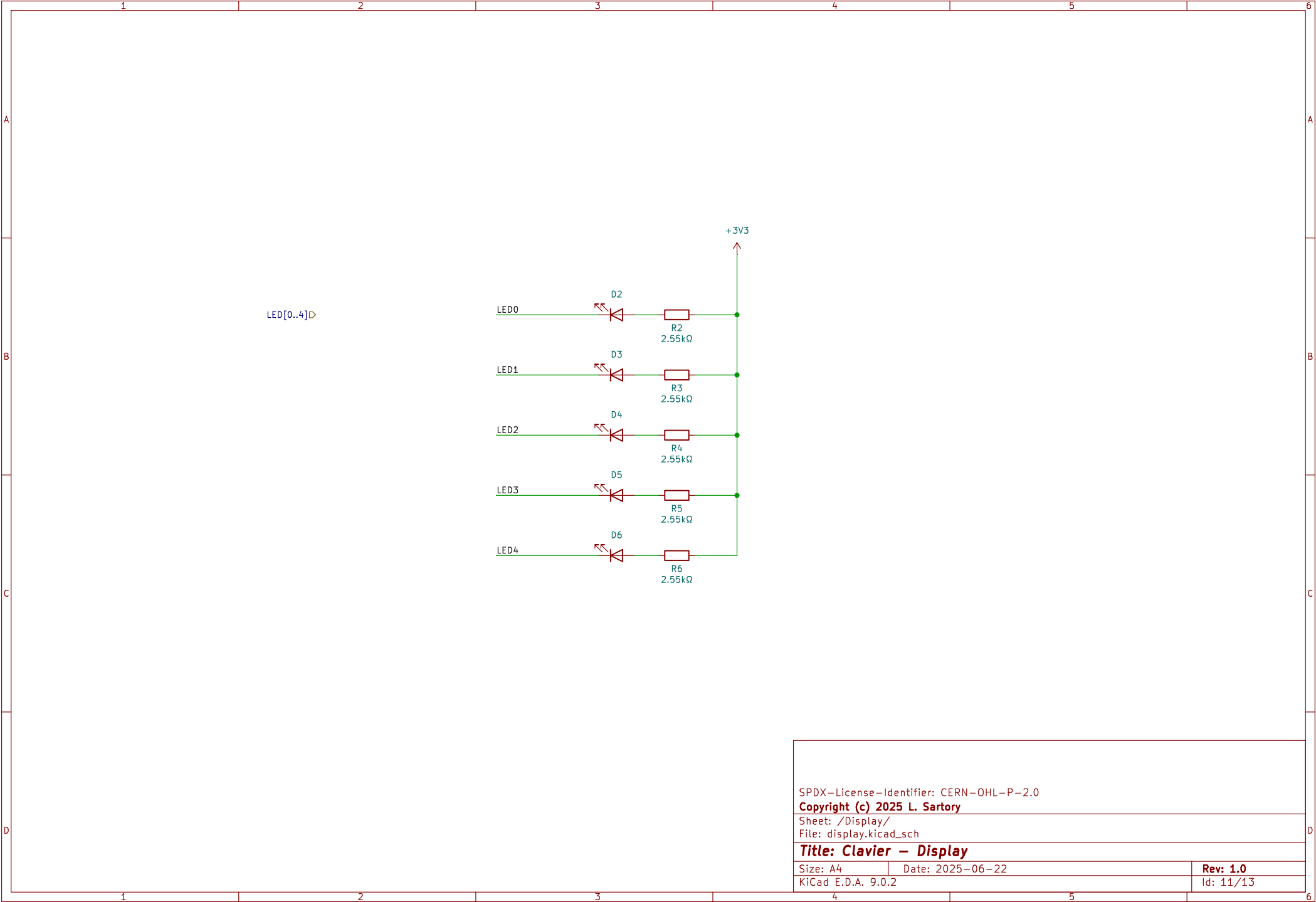
Size: A4

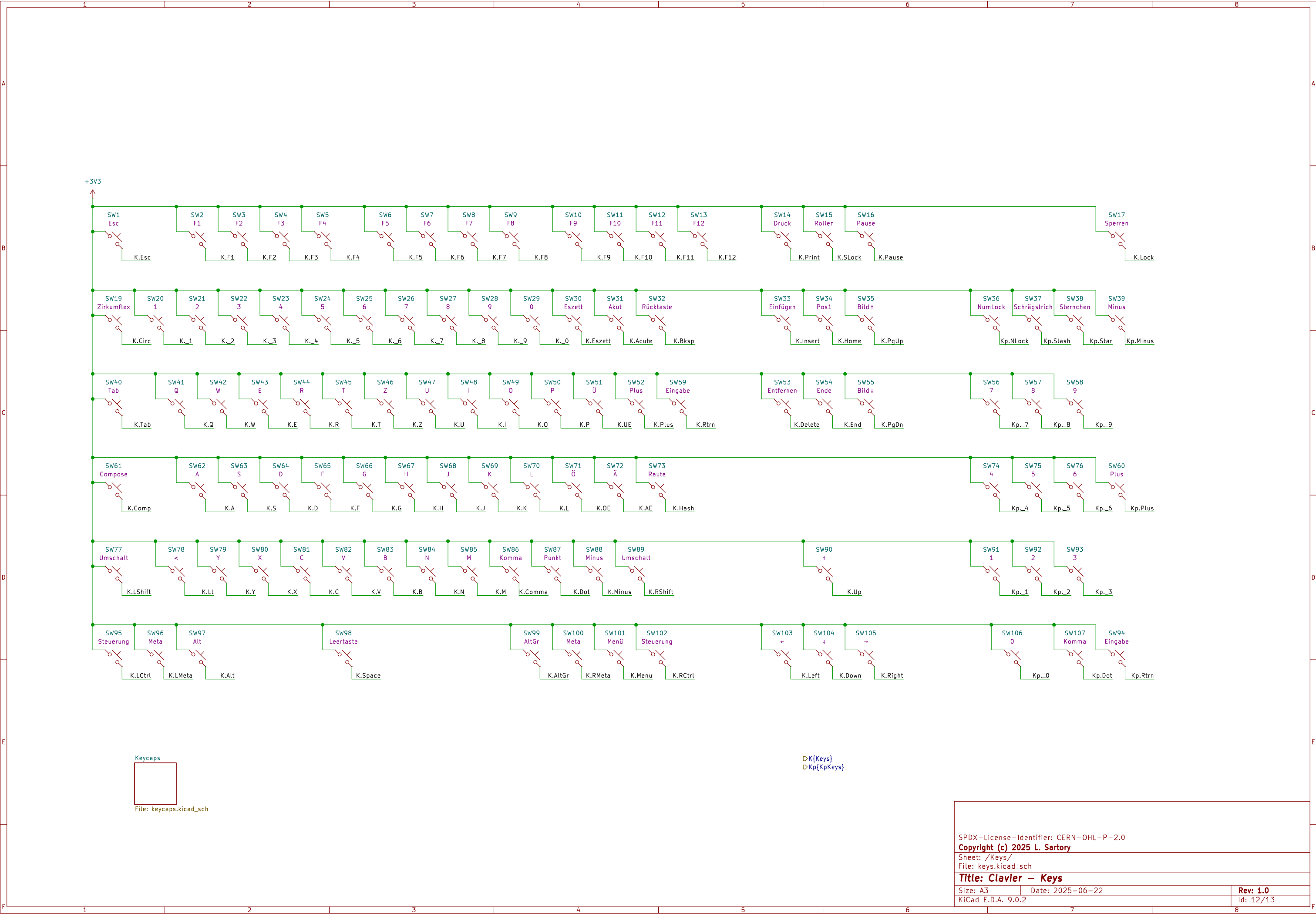
Date: 2025-06-22

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Sheet: /Keys/

File: keys.kicad\_sch

Title: Clavier – Keys

Size: A3

Date: 2025-06-22

Rev: 1.0

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