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Sheet: /Power/

File: power.kicad_sch

Title: Clavier – Power supplies

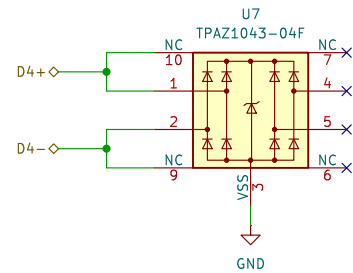
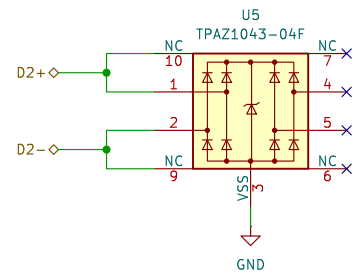
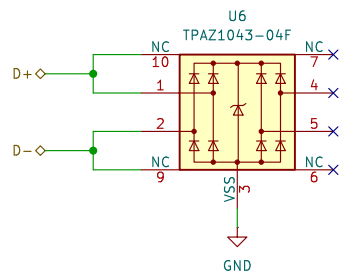
Size: A4

Date: 2025-06-21

Rev: 1.0

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Sheet: /USB hub/USB hub ESD/

File: usb_hub_esd.kicad_sch

Title: Clavier – USB hub ESD protection

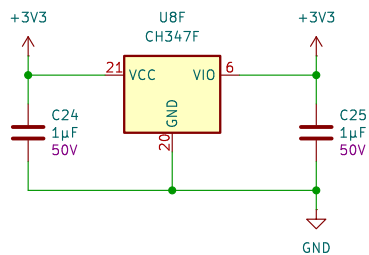
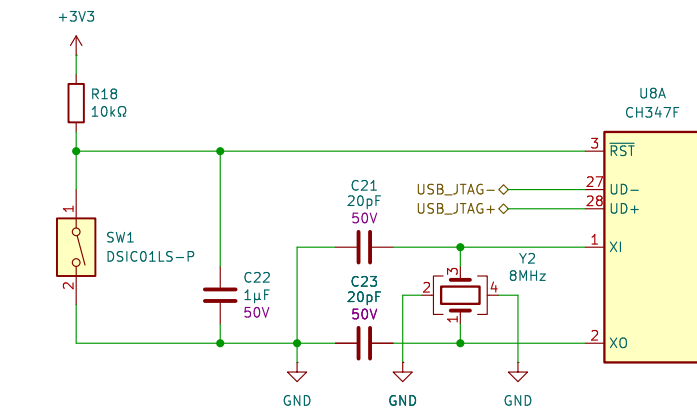
Size: A4

Date: 2025-06-21

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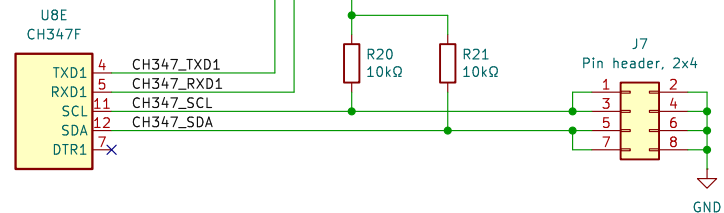
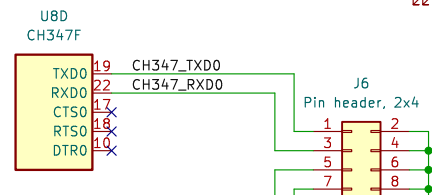
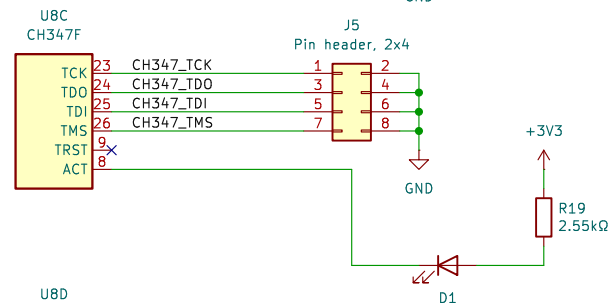
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5-in-1 USB ESD	
CH347_SCS0	CH347_SCS0
CH347_SCK	CH347_SCK
CH347_MISO	CH347_MISO
CH347_MOSI	CH347_MOSI
CH347_TCK	CH347_TCK
CH347_TDO	CH347_TDO
CH347_TDI	CH347_TDI
CH347_TMS	CH347_TMS
CH347_TXD0	CH347_TXD0
CH347_RXD0	CH347_RXD0
CH347_TXD1	CH347_TXD1
CH347_RXD1	CH347_RXD1
CH347_SCL	CH347_SCL
CH347_SDA	CH347_SDA

File: usb_jtag_esd.kicad_sch



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Sheet: /5-in-1 USB/

File: usb_jtag.kicad_sch

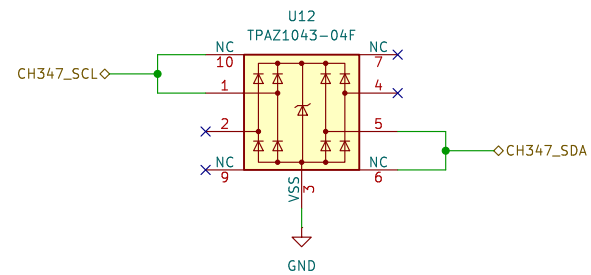
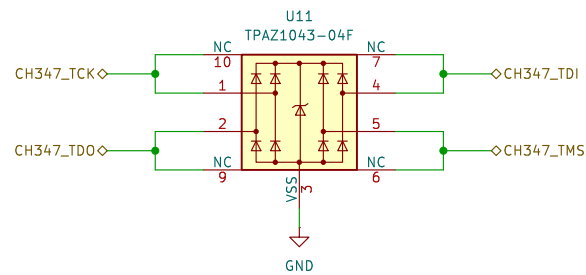
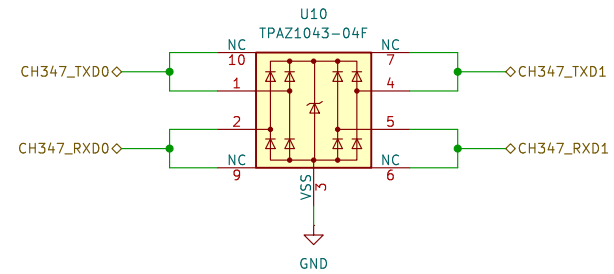
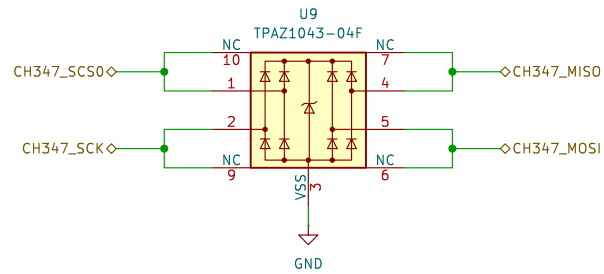
Title: Clavier - 5-in-1 USB

Size: A4 Date: 2025-06-21

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Sheet: /5-in-1 USB/5-in-1 USB ESD/

File: usb_jtag_esd.kicad_sch

Title: Clavier - 5-in-1 USB ESD protection

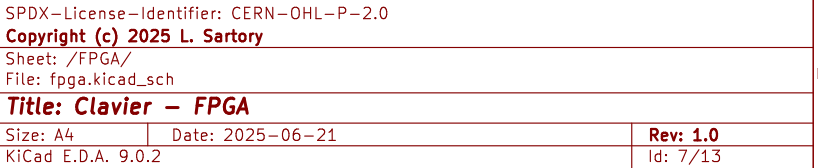
Size: A4

Date: 2025-06-21

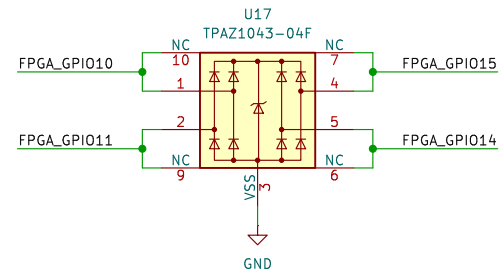
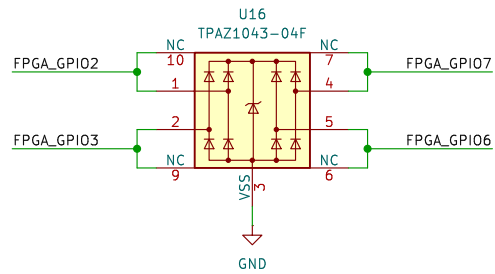
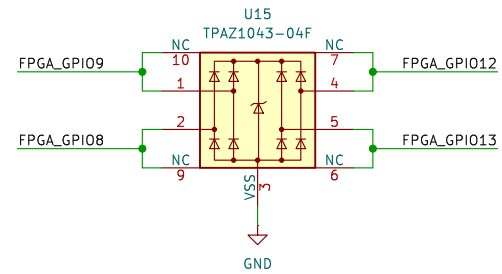
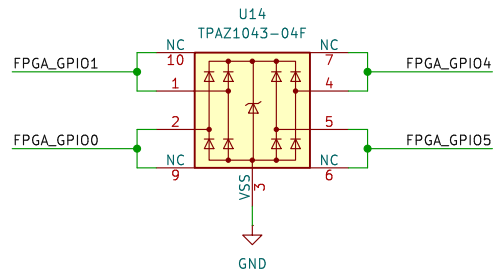
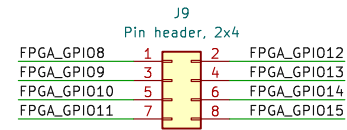
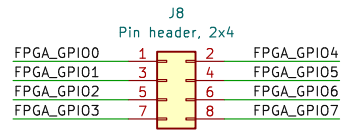
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FPGA_GPIO[0..15]◇



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Sheet: /FPGA/FPGA GPIOs/

File: fpga_gpio.kicad_sch

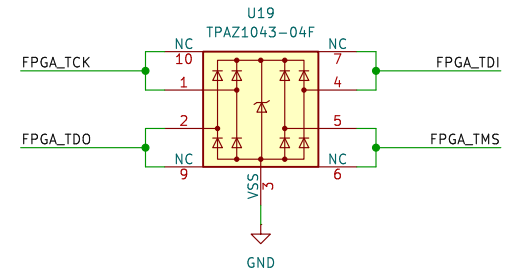
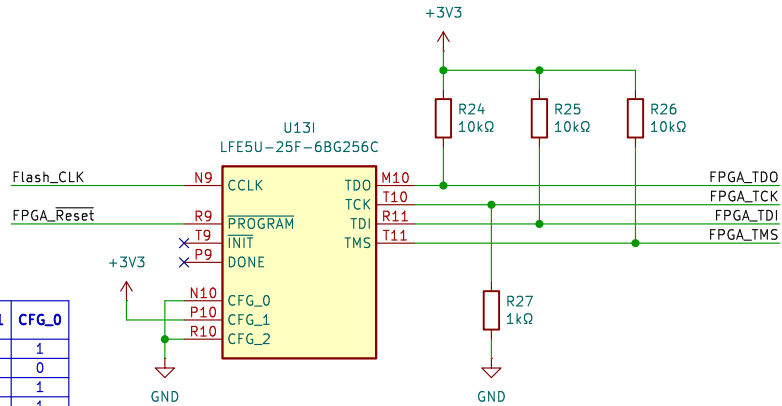
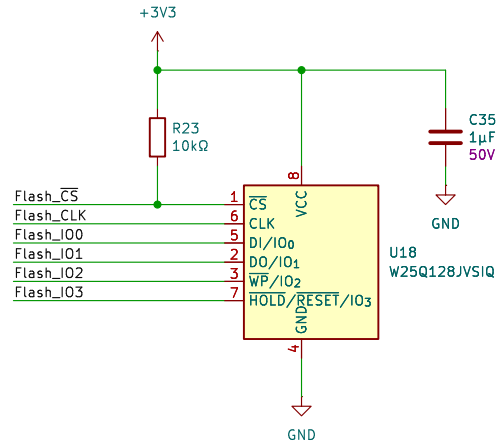
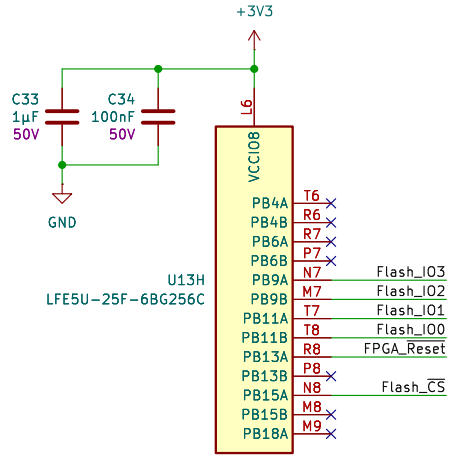
Title: Clavier – FPGA GPIOs

Size: A4 Date: 2025-06-21

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Configuration Mode	Clock	CFG_2	CFG_1	CFG_0
SSPI	CCLK	0	0	1
MSPI	MCLK	0	1	0
SCM	CCLK	1	0	1
SPCM	CCLK	1	1	1

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Sheet: /FPGA/FPGA config/

File: fpga_config.kicad_sch

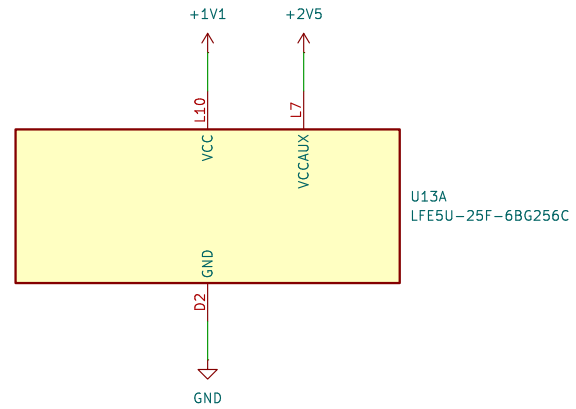
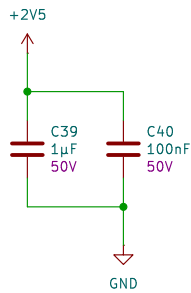
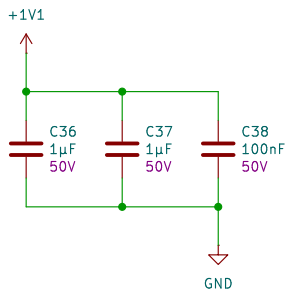
Title: Clavier – FPGA configuration

Size: A4 Date: 2025-06-21

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Sheet: /FPGA/FPGA power/

File: fpga_power.kicad_sch

Title: Clavier – FPGA power

Size: A4

Date: 2025-06-21

Rev: 1.0

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