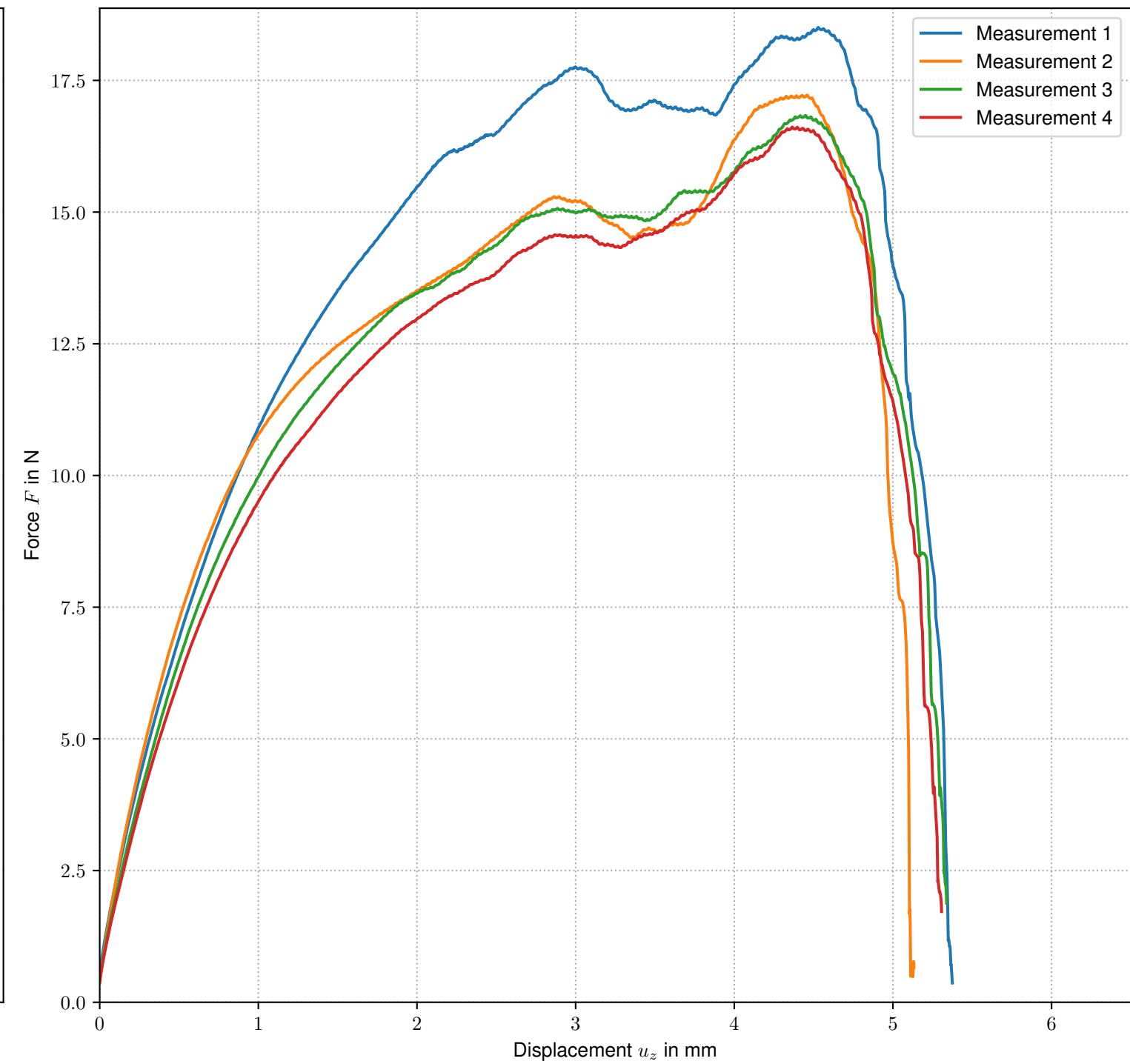
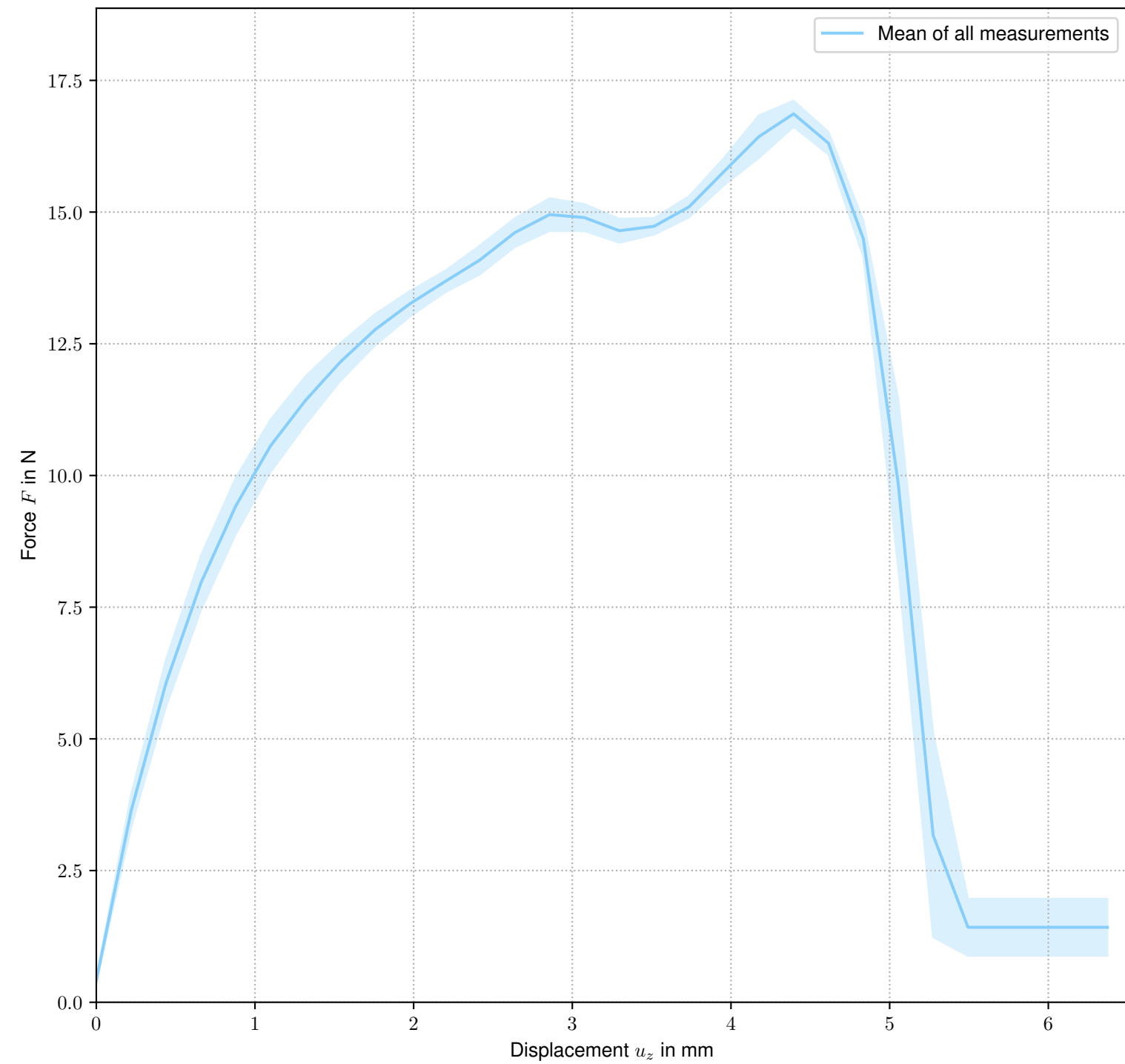
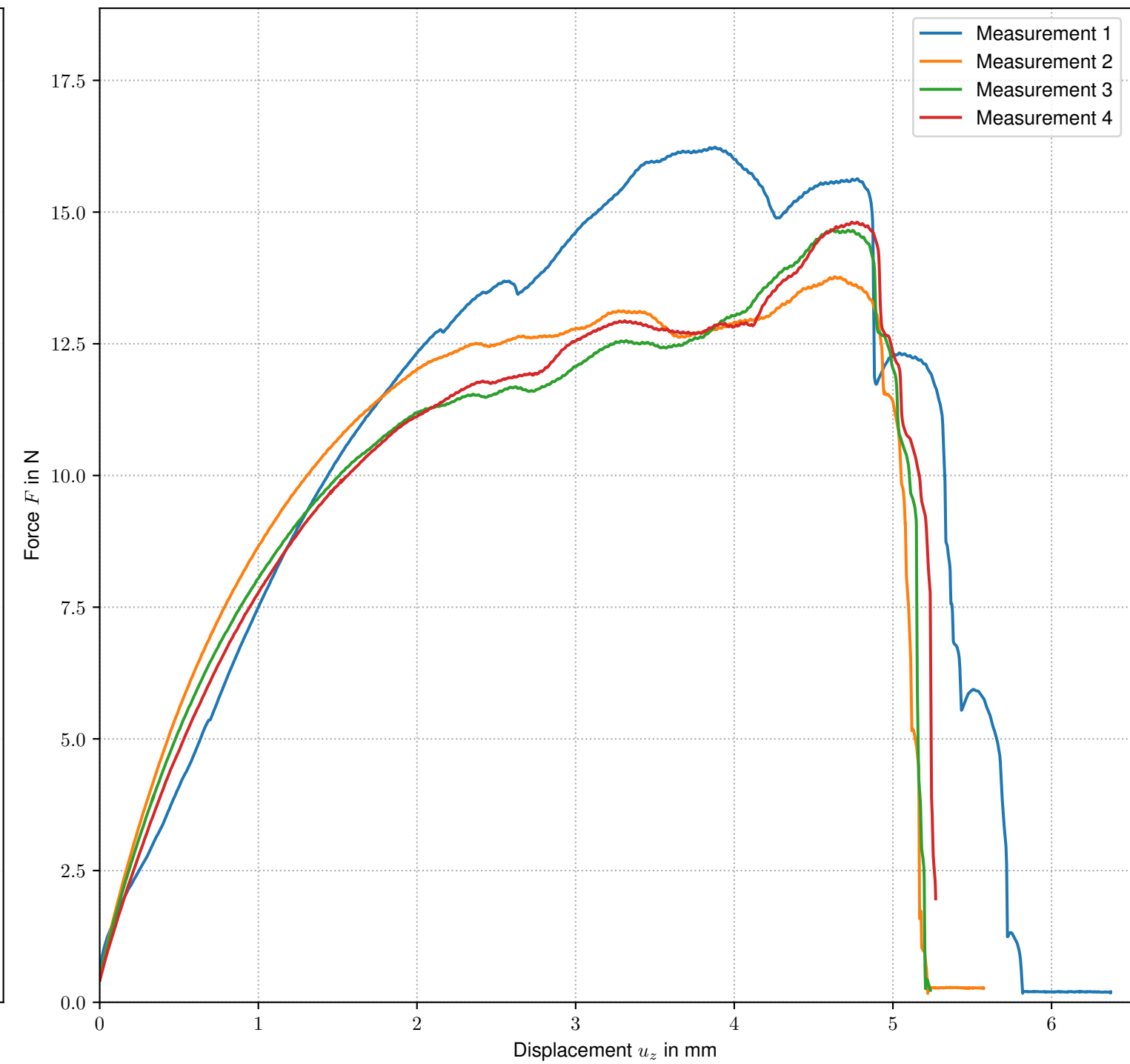
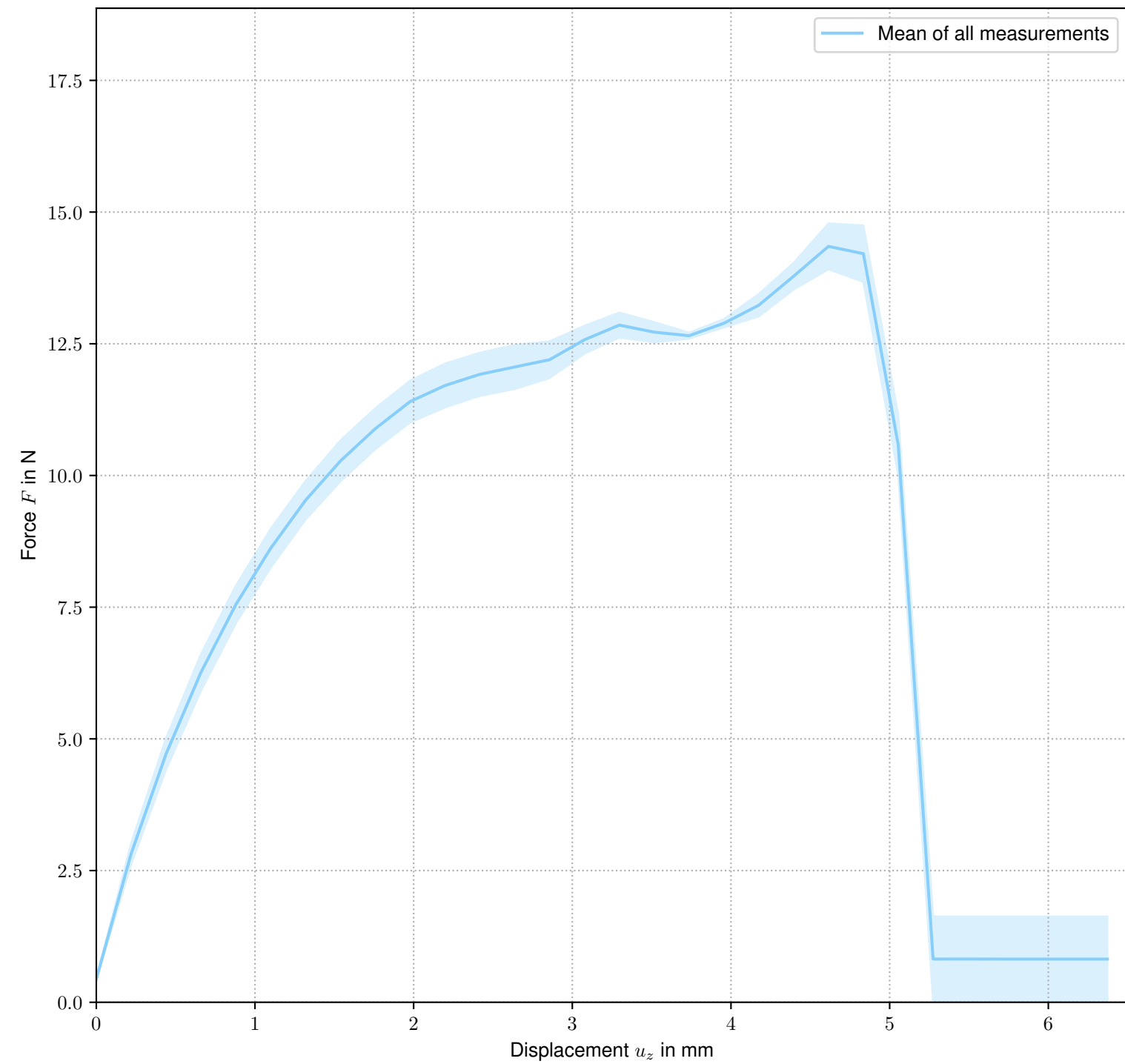


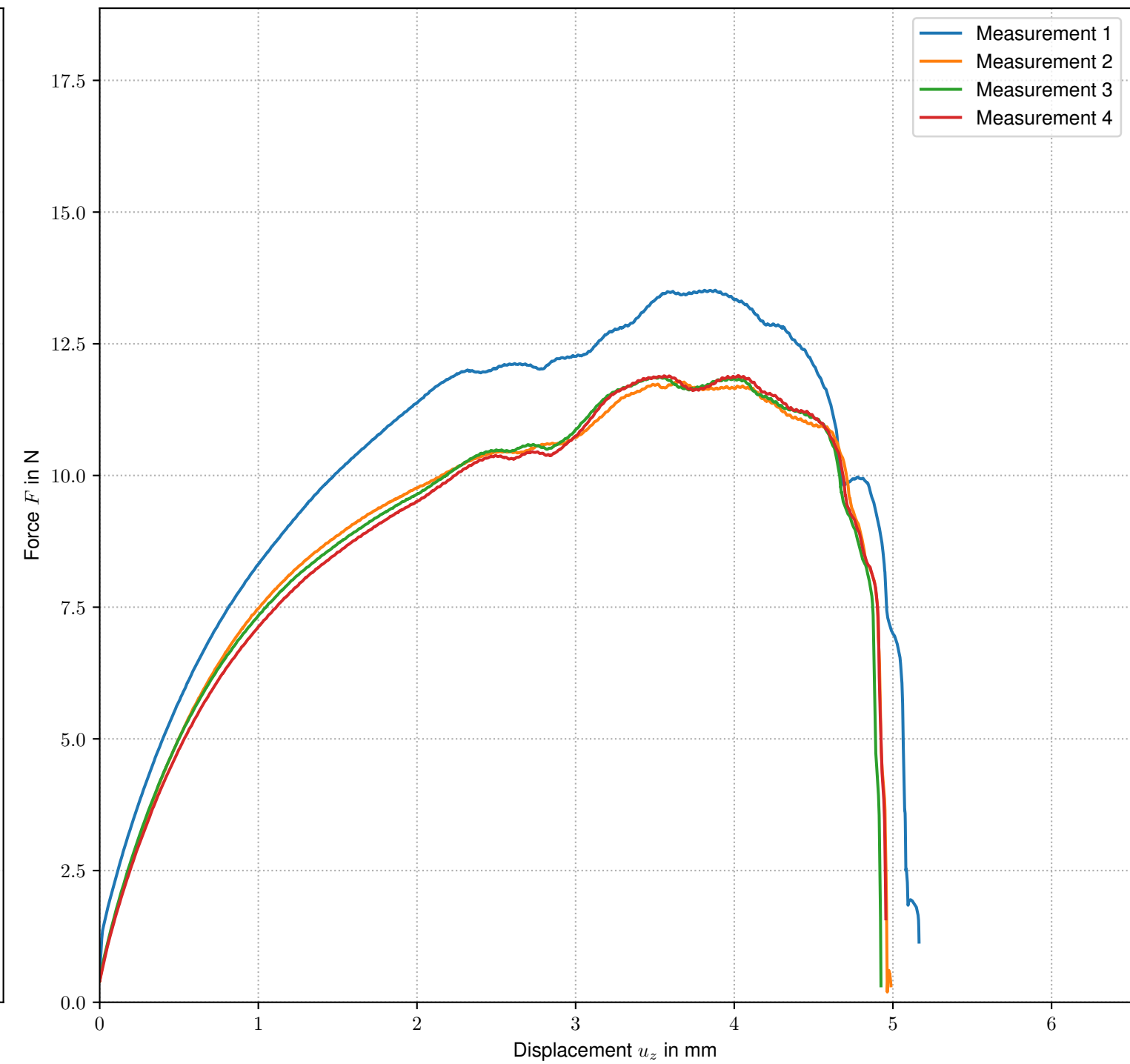
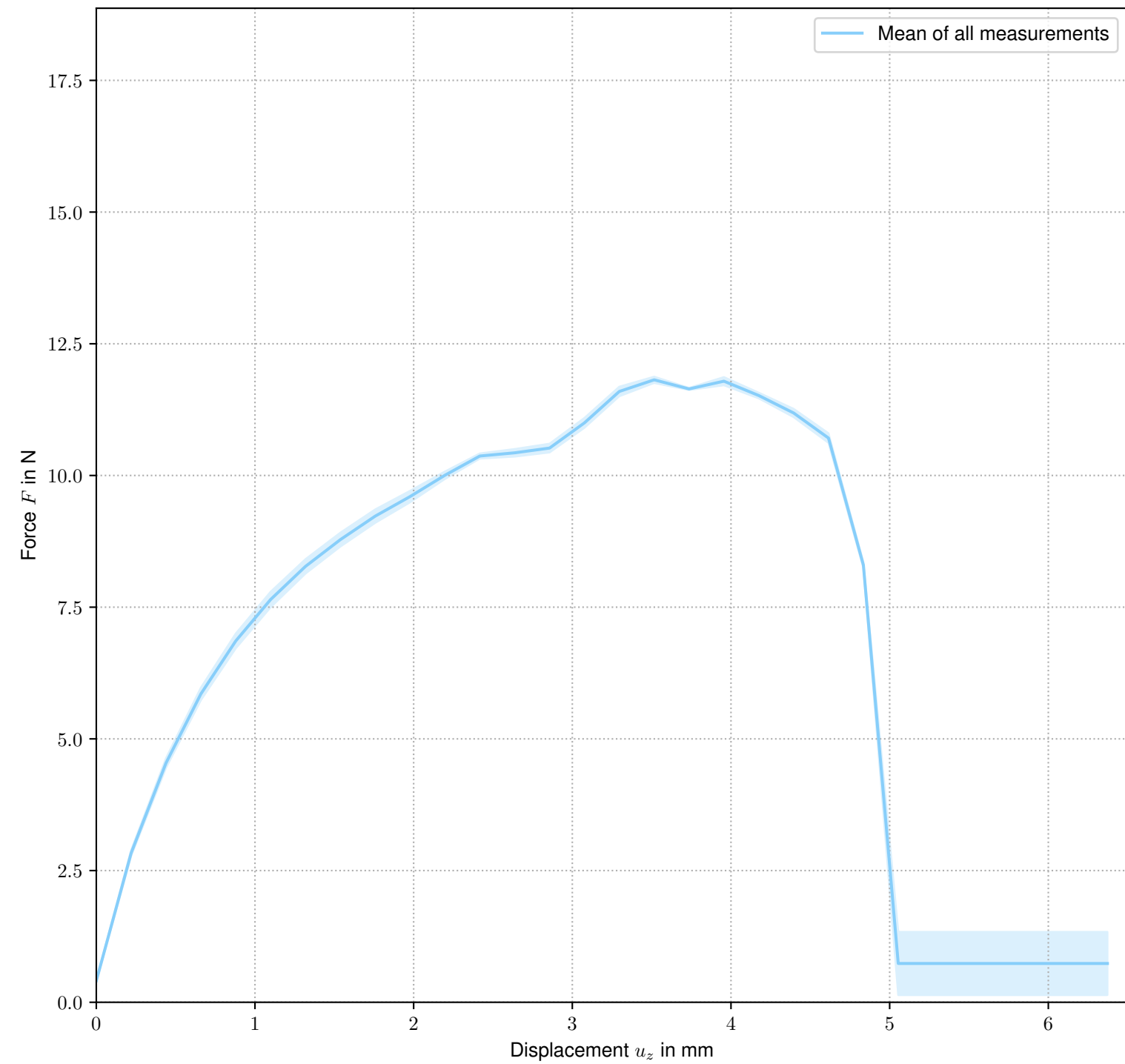
TPU_V7_M1



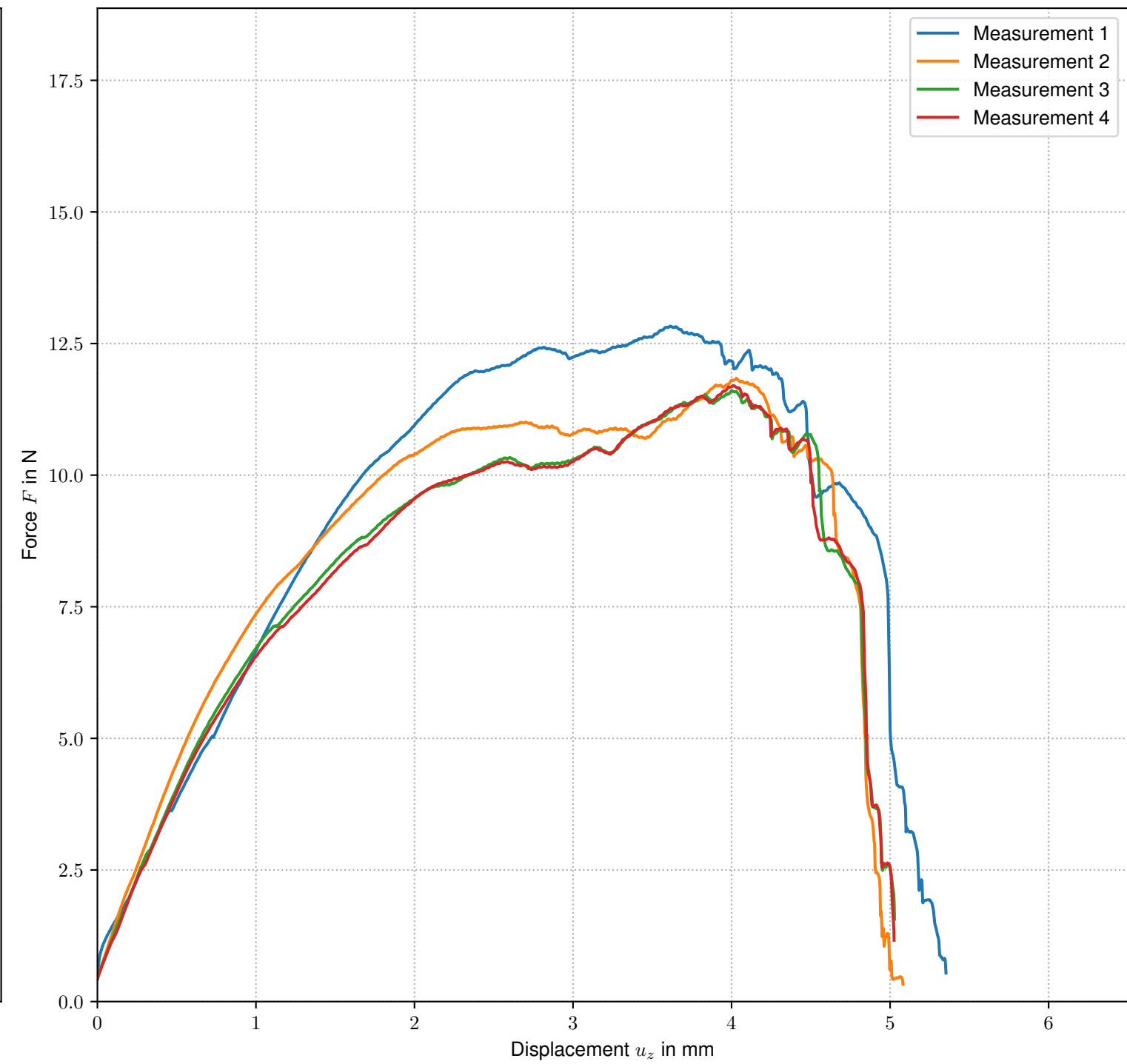
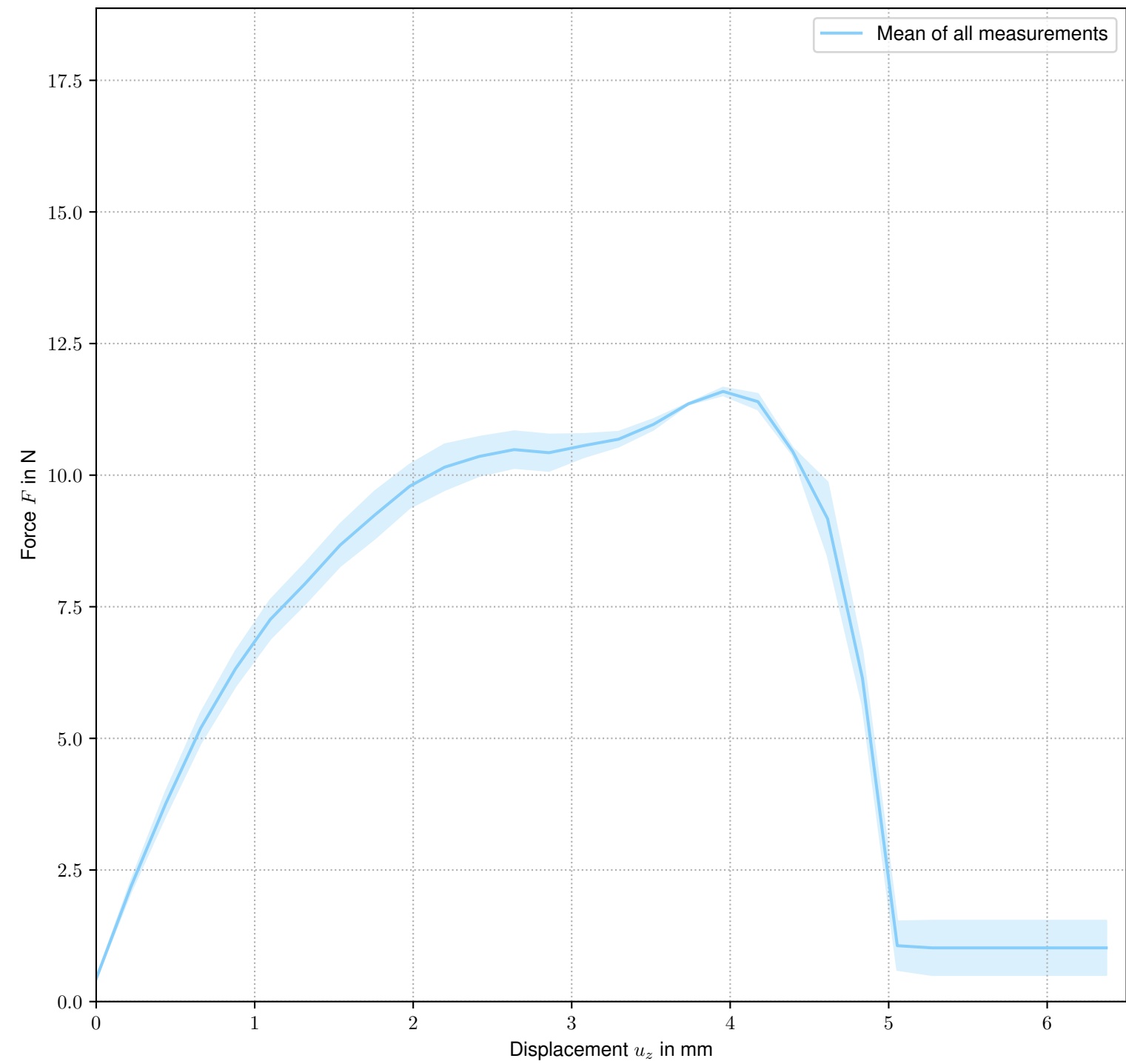
TPU_V7_M2



TPU_V7_M3



TPU_V7_M4



Mean result from all mechanisms

