# M051 Series CMSIS BSP Revision History

## Version 2.01.002 (Released 2012-08-10)

1. Fixed the _TIMER_RESET definition.
2. Fixed the SYSCLK_CLKSEL1_PWM23_HCLK definition.
3. Removed the IARv6 directory and supported only IAR v6.21 and higher versions.
4. Modified PLL enable procedure to avoid unstable condition caused by PLL frequency changes.

## Version 2.01.001 (Released 2012-04-24)

1. Provided a new driver for faster performance with smaller code size.
2. Created a new directory hierarchy.

## Version 1.02.003 (Released 2012-03-12)

1. Fixed the UART driver bug.

## Version 1.02.002 (Released 2011-08-26)

1. Added the NuvotonPlatform_IARv6 directory to support IAR tool v6.10 and later.
2. Supported semihosted input by IAR.

## Version 1.02.001 (Released 2011-07-08)

1. Updated the header to support 32-bit word access for control registers.

## Version 1.01.005 (Released 2011-06-20)

1. Fixed the bug about wrong clock source of timer driver.

## Version 1.01.004 (Released 2011-06-08)

1. Fixed the _PORT_DOUT definition of GPIO header.
2. Fixed the GPIO example in the Driver Reference Guide.

## Version 1.01.003 (Released 2011-05-31)

1. Added new sample code of learning board in KEIL environment: Smpl_I2C_SW, Smpl_I2C_SW_I, Smpl_Idle.
2. Fixed driver bugs.

3. Updated the clock diagram in the Driver Reference Guide.

## Version 1.01.002 (Released 2011-01-05)

1. Fixed bugs and added more samples.
2. Added sample code for the learning board.

## Version 1.01.001 (Released 2010-12-01)

1. Fixed bugs and added more samples.
2. Defaulted the NMI_SEL to 0x31 (assigned NMI to reserved IRQ).
3. Updated the clock diagram in the Driver Reference Guide for the PLL source.
4. Removed the second parameter (E_ADC_INPUT_MODE) from DrvADC_SetADCChannel().
5. Removed multi-function pin configurations from SPI driver (DrvSPI_Open()).
6. Removed multi-function pin configurations from ADC driver [ DrvADC_Open( ) & DrvADC_SetADCChannel( ) ]. Users need to control the multi-function I/O by GPIO driver before they can use the I/O of ADC or SPI.
7. Added sample code for M051-LB_004 (Learning Board).
8. Fixed the gau32ClkSrcTbl setting to avoid wrong system clock calculation.
9. Fixed the bug about EINT0, EINT1 handler cleaning status.
10. Removed unused register definitions in startup_M051Series.s.
11. Fixed the GCR_INT_T definition in M051Series.h.
12. Fixed the wrong definition of DRVUART_STOPBITS_1_5 and DRVUART_STOPBITS_2.

## Version 1.00.001 (Released 2010-08-20)

1. Primary release version.

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