

ARM HW3

2019. 03. 29.

Embedded System LAB SKKU



Implementation Topic

- Using switch
 - LED ON and OFF
 - LED Blink Speed
 - UART Communication



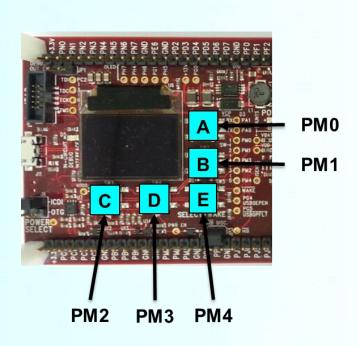
Using switch

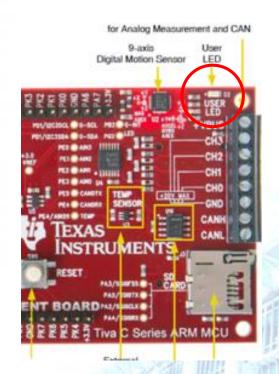
A: LED ON, Print "-SW A"

B: LED OFF, Print "-SW B"

C: LED Blink 5 times Slow, Print "-SW C"

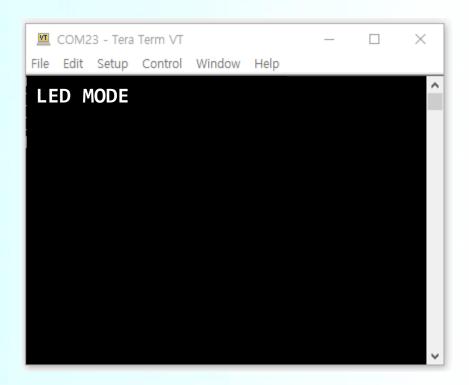
D : LED Blink 5 times Fast, Print "-SW D"

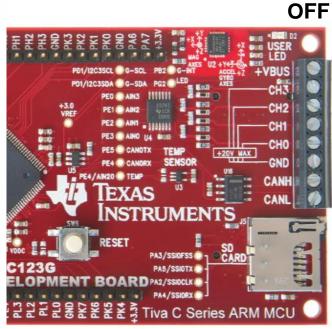






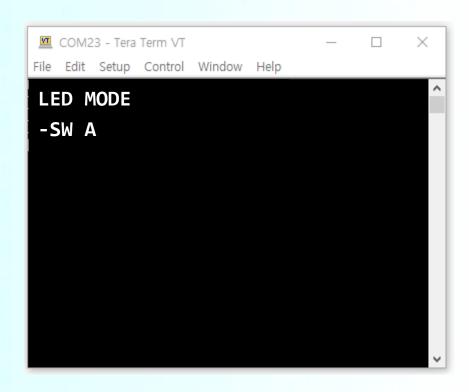
Initial Display in Tera Term

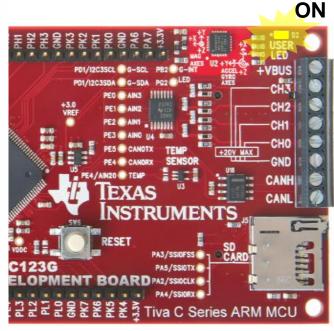






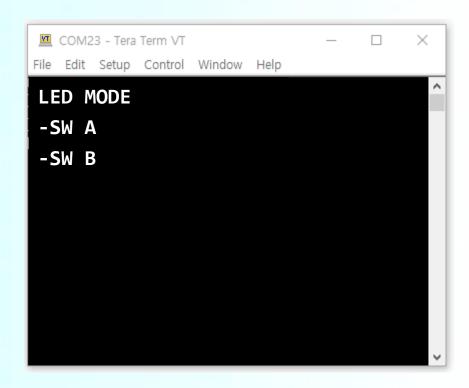
- A Button Push
 - LED ON / Print "-SW A"

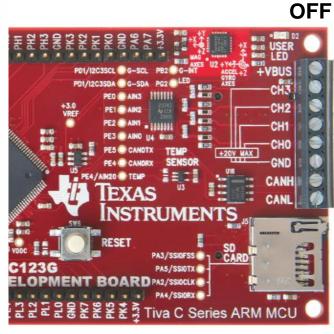






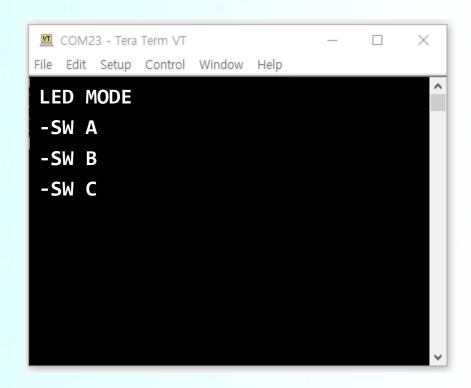
- B Button Push
 - LED OFF / Print "-SW B"

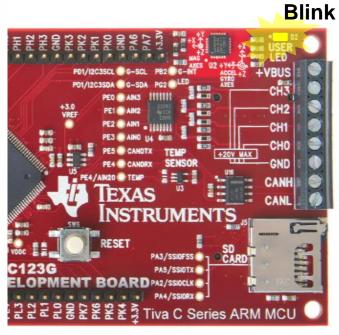






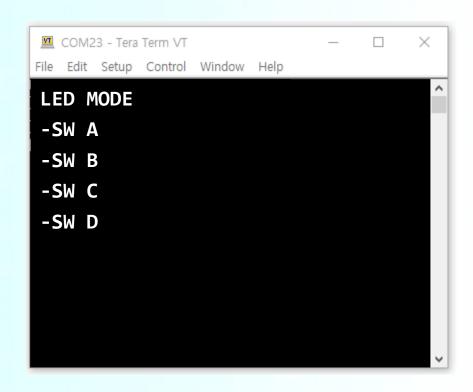
- C Button Push
 - LED Blink 5 times Slow / Print "-SW C"

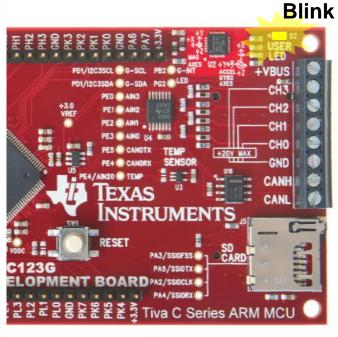






- D Button Push
 - LED Blink 5 times Fast / Print "-SW D"



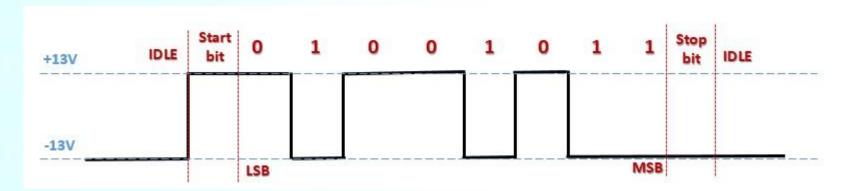




UART Communication

Serial Communication

Communication that transmits data bit by bit between devices

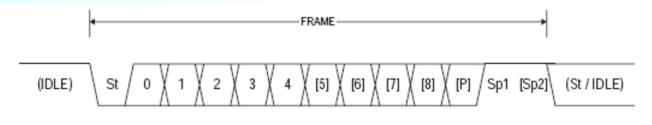


- Serial Communication Protocol
 - I2C(Inter-Integrated Circuit)
 - SPI(Serial Peripheral Interface)
 - UART(Universal Asynchronous Receive-Transmit)



UART Communication

- UART (Universal Asynchronous Receive-Transmit)
 - Asynchronous serial communication
 - Not use a clock to validate data
 - Serial data is transferred one bit at time

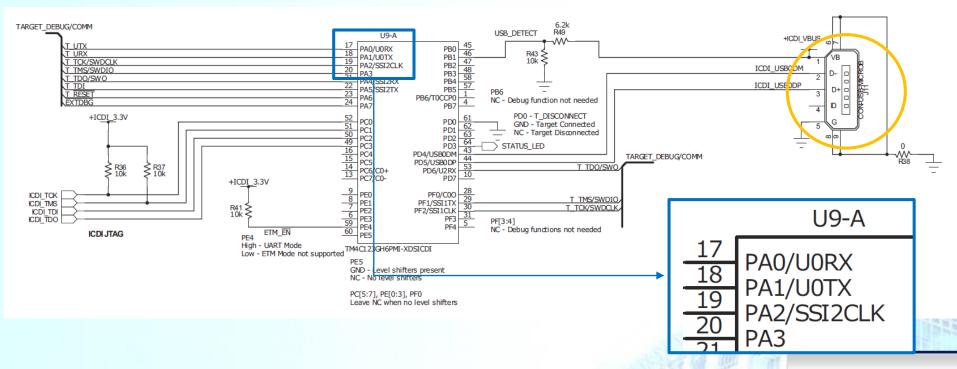


- St Start bit, always low.
- (n) Data bits (0 to 8).
- P Parity bit. Can be odd or even.
- Sp Stop bit, always high.
- IDLE No transfers on the communication line (RxD or TxD). An IDLE line must be high.



ICDI Debug USB Port



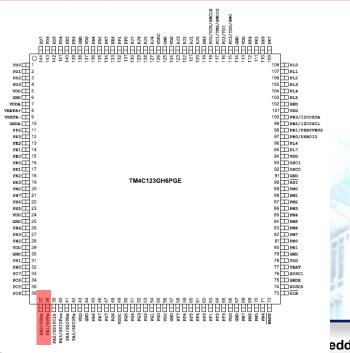




Signals by Pin Number (page 1357)

				I
37	PA0	I/O	TTL	GPIO port A bit 0.
	CAN1Rx	I	TTL	CAN module 1 receive.
	UORx	I	TTL	UART module 0 receive.
38	PA1	I/O	TTL	GPIO port A bit 1.
	CAN1Tx	0	TTL	CAN module 1 transmit.
	UOTx	0	TTL	UART module 0 transmit.







- System Control
 - RCGCUART → Enable
 - RCGCGPIO → Enable
- UART
 - UARTCTL
 - UARTIBRD
 - UARTFBRD
 - UARTLCRH
 - UARTFR
 - UARTDR
- To enable and initialize the UART, some steps are necessary



UART

- Initialization and Configuration (page 928)
 - Step 1 : UARTCTL → Disable UART
 - Step 2 : UARTIBRD / UARTFBRD → Setting to the correct value
 - > UARTSys CLK: 16MHz (ClkDiv is 16)
 - UART Baud Rate: 115200 baud rate
 - Step 3 : UARTLCRH → 8-bit / no parity / 1-stop bit
 - Step 4 : UARTCTL → Enable UART/Enable transmit & receive section
- General-Purpose Input/Outputs
 - GPIOAFSEL → Peripheral signal
 - GPIOPCTL → use U0Rx, U0Tx (refer to page 1396, Table 23-5)
 - GPIODEN → Enable
 - GPIOAMSEL → Disable
- Use GPIO Port A (APB) base address.



Send 'A' by UART in C

```
void Print_Char (void)
{
    while(UARTFR & (1<<5) != 0);  // TXFF bit
    UARTDR = 'A';
}</pre>
```

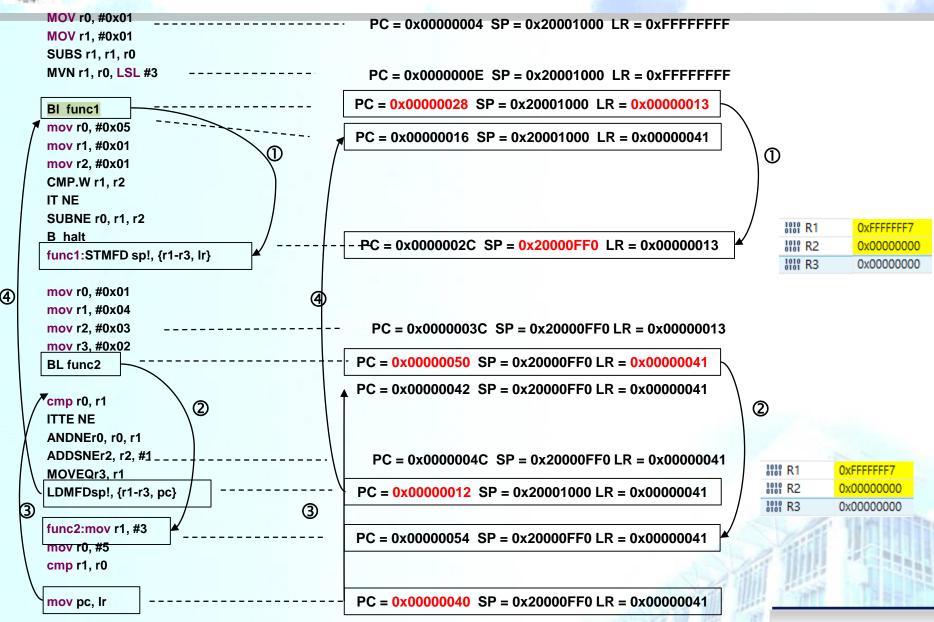
We recommend creating a printf function using a branch



- 16 -

mov r1. #5

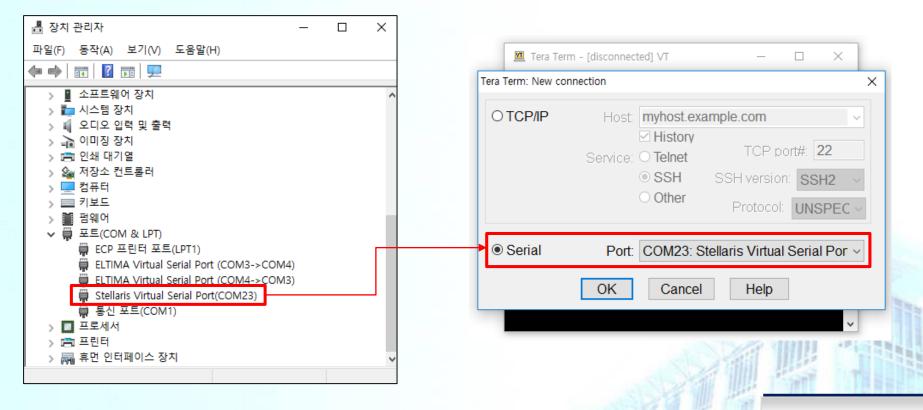
How to use UART



Embedded System Lab



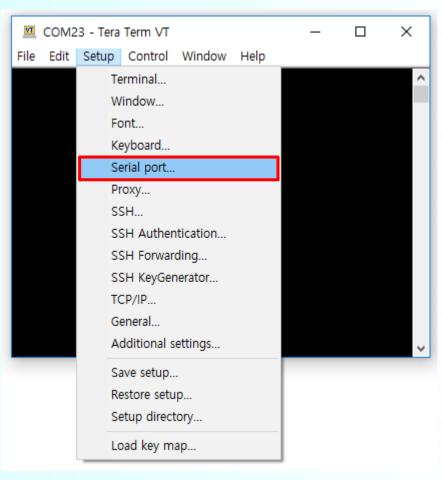
- Tera Term
 - Serial terminal emulation programs
 - Download Link : https://osdn.net/projects/ttssh2/releases/

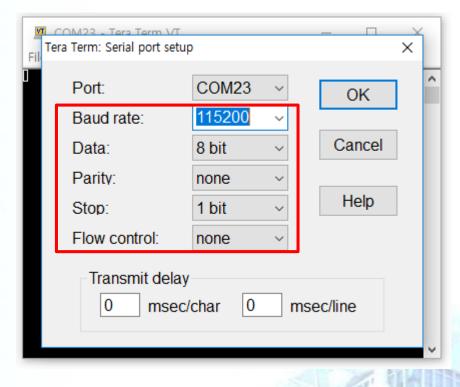




Tera Term

Setting Serial Port

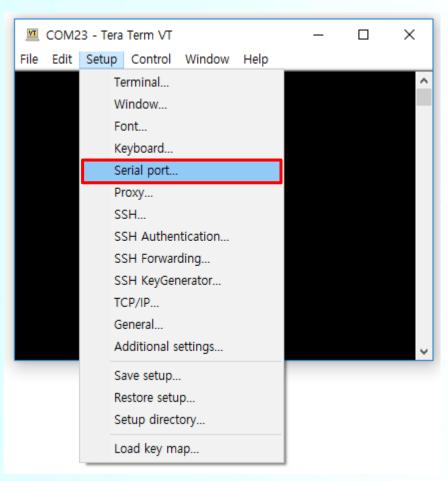


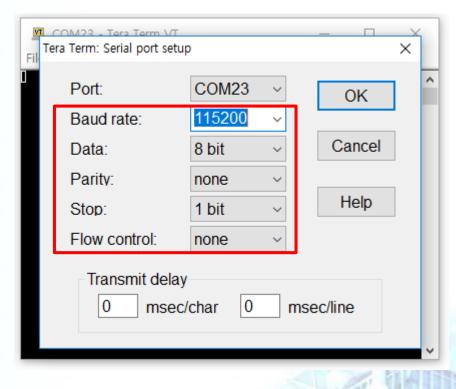




Tera Term

Setting Serial Port







ARM HW3 check

- Time & Place
 - April 5th(Fri) 19:00
 - Semi-conductor building 2 floor workstation room
 - **400202, 400212**
- How to submit
 - I-campus, until April 5th 18:59
 - format
 - > 2012310000_HW3.asm