

USE-CASE

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graph TD; A[USE-CASE] --> B[USER STORIES]; B --> C[FUNCTIONAL REQUIREMENTS]; C --> D[HARDWARE REQUIREMENTS]; D --> E[PART SELECTION]; E --> F[CIRCUIT DESIGN]; F --> G[SCHEMATIC LAYOUT]; G --> H[PCB LAYOUT];
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A vertical flowchart illustrating the PCB design process. It consists of eight rectangular boxes arranged in a column, connected by downward-pointing arrows. The first two boxes, 'USE-CASE' and 'USER STORIES', are blue with white text. The remaining six boxes are white with black text. The steps proceed from top to bottom: USE-CASE, USER STORIES, FUNCTIONAL REQUIREMENTS, HARDWARE REQUIREMENTS, PART SELECTION, CIRCUIT DESIGN, SCHEMATIC LAYOUT, and PCB LAYOUT.

USER STORIES

FUNCTIONAL
REQUIREMENTS

HARDWARE
REQUIREMENTS

PART SELECTION

CIRCUIT DESIGN

SCHEMATIC LAYOUT

PCB LAYOUT