

USE-CASE

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graph TD; A[USE-CASE] --> B[USER STORIES]; B --> C[FUNCTIONAL REQUIREMENTS]; C --> D[HARDWARE REQUIREMENTS]; D --> E[PART SELECTION]; E --> F[CIRCUIT DESIGN]; F --> G[SCHEMATIC LAYOUT]; G --> H[PCB LAYOUT];
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A vertical flowchart showing the stages of PCB design. The stages are: USE-CASE, USER STORIES, FUNCTIONAL REQUIREMENTS, HARDWARE REQUIREMENTS, PART SELECTION, CIRCUIT DESIGN, SCHEMATIC LAYOUT, and PCB LAYOUT. The boxes for FUNCTIONAL REQUIREMENTS and HARDWARE REQUIREMENTS are highlighted in yellow.

USER STORIES

FUNCTIONAL
REQUIREMENTS

HARDWARE
REQUIREMENTS

PART SELECTION

CIRCUIT DESIGN

SCHEMATIC LAYOUT

PCB LAYOUT