

USE-CASE

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graph TD; A[USE-CASE] --> B[USER STORIES]; B --> C[FUNCTIONAL REQUIREMENTS]; C --> D[HARDWARE REQUIREMENTS]; D --> E[PART SELECTION]; E --> F[CIRCUIT DESIGN]; F --> G[SCHEMATIC LAYOUT]; G --> H[PCB LAYOUT];
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A vertical flowchart showing the stages of PCB design. The stages are: USE-CASE, USER STORIES, FUNCTIONAL REQUIREMENTS, HARDWARE REQUIREMENTS, PART SELECTION, CIRCUIT DESIGN, SCHEMATIC LAYOUT, and PCB LAYOUT. Arrows point downwards from each stage to the next. The last two stages, SCHEMATIC LAYOUT and PCB LAYOUT, are highlighted with red backgrounds and white text.

USER STORIES

FUNCTIONAL
REQUIREMENTS

HARDWARE
REQUIREMENTS

PART SELECTION

CIRCUIT DESIGN

SCHEMATIC LAYOUT

PCB LAYOUT