

USE-CASE



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graph TD; A[USE-CASE] --> B[USER STORIES]; B --> C[FUNCTIONAL REQUIREMENTS]; C --> D[HARDWARE REQUIREMENTS]; D --> E[PART SELECTION]; E --> F[CIRCUIT DESIGN]; F --> G[SCHEMATIC LAYOUT]; G --> H[PCB LAYOUT];
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A vertical flowchart illustrating the PCB design process. It consists of eight rounded rectangular boxes connected by downward-pointing arrows. The boxes are color-coded: the first two are blue, the next two are yellow, the next two are green, and the last two are red. The text inside the boxes is as follows: 'USE-CASE' (blue), 'USER STORIES' (blue), 'FUNCTIONAL REQUIREMENTS' (yellow), 'HARDWARE REQUIREMENTS' (yellow), 'PART SELECTION' (green), 'CIRCUIT DESIGN' (green), 'SCHEMATIC LAYOUT' (red), and 'PCB LAYOUT' (red).

USER STORIES

FUNCTIONAL
REQUIREMENTS

HARDWARE
REQUIREMENTS

PART SELECTION

CIRCUIT DESIGN

SCHEMATIC LAYOUT

PCB LAYOUT