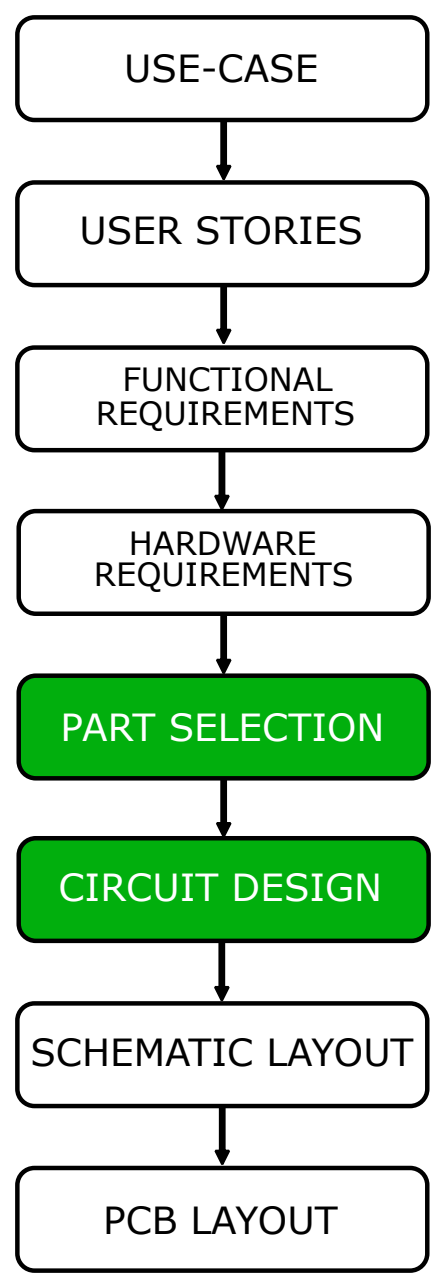


USE-CASE



```
graph TD; A[USE-CASE] --> B[USER STORIES]; B --> C[FUNCTIONAL REQUIREMENTS]; C --> D[HARDWARE REQUIREMENTS]; D --> E[PART SELECTION]; E --> F[CIRCUIT DESIGN]; F --> G[SCHEMATIC LAYOUT]; G --> H[PCB LAYOUT];
```

A vertical flowchart illustrating the PCB design process. It consists of eight rounded rectangular boxes connected by downward-pointing arrows. The first four boxes are white with black outlines and text: 'USE-CASE', 'USER STORIES', 'FUNCTIONAL REQUIREMENTS', and 'HARDWARE REQUIREMENTS'. The next two boxes are solid green with white text: 'PART SELECTION' and 'CIRCUIT DESIGN'. The final two boxes are white with black outlines and text: 'SCHEMATIC LAYOUT' and 'PCB LAYOUT'.

USER STORIES

FUNCTIONAL
REQUIREMENTS

HARDWARE
REQUIREMENTS

PART SELECTION

CIRCUIT DESIGN

SCHEMATIC LAYOUT

PCB LAYOUT