



Source Code



Logic Synthesis



Technology Mapping



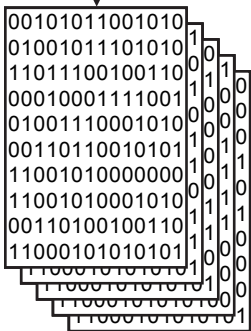
Placement



Routing



Bitstream Generation



Bitstream

