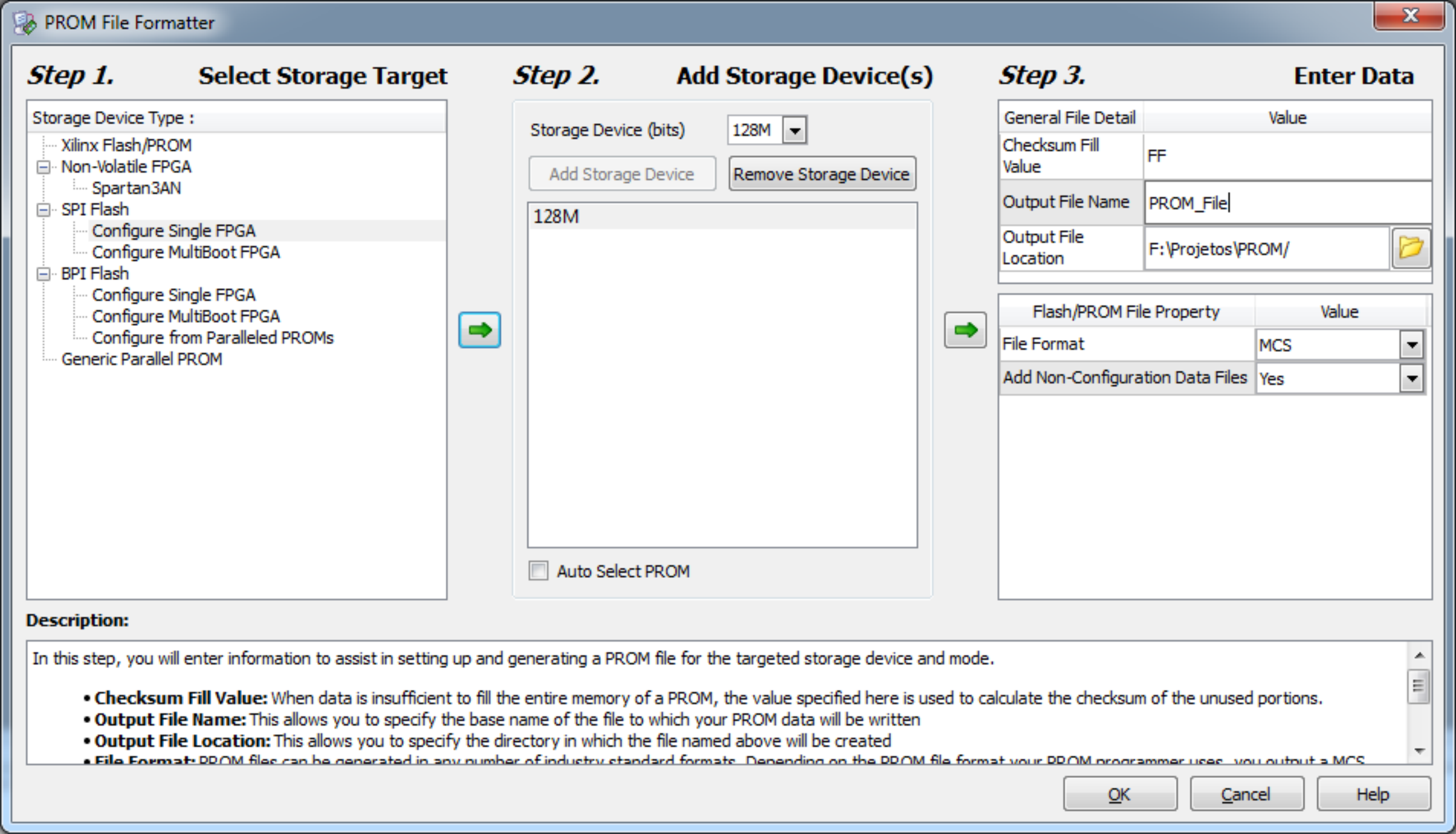
PROM File Formatter
Step 1.
Select Storage Target
Storage Device Type :
Xilinx Flash/PROM
Non-Volatile FPGA
Spartan3AN
SPI Flash
Configure Single FPGA
Configure MultiBoot FPGA
BPI Flash
Configure Single FPGA
Configure MultiBoot FPGA
Configure from Paralleled PROMs
Generic Parallel PROM
Step 2.
Add Storage Device(s)
Storage Device (bits)
128M
Add Storage Device
Remove Storage Device
128M
Auto Select PROM
Step 3.
Enter Data
General File Detail
Value
Checksum Fill Value
FF
Output File Name
PROM_File
Output File Location
F:\Projetos\PROM/
Flash/PROM File Property
Value
File Format
MCS
Add Non-Configuration Data Files
Yes
Description:
In this step, you will enter information to assist in setting up and generating a PROM file for the targeted storage device and mode.
• Checksum Fill Value: When data is insufficient to fill the entire memory of a PROM, the value specified here is used to calculate the checksum of the unused portions.
• Output File Name: This allows you to specify the base name of the file to which your PROM data will be written
• Output File Location: This allows you to specify the directory in which the file named above will be created
OK
Cancel
Help