| Instance | Base Name | Base Address | High Address | Size | Bus Interface(s) | Bus Name | Lock |
|---|---|---|---|---|---|---|---|
| ⊟ microblaze_0's Address Map | | | | | | | |
| microblaze_0_d_bram_ctrl | C_BASEADDR | 0x00000000 | 0x0000FFFF | 64K | SLMB | microblaze_0_dl... | ☐ |
| microblaze_0_i_bram_ctrl | C_BASEADDR | 0x00000000 | 0x0000FFFF | 64K | SLMB | microblaze_0_il... | ☐ |
| Push_Buttons_5Bits | C_BASEADDR | 0x40000000 | 0x4000FFFF | 64K | S_AXI | axi4lite_0 | ☐ |
| LEDs_8Bits | C_BASEADDR | 0x40020000 | 0x4002FFFF | 64K | S_AXI | axi4lite_0 | ☐ |
| DIP_Switches_8Bits | C_BASEADDR | 0x40040000 | 0x4004FFFF | 64K | S_AXI | axi4lite_0 | ☐ |
| RS232_Uart_1 | C_BASEADDR | 0x40600000 | 0x4060FFFF | 64K | S_AXI | axi4lite_0 | ☐ |
| debug_module | C_BASEADDR | 0x41400000 | 0x4140FFFF | 64K | S_AXI | axi4lite_0 | ☐ |
| QSPI_FLASH | C_BASEADDR | 0x70000000 | 0x77FFFFFF | 128M | S_AXI | axi4lite_0 | ☐ |
| DDR3_SDRAM | C_S_AXI_BASEA... | 0xC0000000 | 0xFFFFFFFF | 1G | S_AXI | axi4_0 | ☐ |