

Processor, Cache, and Peripheral Configuration

Configure the processor(s). To add a peripheral, drag it from the "Available Peripherals" list to the Included Peripherals list. To configure a core parameter, click on the peripheral.

Processor Frequency

100 MHz

▼

Processor Configuration

Select a Processor

microblaze\_0

microblaze\_0

Enable Floating Point Unit

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Local Memory Size

8 KB

▼

Instruction Cache Size

8 KB

▼

Data Cache Size

8 KB

▼

Select and Configure Peripherals

Available Peripherals

Peripheral Names

IO Devices

IIC\_MAIN

Linear\_Flash

Ethernet\_Lite

ETHERNET

PCI\_Express

Internal Peripherals

axi\_bram\_ctrl

axi\_timebase\_wdt

axi\_timer

Add >

< Remove

Included Peripherals for microblaze\_0

Select All

Core	Parameter
DDR3_SDRAM (Cached)	
Core	axi_7series_ddrx
DIP_Switches_8Bits	
Core: axi_gpio	
LEDs_8Bits	
Core: axi_gpio	
Push_Buttons_5Bits	
Core: axi_gpio	
QSPI_FLASH	
Core: axi_quad_spi, C_SPI_MODE: Standa...	
RS232_Uart_1	
Core: axi_uartlite, Baud Rate: 9600, Data ...	

NOTE: Base System Builder always enables MicroBlaze caches. All memories connected to the AXI4 interconnect are cached.