

>make

Overriding with data file .\Tools\data_synth.tcl

Synthesis tool is set to xst

***** Synthesizing Reconfig Module named <DynCounter> *****

xst -ifn DynCounter.xst

***** Synthesizing Reconfig Module named <DynFSM> *****

xst -ifn DynFSM.xst

***** Synthesizing Reconfig Module named <Top> *****

xst -ifn Top.xst

***** Finished bottom-up synthesis of all RMs *****