Select and Configure Peripherals						
Available Peripherals		Included Peripherals for microblaze_0			Select All	
Peripheral Names		Coi	re	Parameter		
☐ IO Devices	Add > < Remove	DIP LED Pus QSF	R3_SDRAM (Cached) Core: axi_7series_ddrx _Switches_8Bits Core: axi_gpio 0s_8Bits Core: axi_gpio sh_Buttons_5Bits Core: axi_gpio PI_FLASH Core: axi_quad_spi, C_SPI_MODE: Stand	ā		
			32_Uart_1 RS232_Uart_1	axi_uartlite		_
			Baud Rate	115200		•
			Data Bit Width	8		¥
			Use Interrupt			
			Parity	None		•