

From Specification to Implementation

Holyjs Moscow 2019
Yulia Startsev | @ioctaptceb

```
class ChildNetworkResponseLoader {
  constructor(context, requestId) {
    this.context = context;
    this.requestId = requestId;
  }

  api() {
    const {context, requestId} = this;
    return {
      getContent(callback) {
        return context.childManager.callParentAsyncFunction(
          "devtools.network.Request.getContent",
          [requestId],
          callback);
      },
    };
  }
}
```

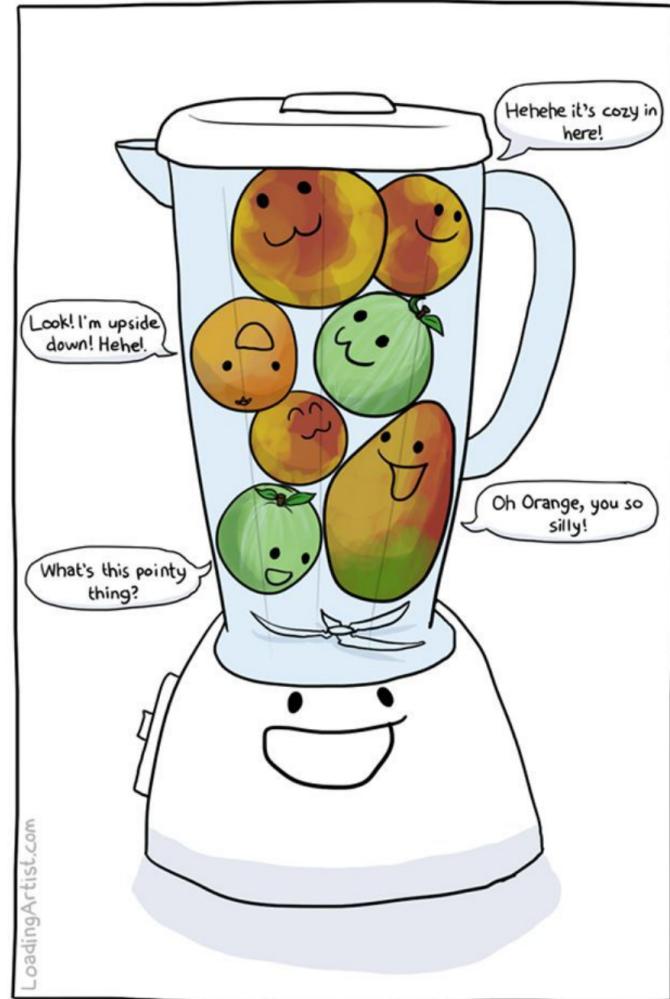






Image of the programmer at work

Yulia Startsev
@ioctaptceb
Mozilla SpiderMonkey Team

Overview

- Very brief Introduction to TC39
- Stage 0 - 3: Nullish Coalescing
 - Reading the spec
 - Issues that shaped the proposal
- Implementation details
 - Implementing the parsing
 - Implementing the byte code
- FREEDOM!

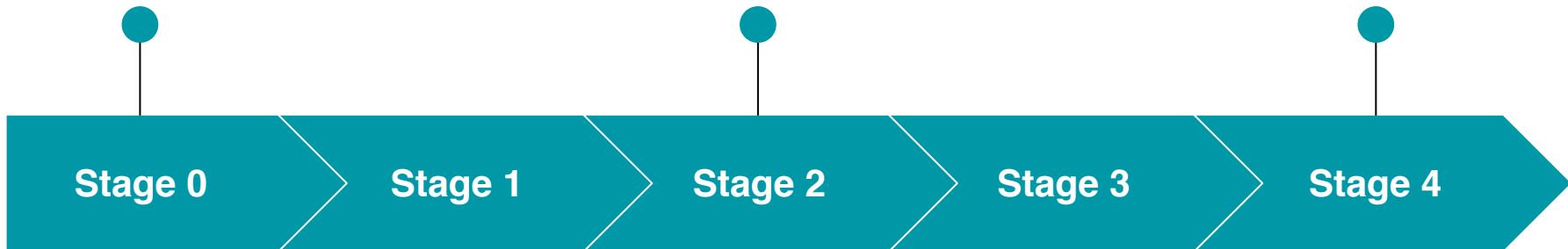
TC39 and its structure

- Part of Ecma International
- Technical Committee 39 of Ecma International
- Takes care of several standards aside from JavaScript, including ECMA-402, ECMA-404, ECMA-414
- Operates via “consensus”

Someone has an idea
and they write it up

Committee discusses if
this feature “should be
in the language”

Proposal is included in
the specification



The idea is presented to
the committee, committee
makes comments

Polyfill and browser
implementations, final form
of the proposal takes shape



Nullish Coalescing

??



Stage 0

- Allow input into the specification

Stage 1

- Make the case for the addition
- Describe the shape of a solution
- Identify potential challenges

Requirements

- Identified “champion” who will advance the addition
- Prose outlining the problem or need and the general shape of a solution
- Illustrative examples of usage
- High-level API
- Discussion of key algorithms, abstractions and semantics
- Identification of potential “cross-cutting” concerns and implementation challenges/complexity

Nullary Coalescing for JavaScript

Status

Current Stage:

- Stage 0

Authors

- Gabriel Isenberg ([github](#), [twitter](#))

Overview and motivation

When performing optional property access in a nested structure in conjunction with the [optional chaining operator](#), it is often desired to provide a default value if the result of that property access is `null` or `undefined`. At present, a typical way to express this intent in JavaScript is by using the `||` operator.

```
const response = {
  settings: {
    nullValue: null,
    height: 400,
    animationDuration: 0,
    headerText: '',
    showSplashScreen: false
  }
}.
```

- ⚙️ **why modify the language when a trivial helper function returnNotNullishOrDefault() can suffice?** 💬 12
#14 by kaizhu256 was closed on Nov 19, 2017 🕒 updated on Nov 19, 2017
- ⚙️ **Nit: "nullary" misused** 💬 25
#3 by BrendanEich was closed on Oct 14, 2017 🕒 updated on Oct 14, 2017
- ⚙️ **Is a new operator needed?** 💬 2
#5 by noppa was closed on Sep 14, 2017 🕒 updated on Sep 14, 2017

Stage 2

Precisely describe the syntax and semantics using formal spec language

The committee expects the feature to be developed and eventually included in the standard

Requirements

- Initial spec text
- all *major* semantics, syntax and API are covered, but TODOs, placeholders and editorial issues are expected

Stage 2 Draft

Nullish Coalescing Operator

Introduction

This document specifies the nullish coalescing operator `??`. See [the explainer](#) for an introduction.

The main design decisions made in this specification are:

1. The right argument of `??` is evaluated only if needed ("short circuiting").
2. `??` has the same precedence than `||`.
3. The right argument is selected if the left argument is `null` or `undefined`.

1 Binary Logical Operators

Syntax

LogicalORExpression `[In, Yield, Await]` :

LogicalANDExpression `[?In, ?Yield, ?Await]`

Syntax

Semantics

Structure

Meaning

Syntax

```
LogicalORExpression [In, Yield, Await]  :  
    LogicalANDExpression [?In, ?Yield, ?Await]  
    LogicalORExpression[?In, ?Yield, ?Await] || LogicalANDExpression[?In, ?Yield, ?Await]  
    LogicalORExpression[?In, ?Yield, ?Await] ?? LogicalANDExpression[?In, ?Yield, ?Await]
```

StatementList [**Return**] :

ReturnStatement

ExpressionStatement



StatementList :

ReturnStatement

ExpressionStatement

StatementList_Return :

ReturnStatement

ExpressionStatement

Issues that shaped the specification

?? has poor associativity/precedence, leading to counterintuitive behavior #26

Edit

New issue

Please simplify the grammar #44

Edit

New issue

Closed

waldemarhorwat opened this issue on Jul 24 · 13 comments · Fixed by #45

Rename LogicalExpression to ShortCircuitExpression #50

Edit

Merged

DanielRosenwas... merged 1 commit into tc39:master from rkirsling:patch-1 on Aug 22

Conversation 3

Commits 1

Checks 0

Files changed 1

+8 -8



rkirsling commented on Aug 8

Member + ...

A nitpick to be sure, but we do want to set a good example for the community at large. 😊

The word "logical" means "truthiness-oriented", so (unless we mean [ternary logic](#) now 😕) I think it's important not to bucket our nullish-oriented ?? with ||, &&, and ! in this way. The thing that ?? truly has in common with || and && is short-circuiting, so it's probably best to say just that.



2



[Rename LogicalExpression to ShortCircuitExpression](#)

Verified

9ac9a2c



ljharb approved these changes on Aug 8

[View changes](#)

Reviewers



ljharb



Assignees

No one—assign yourself



Labels

None yet



Projects

None yet



Milestone

No milestone



?? has poor associativity/precedence, leading to counterintuitive behavior #26

[Edit](#)[New issue](#)[Open](#)

waldemarhorwat opened this issue on Mar 15, 2018 · 21 comments



waldemarhorwat commented on Mar 15, 2018

Member

+ ...

When faced with a long short-circuiting expression such as

```
a || b || c || d || e
```

I usually think of it as selecting the first one of a, b, c, d, e that's not falsy and not evaluating the rest. In effect, I'm viewing the expression as though it were right-associative:

```
a || (b || (c || (d || e)))
```

That works fine. The spec grammar happens to actually specify it as left-associative:

```
((a || b) || c) || d) || e
```

However, that associativity is merely a spec-writing artifact, invisible to users. The spec could have associated the other way without any visible changes to existing user code behavior.

Unfortunately, introducing ?? at the same precedence level as || makes the spec associativity visible in the language and leads to problematic consequences:

Assignees



No one—assign yourself

Labels



None yet

Projects



None yet

Milestone



No milestone

Notifications

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```

24 24    <emu-grammar>
25 -      LogicalORExpression[In, Yield, Await] :
26 -        LogicalANDExpression[?In, ?Yield, ?Await]
27 -        LogicalORExpression[?In, ?Yield, ?Await] `||` LogicalANDExpression[?In, ?Yield, ?Await]
28 -        <ins>LogicalORExpression[?In, ?Yield, ?Await] `??` LogicalANDExpression[?In, ?Yield, ?Await]</ins>
25 +      ConditionalExpression[In, Yield, Await] :
26 +        <del>LogicalORExpression[?In, ?Yield, ?Await]</del>
27 +        <del>LogicalORExpression[?In, ?Yield, ?Await] `?` AssignmentExpression[+In, ?Yield, ?Await] `:` AssignmentExpr
28 +        <ins>NullishExpression[?In, ?Yield, ?Await, ~Nullish]</ins>
29 +        <ins>NullishExpression[?In, ?Yield, ?Await, ~Nullish] `?` AssignmentExpression[+In, ?Yield, ?Await] `:` AssignmentExpr
30 +
31 +        <ins>
32 +        NullishExpression[In, Yield, Await, Nullish] :
33 +          LogicalORExpression[?In, ?Yield, ?Await, ?Nullish]
34 +          LogicalORExpression[?In, ?Yield, ?Await, +Nullish] `??` NullishExpression[?In, ?Yield, ?Await, +Nullish]
35 +        </ins>
36 +
37 +        LogicalORExpression[In, Yield, Await, <ins>Nullish</ins>] :
38 +          LogicalANDExpression[?In, ?Yield, ?Await, <ins>?Nullish</ins>]
39 +          <del>LogicalORExpression[?In, ?Yield, ?Await] `||` LogicalANDExpression[?In, ?Yield, ?Await]</del>
40 +          <ins>[~Nullish] LogicalORExpression[?In, ?Yield, ?Await, ~Nullish] `||` LogicalANDExpression[?In, ?Yield, ?Await]
41 +
42 +        LogicalANDExpression[In, Yield, Await, <ins>Nullish</ins>] :
43 +          BitwiseORExpression[?In, ?Yield, ?Await]
44 +          <del>LogicalANDExpression[?In, ?Yield, ?Await] `&&` BitwiseORExpression[?In, ?Yield, ?Await]</del>
45 +          <ins>[~Nullish] LogicalANDExpression[?In, ?Yield, ?Await] `&&` BitwiseORExpression[?In, ?Yield, ?Await]</ins>
29 46    </emu-grammar>
30 47  </emu-clause>
31 48

```

StatementList [**Return**] :

[+Return] *ReturnStatement*

ExpressionStatement



StatementList :

ExpressionStatement

StatementList_Return :

ReturnStatement

ExpressionStatement

StatementList [**Return**] :

[~Return] *ReturnStatement*

ExpressionStatement



StatementList :

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ExpressionStatement



```
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26 +         <del>LogicalORExpression[?In, ?Yield, ?Await]</del>
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```

Please simplify the grammar #44

[Edit](#)[New issue](#)

waldemarhorwat opened this issue on Jul 24 · 13 comments · Fixed by #45



waldemarhorwat commented on Jul 24

Member + 😊 ...

The Coalesced grammar parameter is unnecessary. There are a variety of ways to get rid of it. Here is one possible simpler way to encode the grammar:

Modify *ConditionalExpression* to become:

ConditionalExpression :

LogicalExpression

LogicalExpression ? *ConditionalExpression* : *ConditionalExpression*

Write three new productions:

LogicalExpression :

LogicalORExpression

CoalesceExpression

CoalesceExpression :

BitwiseORExpression ?? *CoalesceExpressionRest*

CoalesceExpressionRest :

BitwiseORExpression

CoalesceExpression



2

Assignees

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Labels

None yet

Projects

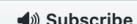
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Milestone

No milestone

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Rename LogicalExpression to ShortCircuitExpression #50

[Edit](#)**Merged**DanielRosenwas... merged 1 commit into [tc39:master](#) from [rkirsling:patch-1](#)  on Aug 22[Conversation 3](#)[Commits 1](#)[Checks 0](#)[Files changed 1](#)[+8 -8](#) 

rkirsling commented on Aug 8

Member

+ ...

A nitpick to be sure, but we do want to set a good example for the community at large. 😊

The word "logical" means "truthiness-oriented", so (unless we mean [ternary logic](#) now 😊) I think it's important not to bucket our nullish-oriented `??` with `||`, `&&`, and `!` in this way. The thing that `??` truly has in common with `||` and `&&` is short-circuiting, so it's probably best to say just that.



2

[Rename LogicalExpression to ShortCircuitExpression](#)

Verified



9ac9a2c



ljharb approved these changes on Aug 8

[View changes](#)

ljharb left a comment

Member

+ ...

I typically refer to pipes and amps as "value selection operators", but perhaps this is more accurate.

Reviewers



ljharb



Assignees

No one—assign yourself



Labels

None yet



Projects

None yet



Milestone

No milestone



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Nullish Coalescing Operator

Introduction

This document specifies the nullish coalescing operator `??`. See [the explainer](#) for an introduction.

The main design decisions made in this specification are:

1. The right argument of `??` is evaluated only if needed ("short circuiting").
2. `??` has lower precedence than `||`.
3. `??` cannot immediately contain, or be contained within, an `&&` or `||` operation.
4. The right argument is selected if the left argument is `null` or `undefined`.

1 Binary Logical Operators

Syntax

```
ShortCircuitExpression[In, Yield, Await] :  
  LogicalORExpression[?In, ?Yield, ?Await]  
  CoalesceExpression[?In, ?Yield, ?Await]
```

Stage 3

Indicate that further refinement will require feedback from implementations and users

The solution is complete and no further work is possible without implementation experience, significant usage and external feedback.

Requirements

- Complete spec text
- Designated reviewers have signed off on the current spec text
- All ECMAScript editors have signed off on the current spec text
- All semantics, syntax and API are completed described

How do we start?

Syntax
(structure)

Semantics
(meaning)

TokenStream

Parser

Bytecode

Interpreter

JITS

Syntax
(structure)

Semantics
(meaning)

TokenStream

Parser

Bytecode

Interpreter

JITS



Implementing Syntax

Syntax

ShortCircuitExpression[**In**, **Yield**, **Await**] :
 LogicalORExpression[?In, ?Yield, ?Await]
 CoalesceExpression[?In, ?Yield, ?Await]

CoalesceExpression[**In**, **Yield**, **Await**] :
 CoalesceExpressionHead[?In, ?Yield, ?Await] ??
 BitwiseORExpression[?In, ?Yield, ?Await]

CoalesceExpressionHead[**In**, **Yield**, **Await**] :
 CoalesceExpression[?In, ?Yield, ?Await]
 BitwiseORExpression[?In, ?Yield, ?Await]

Given a Minimal program

....

```
function coalesce(a, b) {  
    return a ?? b;  
}
```

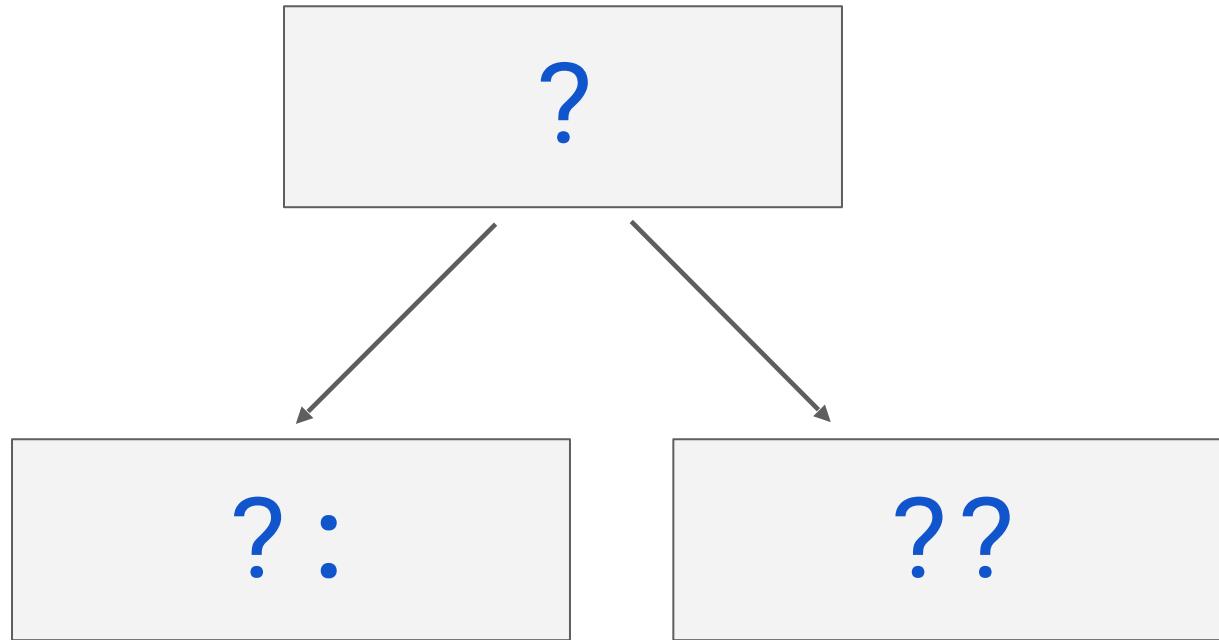
Text

```
function coalesce(a, b) {  
    return a ?? b;  
}
```



Tokens

```
[Function]  
[Iden="coalesce"] [LeftParen] [Iden="a"] [Comma] [Iden="b"]  
[RightParen] [LeftCurly]  
[Return] [Iden="a"] [Coalesce] [Iden="b"] [Semi]  
[RightCurly]
```



Ternary

Nullish Coalesce

Update TokenStream.cpp

```
2325
2326     LastCharKind = Other
2327 };
2328
2329 // OneChar: 40, 41, 44, 58, 59, 63, 91, 93, 123, 125, 126:
2330 //      '(', ')', ',', ':', ';', '?', '[', ']', '{', '}', '~'
2331 // Ident: 36, 65..90, 95, 97..122: '$', 'A'..'Z', '_', 'a'..'z'
2332 // Dot:    46: '.'
2333 // Equals: 61: '='
2334 // String: 34, 39, 96: "", "\\", ``
```

Update TokenStream.cpp

```
2344 #define T_LP size_t(TokenKind::LeftParen)
2345 #define T_RP size_t(TokenKind::RightParen)
2346 #define T_SEMI size_t(TokenKind::Semi)
2347 #define T_HOOK size_t(TokenKind::Hook)
2348 #define T_LB size_t(TokenKind::LeftBracket)

2361 /* 50+ */ Dec, T_COLON,
      T_SEMI,
2362 /* 60+ */ _____, _____, _____, T_HOOK, _____, Ident, Ident, Ident, Ident,
      Ident,
2363 /* 70+ */ Ident, Ident, Ident, Ident, Ident, Ident, Ident, Ident, Ident, Ident,
      Ident,
2364 /* 80+ */ Ident, Ident, Ident, Ident, Ident, Ident, Ident, Ident, Ident, Ident,
      Ident,

2376 #undef T_SEMI
2377 #undef T_HOOK
2378 #undef T_LB
2379 #undef T_RB
```

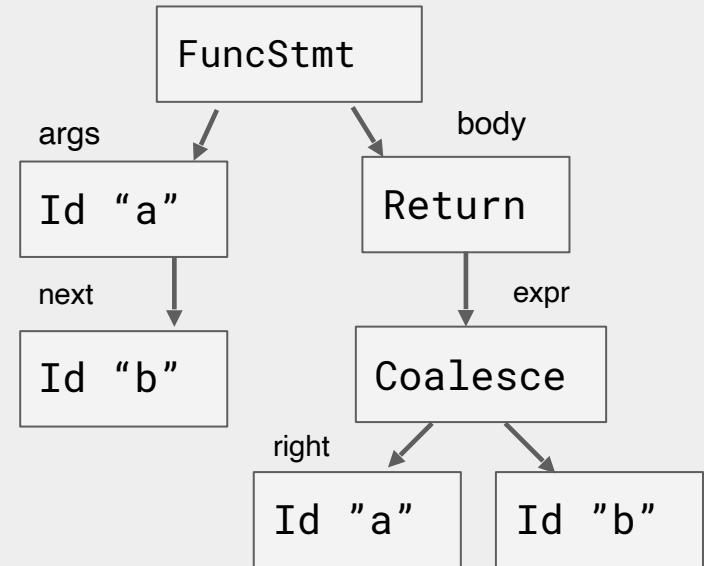
Update TokenKind.h List

```
159  /*
160   * Binary operators tokens, Or thru Pow. These must be in the same      \
161   * order as F(Or) and friends in FOR_EACH_PARSE_NODE_KIND in ParseNode.h. \
162   */
163 MACRO(Pipeline, "'|>''")
164 RANGE(BinOpFirst, Pipeline)
165 MACRO(Coalesce, "'??'")
166 MACRO(Or, "'||'") /* logical or */
167 MACRO(And, "'&&'") /* logical and */
168 MACRO(BitOr, "'|'") /* bitwise-or */
169 MACRO(BitXor, "'^'") /* bitwise-xor */
170 MACRO(BitAnd, "'&'") /* bitwise-and */
171
```

Update TokenStream::getTokenInternal

```
2733 template <typename Unit, class AnyCharsAccess>
2734 MOZ_MUST_USE bool TokenStreamSpecific<Unit, AnyCharsAccess>::getTokenInternal(
2735     TokenKind* const ttp, const Modifier modifier) {
2736
2737     switch (modifier) {
2738         case kNone:
2739             simpleKind = matchCodeUnit('=') ? TokenKind::BitAndAssign : TokenKind::BitAnd;
2740             break;
2741
2742         case kCoalesce:
2743             simpleKind = matchCodeUnit('?') ? TokenKind::Coalesce : TokenKind::Hook;
2744             break;
2745
2746         case kNot:
2747             simpleKind = matchCodeUnit('!') ? TokenKind::StrictNe : TokenKind::Ne;
2748             break;
2749
2750         case kCoerce:
2751             simpleKind = matchCodeUnit('!') ? TokenKind::Coerce : TokenKind::Not;
2752             break;
2753
2754         default:
2755             MOZ_ASSERT(false);
2756             break;
2757     }
2758
2759     if (simpleKind == TokenKind::None) {
2760         simpleKind = TokenKind::Not;
2761     }
2762
2763     *ttp = simpleKind;
2764
2765     return true;
2766 }
```

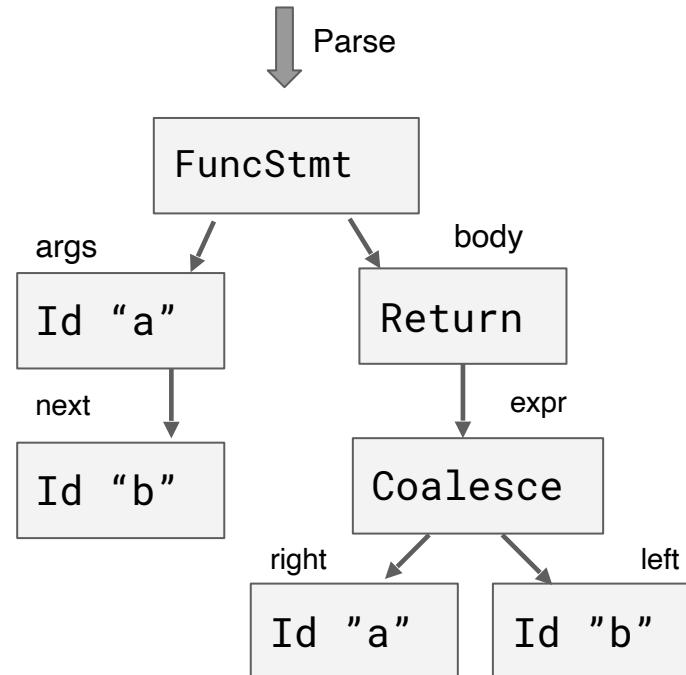
Building the AST



Tokens

```
[Function]
[Iden="coalesce"][LeftParen][Iden="a"][Comma][Iden="b"]
[RightParen][LeftCurly]
[Return][Iden="a"][Coalesce][Iden="b"][Semi]
[RightCurly]
```

Abstract Syntax Tree (AST)



ParseNode.h

```
162     /*                                     \
163      * Binary operators.                  \
164      * These must be in the same order as TOK_OR and friends in TokenStream.h. \
165      */                                \
166     F(PipelineExpr, ListNode)           \
167     F(CoalesceExpr, ListNode)          \
168     F(OrExpr, ListNode)                \
169     F(AndExpr, ListNode)               \
170     F(BitOrExpr, ListNode)             \
171     F(BitXorExpr, ListNode)            \
172     F(BitAndExpr, ListNode)            \
173     F(StrictEqExpr, ListNode)
```

Parser.cpp

```
8325 // This list must be kept in the same order in several places:  
8326 //   - The binary operators in ParseNode.h ,  
8327 //   - the binary operators in TokenKind.h  
8328 //   - the JSOp code list in BytecodeEmitter.cpp  
8329 static const int PrecedenceTable[] = {  
8330     1, /* ParseNodeKind::PipeLine */  
8331     2, /* ParseNodeKind::Coalesce */  
8332     3, /* ParseNodeKind::Or */  
8333     4, /* ParseNodeKind::And */  
8334     5, /* ParseNodeKind::BitOr */  
8335     6, /* ParseNodeKind::BitXor */  
8336     7, /* ParseNodeKind::BitAnd */  
8337     8, /* ParseNodeKind::StrictEq */  
8338     8, /* ParseNodeKind::Eq */  
8339     8, /* ParseNodeKind::StrictNe */
```

BytecodeEmitter.cpp

```
7513 // This list must be kept in the same order in several places:  
7514 //   - The binary operators in ParseNode.h ,  
7515 //   - the binary operators in TokenKind.h  
7516 //   - the precedence list in Parser.cpp  
7517 static const JSOp ParseNodeKindToJSOp[] = {  
7518     // JSOP_NOP is for pipeline operator which does not emit its own JSOp  
7519     // but has highest precedence in binary operators  
7520     JSOP_NOP,          JSOP_NOP,          JSOP_OR,           JSOP_AND, JSOP_BITOR,  
7521     JSOP_BITXOR,       JSOP_BITAND,       JSOP_STRICTEQ,    JSOP_EQ,   JSOP_STRICTNE,  
7522     JSOP_NE,           JSOP_LT,          JSOP_LE,           JSOP_GT,   JSOP_GE,  
7523     JSOP_INSTANCEOF,  JSOP_IN,          JSOP_LSH,         JSOP_RSH, JSOP_URSH,  
7524     JSOP_ADD,          JSOP_SUB,         JSOP_MUL,         JSOP_DIV, JSOP_MOD,  
7525     JSOP_POW};  
7526
```

If we don't keep the lists in sync, this happens

```
shouldBe(1 & null ?? 3, 0); // true
shouldBe(3 = null ?? 3, false); // false
shouldBe(3 ≠ null ?? 3, true); // false
shouldBe(3 ≡ null ?? 3, false); // true
shouldBe(3 ≢ null ?? 3, true); // true
shouldBe(1 < null ?? 3, false); // true
shouldBe(1 > null ?? 3, true); // true
shouldBe(1 ≤ null ?? 3, false); // false
shouldBe(1 ≥ null ?? 3, true); // TypeError: invalid
shouldBe(1 << null ?? 3, 1); // true
shouldBe(1 >> null ?? 3, 1); // true
shouldBe(1 >>> null ?? 3, 1); // true
```



Parser.cpp

```
8375 template <class ParseHandler, typename Unit>
8376 MOZ_ALWAYS_INLINE typename ParseHandler::Node
8377 GeneralParser<ParseHandler, Unit>::orExpr(
8378     InHandling inHandling, YieldHandling yieldHandling,
8379     TripledotHandling tripledotHandling, PossibleError* possibleError,
8380     InvokedPrediction invoked /* = PredictUninvoked */) {
```

```
8391 for (;;) {
8392     pn = unaryExpr(yieldHandling, triplidotHandling, possibleError, invoked);
8393     if (!pn) {
8394         return null();
8395     }
8396
8397     // If a binary operator follows, consume it and compute the
8398     // corresponding operator.
8399     TokenKind tok;
8400     if (!tokenStream getToken(&tok)) {
8401         return null();
8402     }
8415     pnk = BinaryOpTokenKindToParseNodeKind(tok);
8416 } else {
8417     tok = TokenKind::Eof;
8418     pnk = ParseNodeKind::Limit;
8419 }
```

Pretending to be `||`

Tiny bit of byte code

BytecodeEmitter.cpp

```
9230     case ParseNodeKind::CoalesceExpr:  
9231     case ParseNodeKind::OrExpr:  
9232     case ParseNodeKind::AndExpr:  
9233         if (!emitLogical(&pn->as<ListNode>())) {  
9234             return false;  
9235         }  
9236         break;
```

BytecodeEmitter.cpp

```
7558 bool BytecodeEmitter::emitLogical(ListNode* node) {  
7559     MOZ_ASSERT(node->isKind(ParseNodeKind::OrExpr) ||  
7560                 node->isKind(ParseNodeKind::CoalesceExpr) ||  
7561                 node->isKind(ParseNodeKind::AndExpr));
```

```
7575 /* Left-associative operator chain: avoid too much recursion. */  
7576 ParseNode* expr = node->head();  
7577 if (!emitTree(expr)) {  
    return false;  
}  
7579 JSOp op = (node->isKind(ParseNodeKind::OrExpr) || node->isKind(ParseNodeKind::Co  
alesceExpr) ? JSOP_OR : JSOP_AND);  
7581 JumpList jump;
```

Let's run it

?? has poor associativity/precedence, leading to counterintuitive behavior #26

[Edit](#)[New issue](#)[Open](#)

waldemarhorwat opened this issue on Mar 15, 2018 · 21 comments



waldemarhorwat commented on Mar 15, 2018

Member

+ 😊 ...

When faced with a long short-circuiting expression such as

```
a || b || c || d || e
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I usually think of it as selecting the first one of `a`, `b`, `c`, `d`, `e` that's not falsy and not evaluating the rest. In effect, I'm viewing the expression as though it were right-associative:

```
a || (b || (c || (d || e)))
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That works fine. The spec grammar happens to actually specify it as left-associative:

```
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However, that associativity is merely a spec-writing artifact, invisible to users. The spec could have associated the other way without any visible changes to existing user code behavior.

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8332     3, /* ParseNodeKind::Or */  
8333     4, /* ParseNodeKind::And */  
8334     5, /* ParseNodeKind::BitOr */  
8335     6, /* ParseNodeKind::BitXor */  
8336     7, /* ParseNodeKind::BitAnd */  
8337     8, /* ParseNodeKind::StrictEq */  
8338     8, /* ParseNodeKind::Eq */  
8339     8, /* ParseNodeKind::StrictNe */
```

Parser.cpp

```
8373 enum class EnforcedParentheses : uint8_t { CoalesceExpr, AndOrExpr, None };  
8374  
8375 template <class ParseHandler, typename Unit>  
8376 MOZ_ALWAYS_INLINE typename ParseHandler::Node  
8377 GeneralParser<ParseHandler, Unit>::orExpr(  
8378     InHandling inHandling, YieldHandling yieldHandling,  
8379     TripledotHandling tripledotHandling, PossibleError* possibleError,  
8380     InvokedPrediction invoked /* = PredictUninvoked */)  
8381     // Shift-reduce parser for the binary operator part of the JS expression  
8382     // syntax.
```

```
8413     switch (tok) {  
8414         case TokenKind::Or:  
8415         case TokenKind::And:  
8416             if (unparenthesizedExpression == EnforcedParentheses::CoalesceExpr) {  
8417                 error(JMSG_BAD_COALESCE_MIXING);  
8418                 return null();  
8419             }  
8420             // If we have not detected a mixing error at this point, record that  
8421             // we have an unparenthesized expression, in case we have one later.  
8422             unparenthesizedExpression = EnforcedParentheses::AndOrExpr;  
8423     }
```

```
8436     case TokenKind::Coalesce:  
8437         if (unparenthesizedExpression == EnforcedParentheses::AndOrExpr) {  
8438             error(JMSG_BAD_COALESCE_MIXING);  
8439             return null();  
8440         }  
8441         // If we have not detected a mixing error at this point, record that  
8442         // we have an unparenthesized expression, in case we have one later.  
8443         unparenthesizedExpression = EnforcedParentheses::CoalesceExpr;  
8444         break;  
8445 }
```

Let's run it

Syntax

ShortCircuitExpression[**In**, **Yield**, **Await**] :
 LogicalORExpression[?In, ?Yield, ?Await]
 CoalesceExpression[?In, ?Yield, ?Await]

CoalesceExpression[**In**, **Yield**, **Await**] :
 CoalesceExpressionHead[?In, ?Yield, ?Await] ??
 BitwiseORExpression[?In, ?Yield, ?Await]

CoalesceExpressionHead[**In**, **Yield**, **Await**] :
 CoalesceExpression[?In, ?Yield, ?Await]
 BitwiseORExpression[?In, ?Yield, ?Await]

Syntax Covered.

Syntax
(structure)

Semantics
(meaning)

TokenStream

Parser

Bytecode

Interpreter

JTS



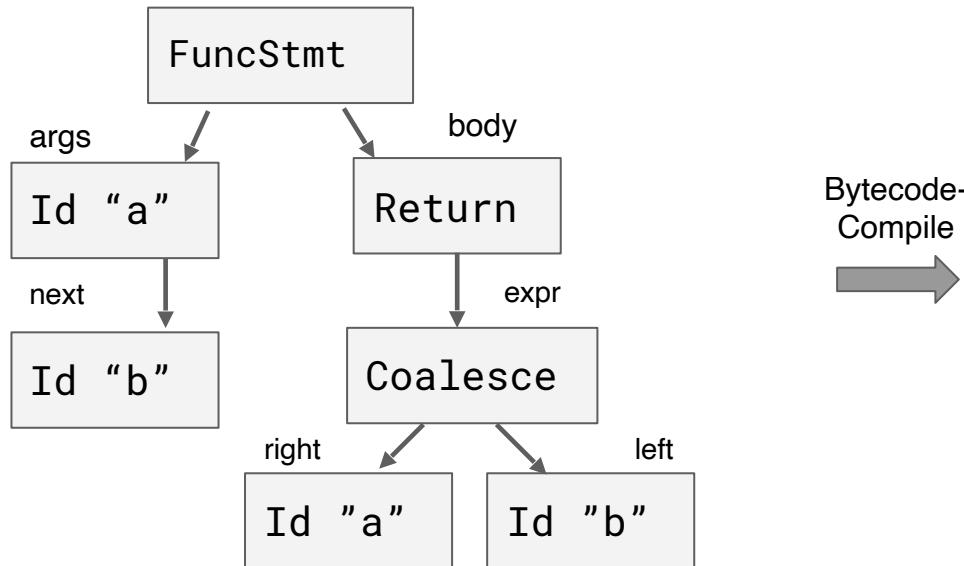
Implementing Semantics

1.3 Runtime Semantics: Evaluation

CoalesceExpression : *CoalesceExpressionHead* **??** *BitwiseORExpression*

1. Let *lref* be the result of evaluating *CoalesceExpressionHead*.
2. Let *lval* be ? *GetValue(lref)*.
3. If *lval* is **undefined** or **null**,
 - a. Let *rref* be the result of evaluating *BitwiseORExpression*.
 - b. Return ? *GetValue(rref)*.
4. Otherwise, return *lval*.

(AST)



Bytecode

JSScript

```
getarg 0
coalesce
jumptarget 0
pop
getarg 1
jumptarget 0
return
```

Bytecode-
Compile
→

C01E 8D F0	INHEX	BSR	INCH	GET A CHAR
C020 81 30		CMP A	#'0	ZERO
C022 2B 11		BMI	HEXERR	NOT HEX
C024 81 39		CMP A	#'9	NINE
C026 2F 0A		BLE	HEXRTS	GOOD HEX
C028 81 41		CMP A	#'A	

How do you feel about assembly?

C030 80 07		SUB A	#7	FIX A-F
C032 84 0F	HEXRTS	AND A	#\$0F	CONVERT ASCII TO DIGIT
C034 39		RTS		
C035 7E C0 AF	HEXERR	JMP	CTRL	RETURN TO CONTROL LOOP

- SUBMAXIMUM SELECTOR -

> TEST 1 PASSED IN 666 CYCLES
> RUNNING TEST 2...

	IN.A	IN.B	IN.C	IN.D	OUT
25	86	43	55	55	55
81	53	0	38	53	53
85	65	61	84	84	84
32	83	19	37	37	37
66	51	15	86	66	66
13	52	60	91	60	60
16	7	36	78	36	36
75	71	36	85	75	75
43	24	44	85	44	44
12	55	4	96	55	55
57	51	3	99	57	57
18	20	91	87	87	87
18	64	71	27	64	64
92	67	89	39	89	89
42	19	11	96	42	42
40	45	33	88	45	45
8	10	81	33	33	33
65	99	45	78	78	78
32	51	15	86	51	51
60	93	1	76	76	76
65	17	66	78	66	66
31	98	51	55	55	55
16	34	3	79	34	34
90	5	50	28	28	28
18	52	84	32	52	52
59	98	85	41	85	85
99	54	60	13	60	60
36	98	87	86	87	87
2	26	92	61	76	76
54	28	37	29	37	37
92	28	49	32	49	49
39	47	93	91	91	91
51	61	26	46	51	51
56	3	88	78	78	78
15	89	85	69	85	85
36	23	17	87	36	36
41	74	38	58	58	58
65	3	85	26	65	65
0	22	6	13	13	13

IN.A ↓ 36
(IDLE 88x)

IN.B ↓ 23
(IDLE 88x)

IN.C ↓ 17
(IDLE 88x)

IN.D ↓ 87
(IDLE 88x)

MOU UP ACC	ACC 15
MOU ACC RIGHT	BAK <0>
MOU ACC RIGHT	LAST N/A
MOU RIGHT DOWN	MODE WRITE
	IDLE 58x

START: MOU UP ACC	ACC 70
MOU ACC RIGHT	BAK <05>
MOU ACC RIGHT	LAST N/A
MOU RIGHT ACC	SUP
SUB LEFT	JLZ LEFT
JLZ LEFT	SUP
RIGHT: MOU LEFT LEFT	JMP EXIT
LEFT: MOU LEFT ACC	SUP
SUP	LAST N/A
MOU ACC LEFT	MODE RUN
SUP	IDLE 8x

START: MOU UP ACC	ACC 89
SAU	BAK <05>
SUB LEFT	JLZ LEFT
JLZ LEFT	SUP
RIGHT: MOU LEFT LEFT	JMP EXIT
LEFT: MOU LEFT ACC	SUP
SUP	LAST N/A
MOU ACC RIGHT	MODE RUN
EXIT: MOU ACC DOWN	IDLE 14x

START: MOU UP ACC	ACC 89
SAU	BAK <69>
SUB LEFT	JLZ LEFT
JLZ LEFT	SUP
RIGHT: MOU LEFT LEFT	JMP EXIT
LEFT: MOU LEFT ACC	SUP
SUP	LAST N/A
MOU ACC LEFT	MODE RUN
SUP	IDLE 38x

↓ 15

MOU UP ACC	ACC 3
MOU ACC RIGHT	BAK <0>
MOU ACC RIGHT	LAST N/A
MOU RIGHT NIL	MODE READ
	IDLE 65x

START: MOU UP ACC	ACC 56
MOU ACC RIGHT	BAK <53>
MOU ACC RIGHT	LAST N/A
MOU RIGHT ACC	SUP
SUB LEFT	JLZ LEFT
JLZ LEFT	SUP
RIGHT: MOU LEFT LEFT	JMP START
LEFT: MOU LEFT ACC	SUP
SUP	LAST N/A
MOU ACC LEFT	MODE READ
SUP	IDLE 24x

START: MOU UP ACC	ACC 78
SAU	BAK <22>
SUB LEFT	JLZ LEFT
JLZ LEFT	SUP
RIGHT: MOU LEFT LEFT	JMP EXIT
LEFT: MOU LEFT ACC	SUP
SUP	LAST N/A
MOU ACC LEFT	MODE RUN
EXIT: MOU ACC RIGHT	IDLE 16x

START: MOU UP ACC	ACC 88
SAU	BAK <18>
SUB LEFT	JLZ LEFT
JLZ LEFT	SUP
RIGHT: MOU LEFT LEFT	JMP START
LEFT: MOU LEFT ACC	SUP
SUP	LAST N/A
MOU ACC LEFT	MODE RUN
SUP	IDLE 53x

↓ ?

MOU UP NIL	ACC 0
	BAK <0>
	LAST N/A
	MODE READ
	IDLE 100x

MOU UP DOUN	ACC 0
	BAK <0>
	LAST N/A
	MODE WRITE
	IDLE 88x

MOU UP NIL	ACC 0
	BAK <0>
	LAST N/A
	MODE READ
	IDLE 100x

MOU UP NIL	ACC 0
	BAK <0>
	LAST N/A
	MODE READ
	IDLE 100x

↓ ?

COMMUNICATION FAILURE	
BASIC	



OUT ↓ 76

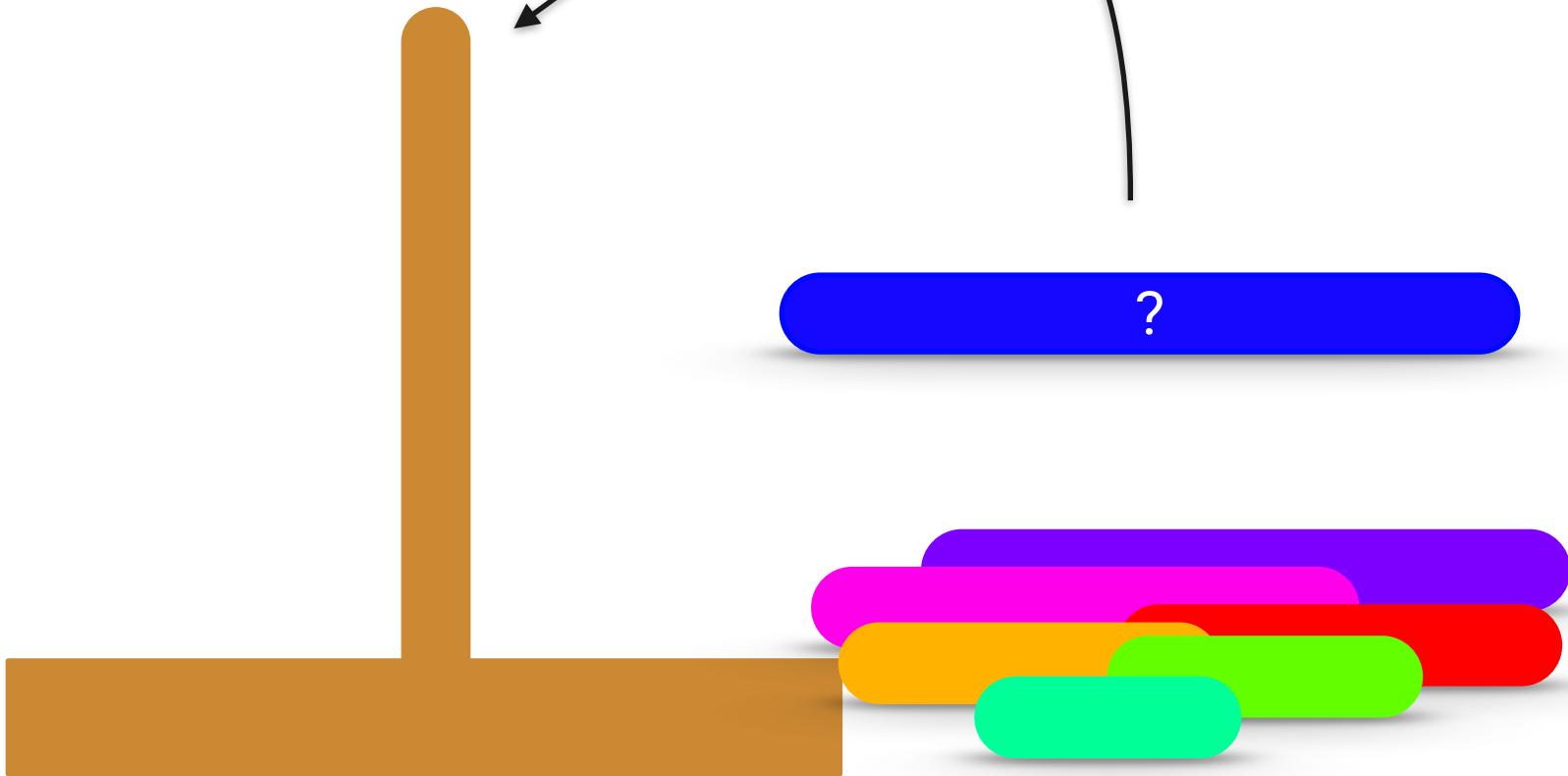
“the assembly writing game you never knew you wanted”

What is a stack?



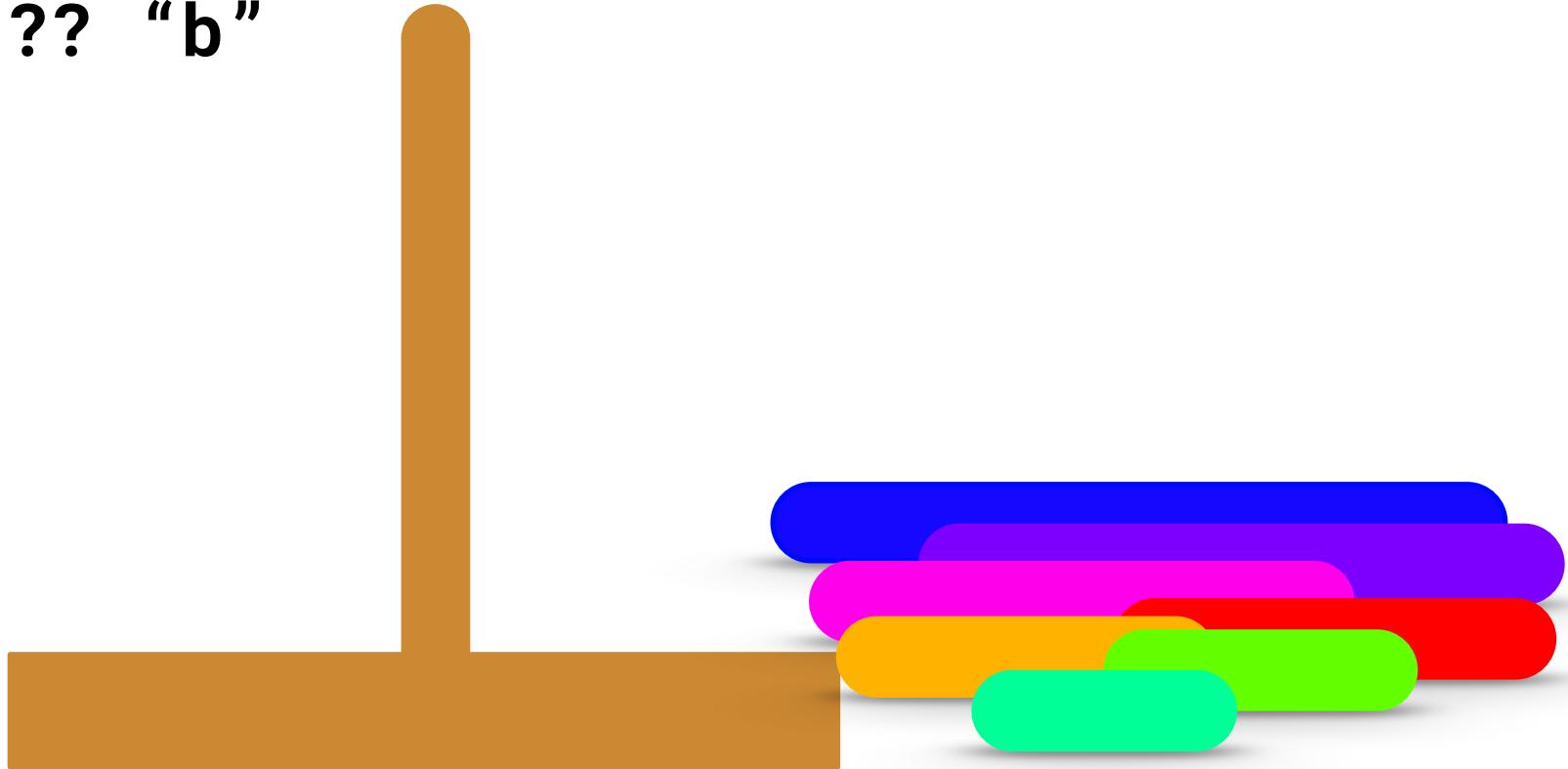
Instruction

What is a stack?



input:

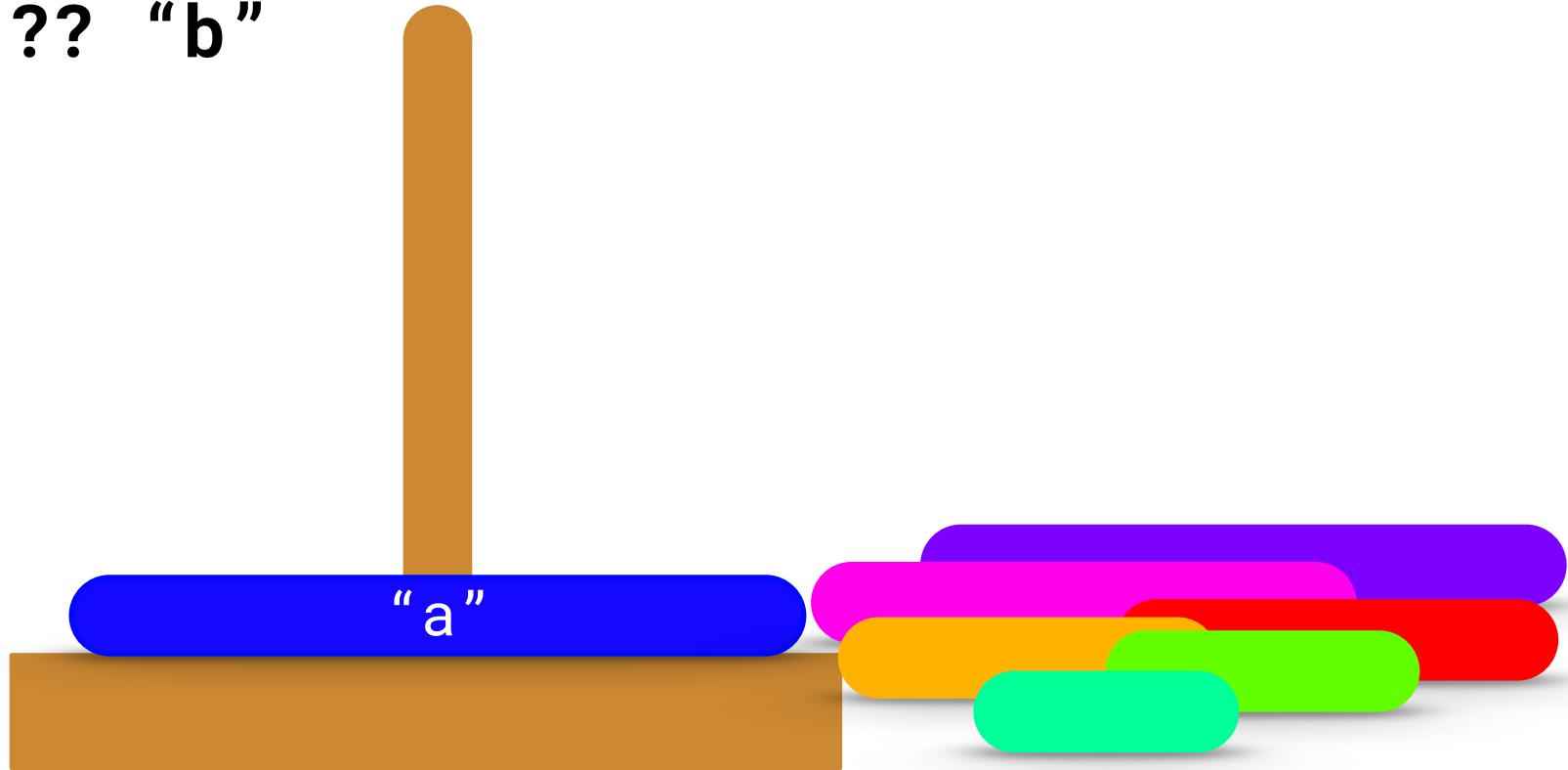
“a” ?? “b”



get 0

input:

“a” ?? “b”



JSOP_DUP [-1, +2]

Value	12 (0x0c)
Operands	
Length	1
Stack Uses	v
Stack Defs	v, v

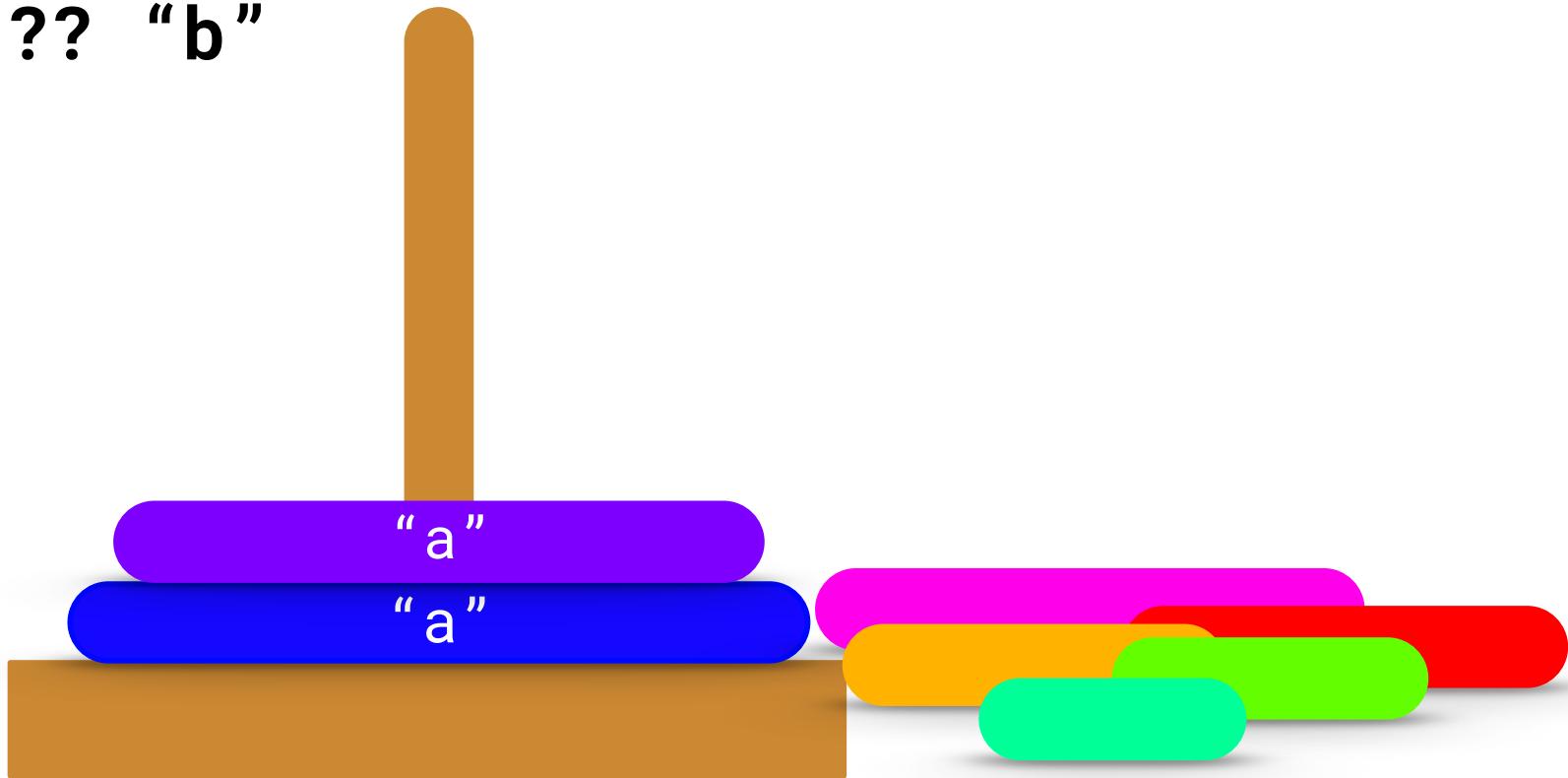
Pushes a copy of the top value on the stack.

input:

“a” ?? “b”

get 0

JSOP_DUP



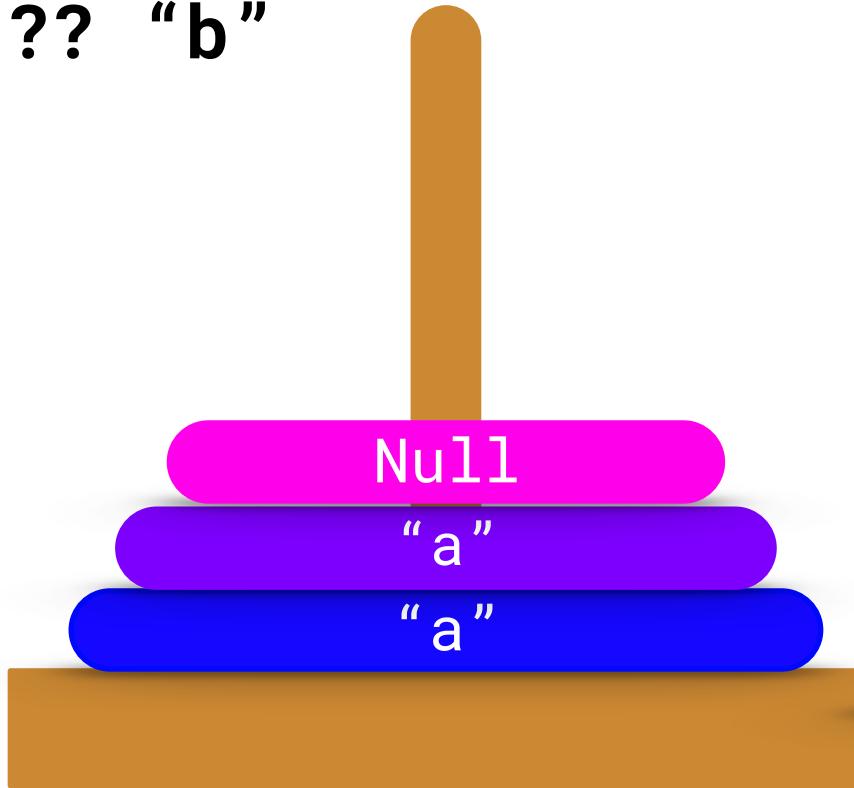
JSOP_NULL [-0, +1]

Value	64 (0x40)
Operands	
Length	1
Stack Uses	
Stack Defs	null

Pushes `null` onto the stack.

input:

“a” ?? “b”



get 0
JSOP_DUP
JSOP_NULL

JSOP_NE [-2, +1] (DETECTING, IC)

Value	JSOP_NE: 19 (0x13)
Operands	
Length	1
Stack Uses	lval, rval
Stack Defs	(lval OP rval)

Pops the top two values from the stack and pushes the result of comparing them.

input:

“a” ?? “b”



True

“a”

get 0

JSOP_DUP

JSOP_NULL

JSOP_NE

JSOP_IFNE [-1, +0] (JUMP, IC)

Value	8 (0x08)
Operands	int32_t offset
Length	5
Stack Uses	cond
Stack Defs	

Pops the top of stack value, converts it into a boolean, if the result is true, jumps to a 32-bit offset from the current bytecode.

JSOP_IFEQ [-1, +0] (JUMP, DETECTING, IC)

Value	7 (0x07)
Operands	int32_t offset
Length	5
Stack Uses	cond
Stack Defs	

Pops the top of stack value, converts it into a boolean, if the result is false, jumps to a 32-bit offset from the current bytecode.

The idea is that a sequence like JSOP_ZERO; JSOP_ZERO; JSOP_EQ; JSOP_IFEQ; JSOP_RETURN; reads like a nice linear sequence that will execute the return.

input:

“a” ?? “b”



“a”



get 0
JSOP_DUP
JSOP_NULL
JSOP_NE
JSOP_IFNE Jump



input:

“a” ?? “b”



“a”



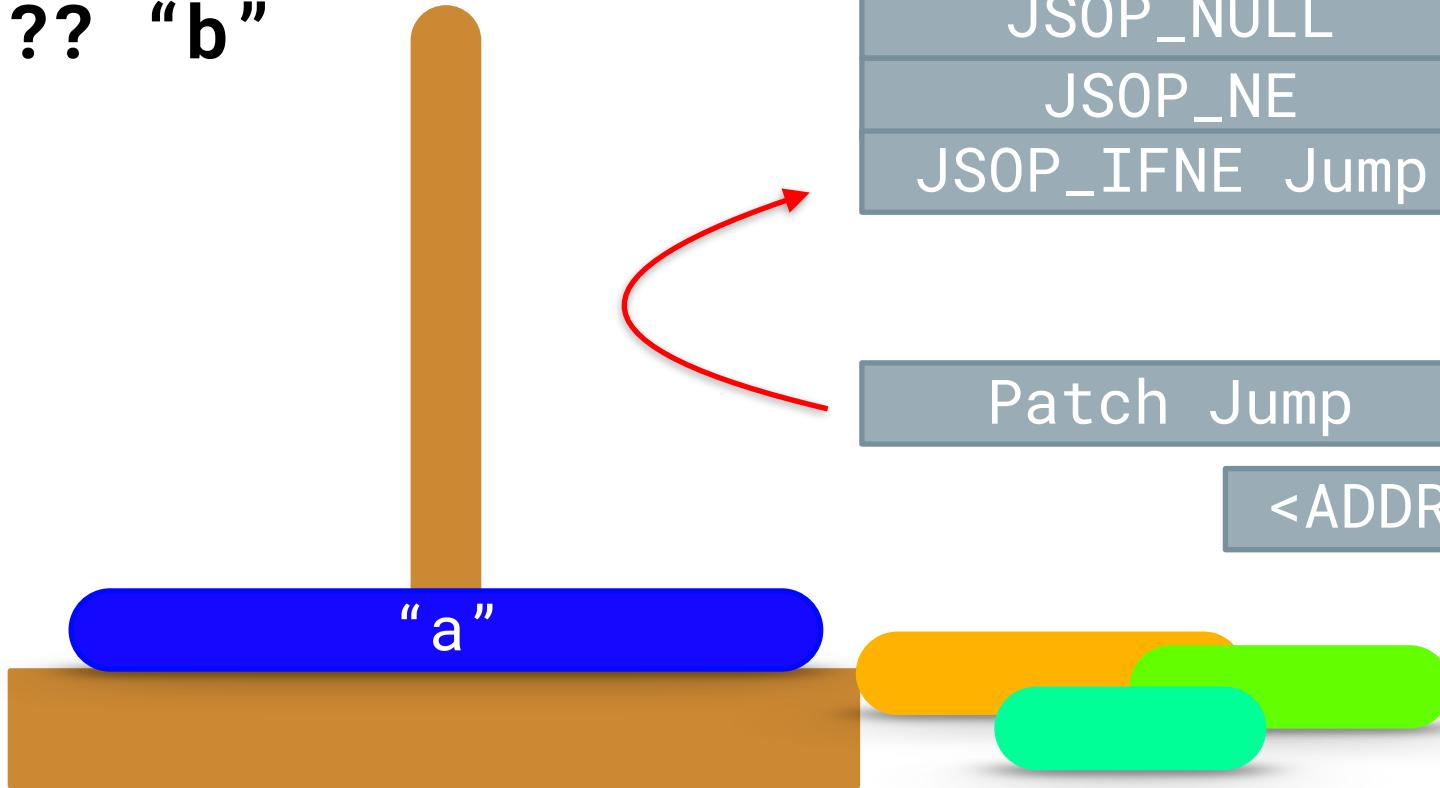
get 0
JSOP_DUP
JSOP_NULL
JSOP_NE
JSOP_IFNE Jump

Patch Jump



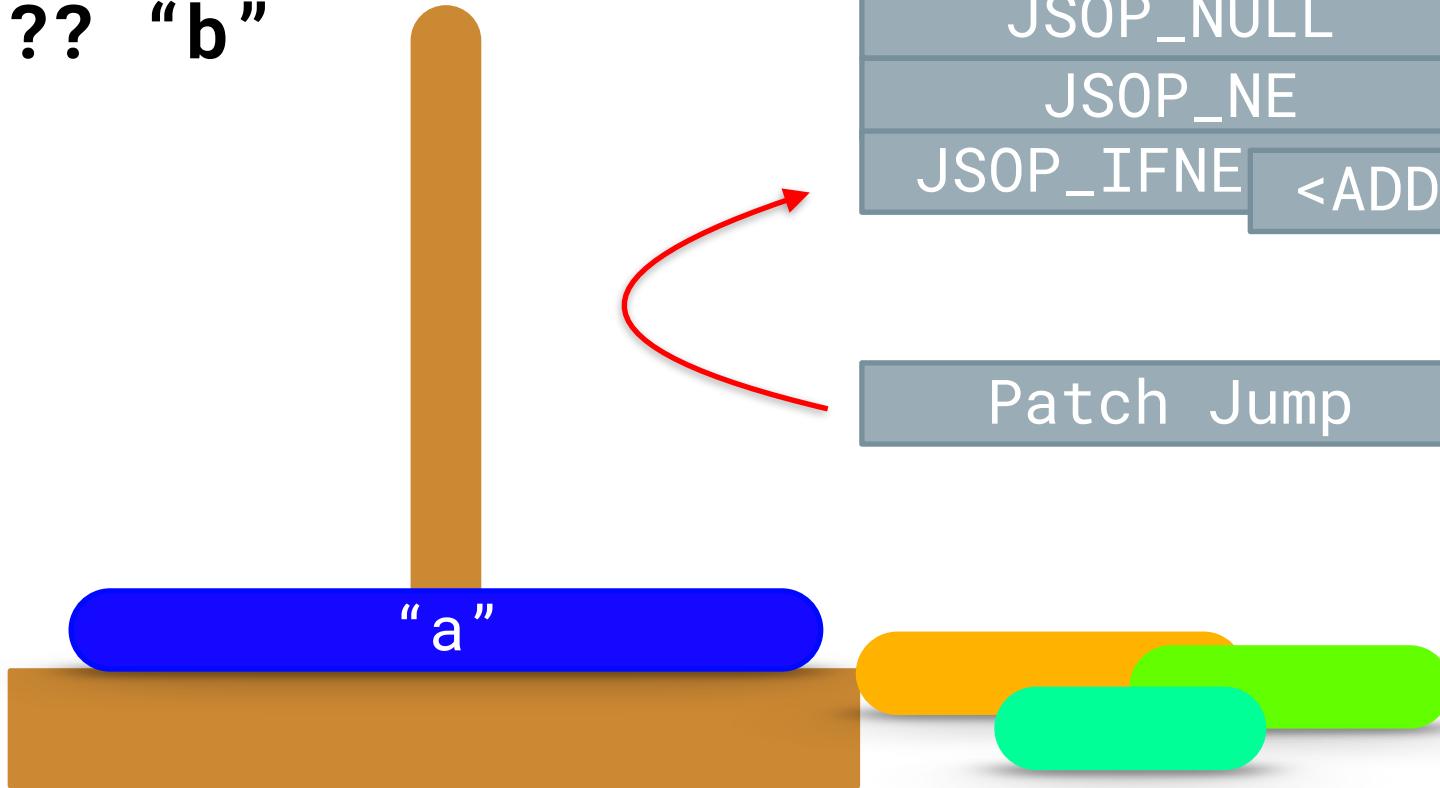
input:

“a” ?? “b”

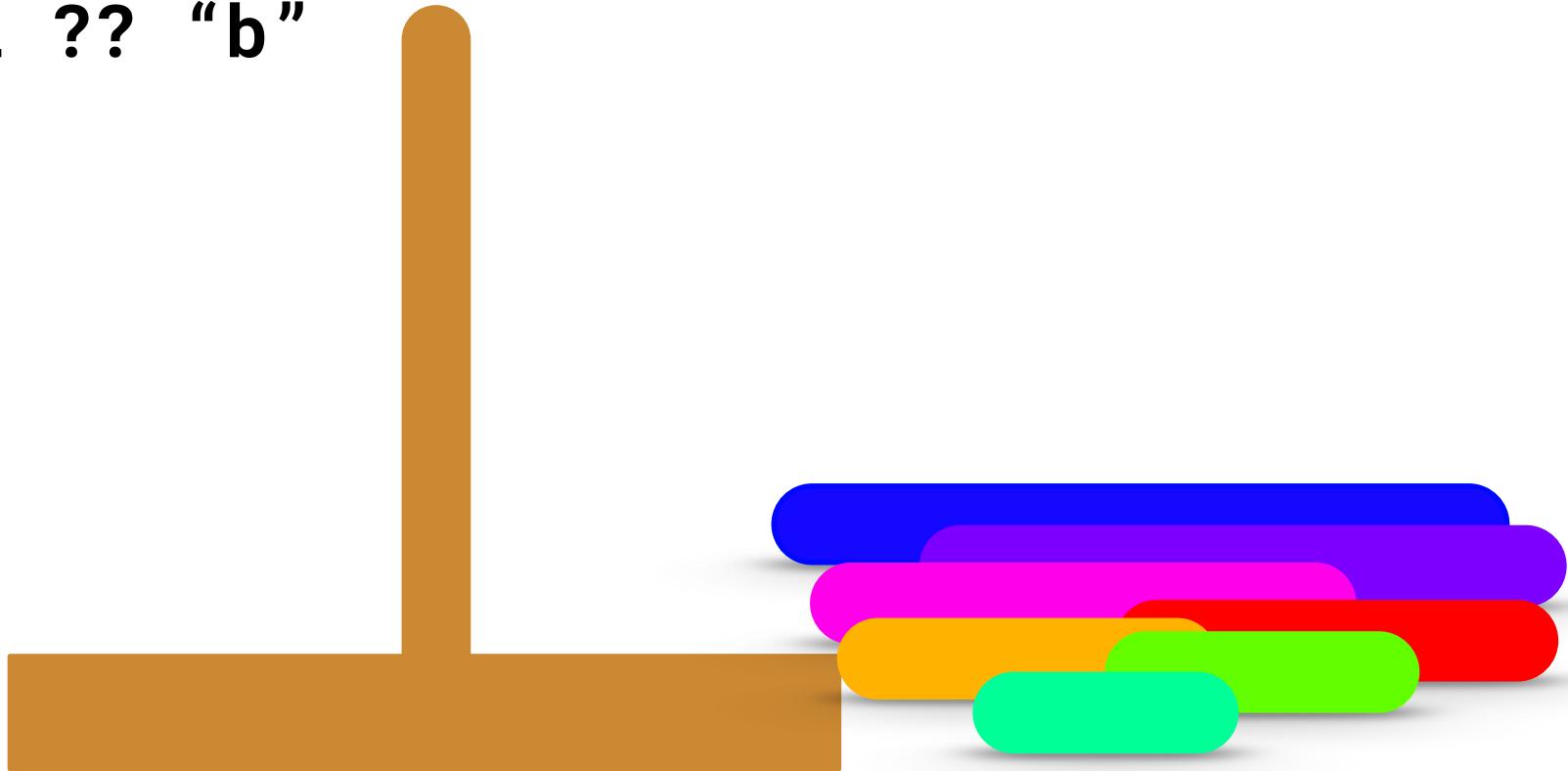


input:

“a” ?? “b”



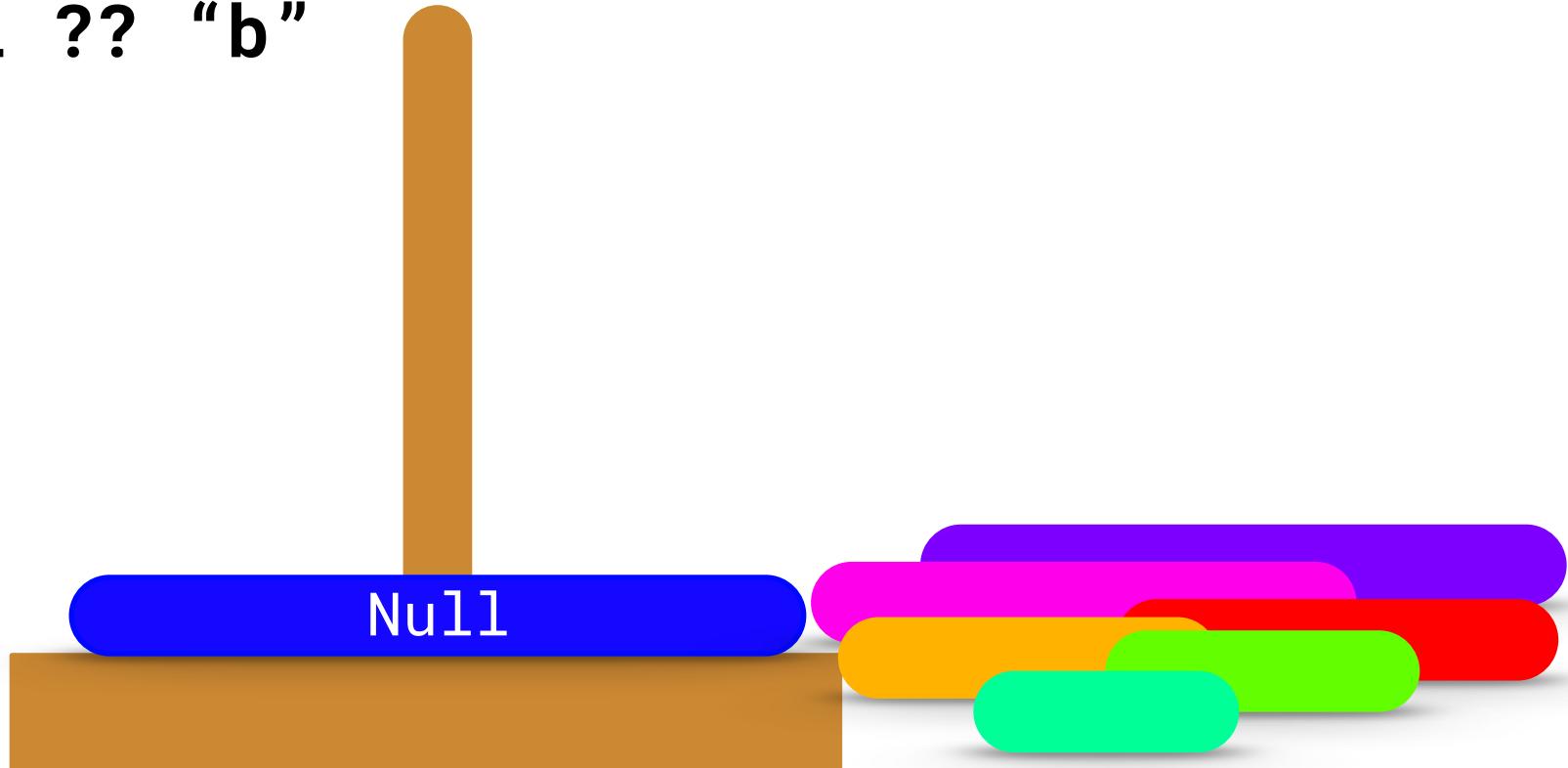
input:
null ?? “b”



get 0

input:

null ?? "b"



input:

null ?? "b"

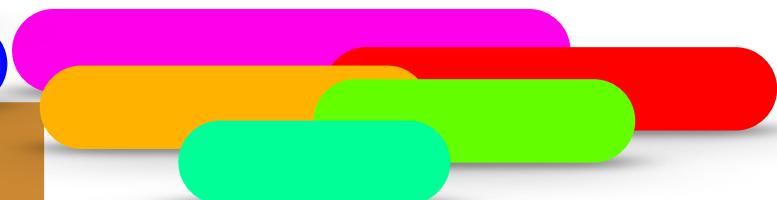
get 0

JSOP_DUP



Null

Null

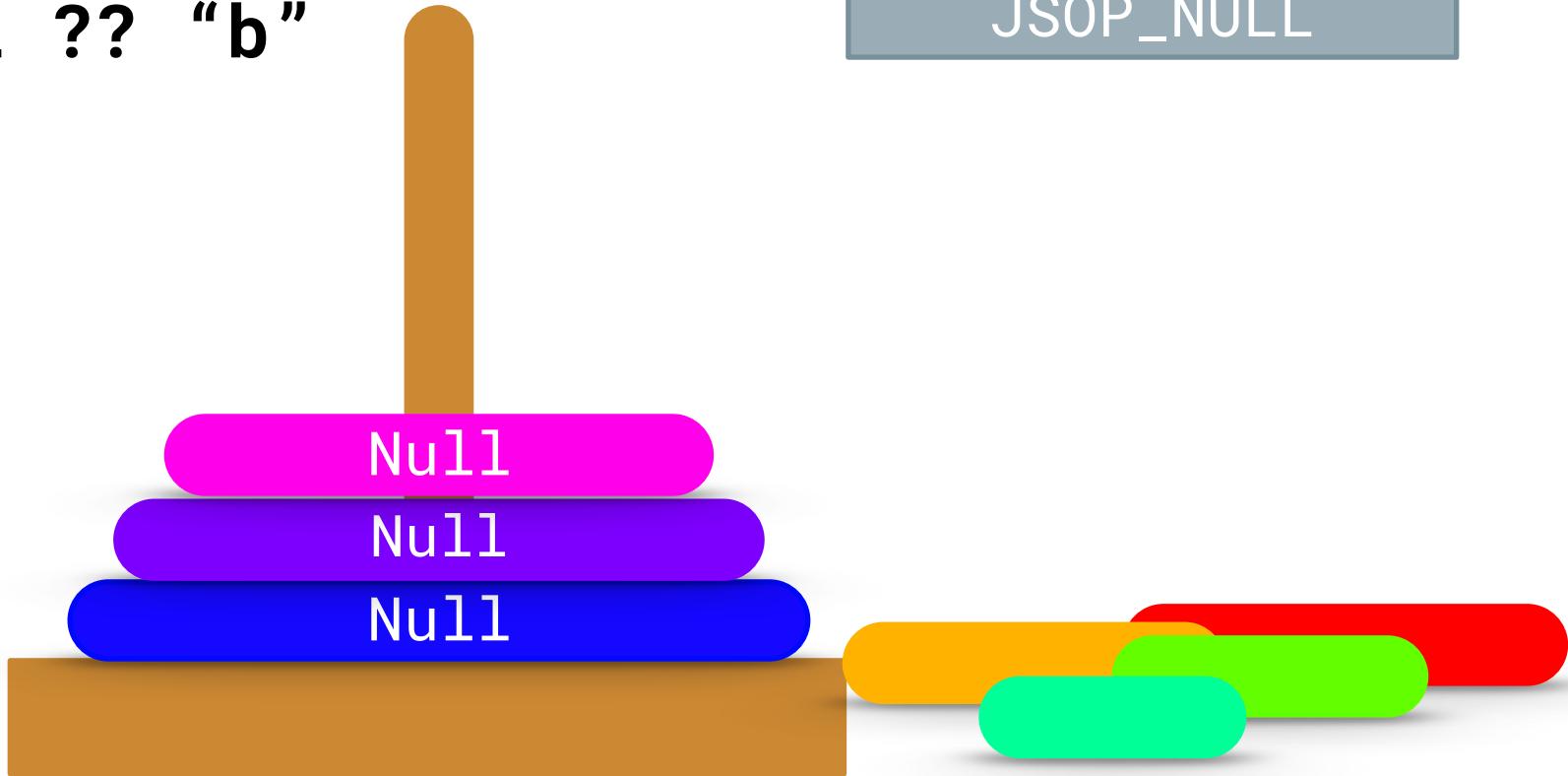


input:
null ?? “b”

get 0

JSOP_DUP

JSOP_NULL



input:

null ?? "b"



False

Null



get 0
JSOP_DUP
JSOP_NULL
JSOP_NE



input:

null ?? "b"



Null

get 0
JSOP_DUP
JSOP_NULL
JSOP_NE
JSOP_IFNE Jump

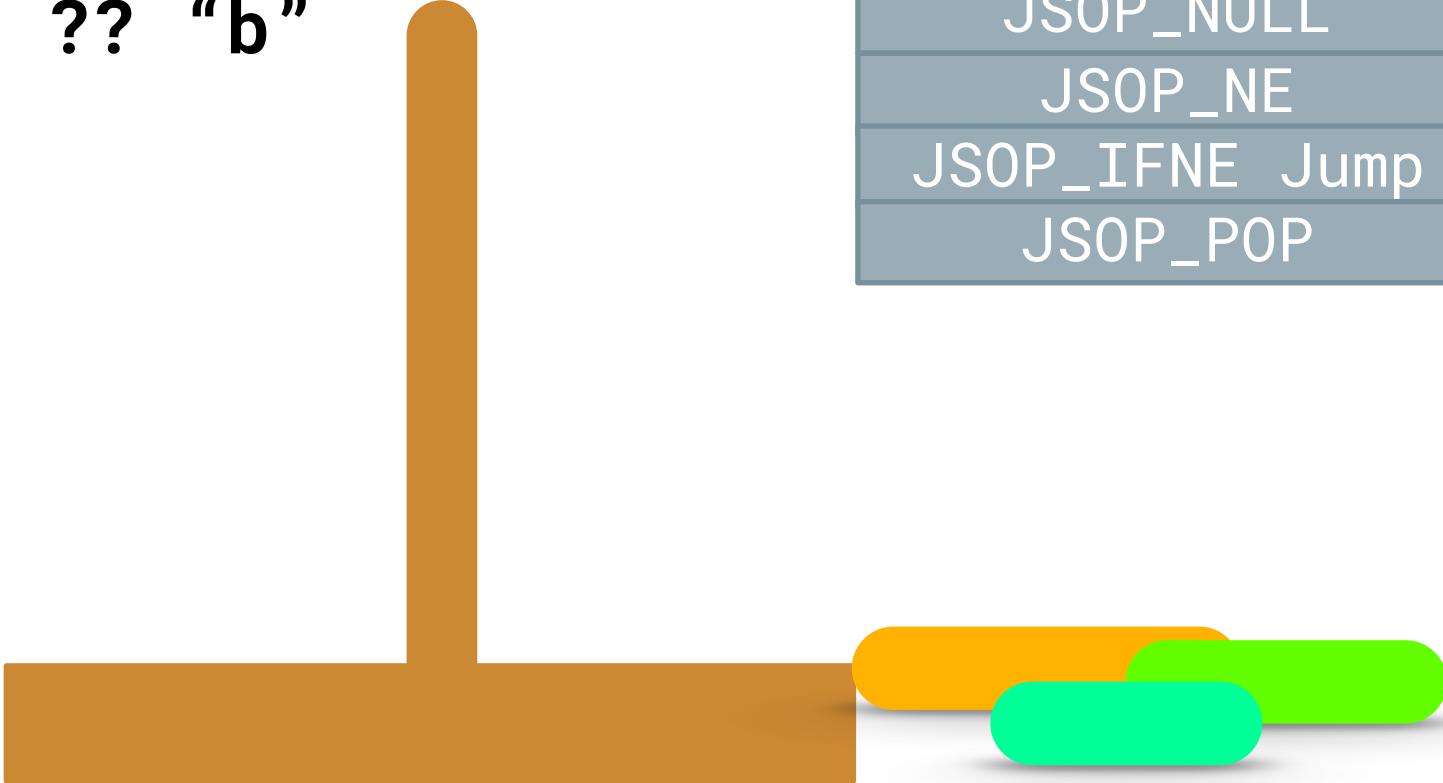


JSOP_POP [-1, +0]

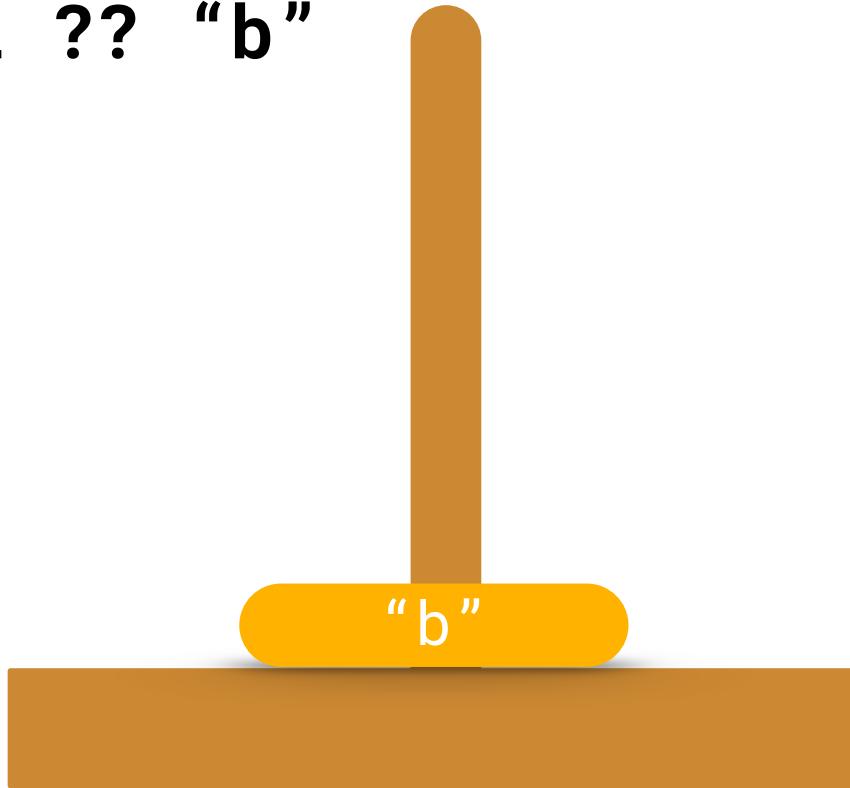
Value	81 (0x51)
Operands	
Length	1
Stack Uses	v
Stack Defs	

Pops the top value off the stack.

input:
null ?? “b”



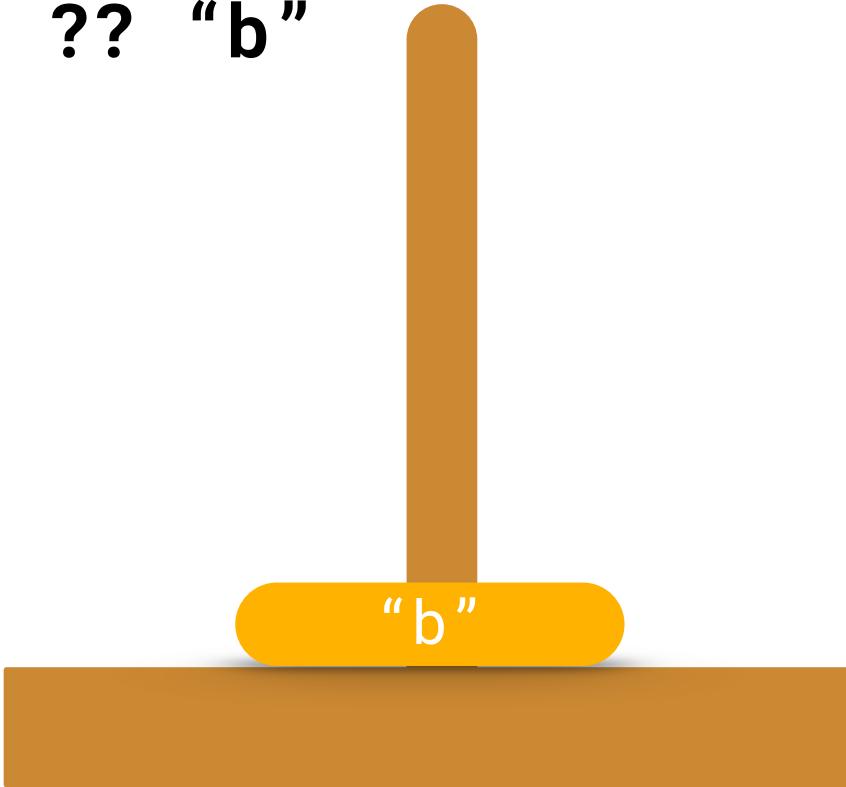
input:
null ?? “b”



get 0
JSOP_DUP
JSOP_NULL
JSOP_NE
JSOP_IFNE Jump
JSOP_POP
get 1

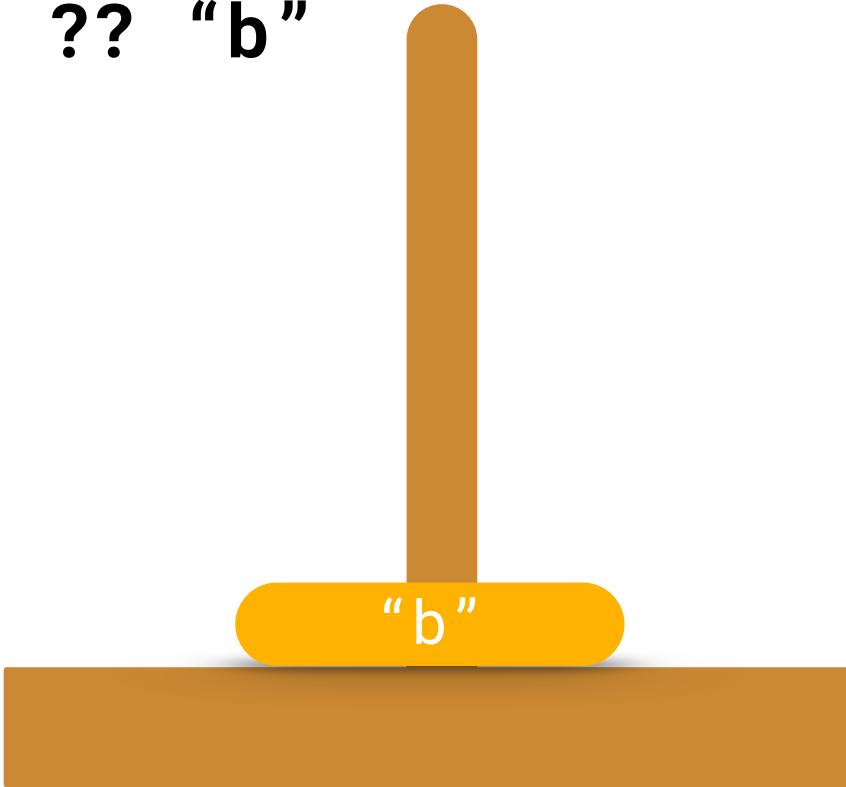


input:
null ?? “b”



get 0
JSOP_DUP
JSOP_NULL
JSOP_NE
JSOP_IFNE Jump
JSOP_POP
get 1
Patch Jump

input:
null ?? “b”



get 0
JSOP_DUP
JSOP_NULL
JSOP_NE
JSOP_IFNE Jump
JSOP_POP
get 1
Patch Jump



The implementation

```
7576 bool BytecodeEmitter::emitNullCoalesce(ListNode* node) {  
7577     MOZ_ASSERT(node->isKind(ParseNodeKind::CoalesceExpr));  
7578     TDZCheckCache tdzCache(this);  
7579     JumpList jump;  
7580     for (ParseNode* expr = node->head(); expr = expr->pn_next) {  
7581         // Loop  
7582     }  
7583  
7584     // end  
7585  
7586     return true;  
7587 }
```

JSOP_IFNE [-1, +0] (JUMP, IC)

Value	8 (0x08)
Operands	int32_t offset
Length	5
Stack Uses	cond
Stack Defs	

Pops the top of stack value, converts it into a boolean, if the result is true, jumps to a 32-bit offset from the current bytecode.

JSOP_IFEQ [-1, +0] (JUMP, DETECTING, IC)

Value	7 (0x07)
Operands	int32_t offset
Length	5
Stack Uses	cond
Stack Defs	

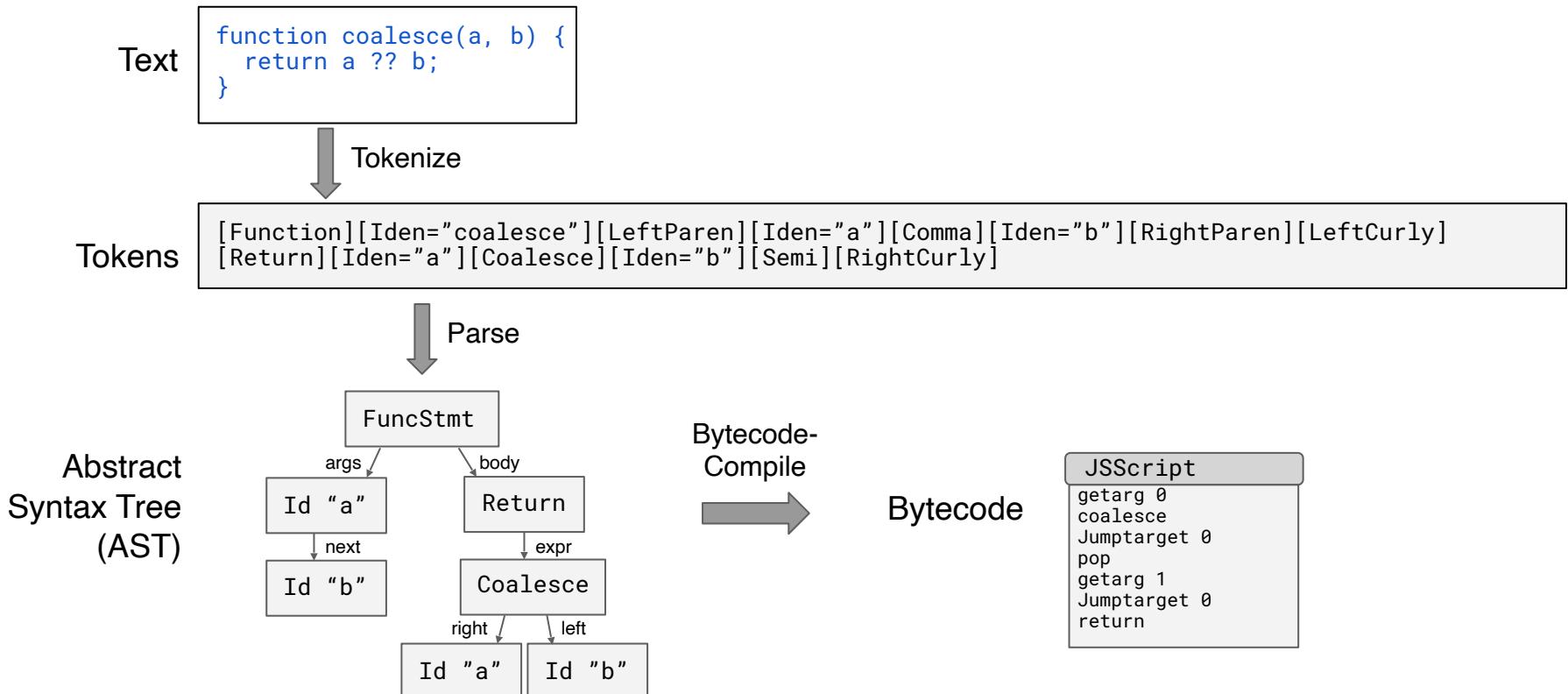
Pops the top of stack value, converts it into a boolean, if the result is false, jumps to a 32-bit offset from the current bytecode.

The idea is that a sequence like JSOP_ZERO; JSOP_ZERO; JSOP_EQ; JSOP_IFEQ; JSOP_RETURN; reads like a nice linear sequence that will execute the return.

The implementation

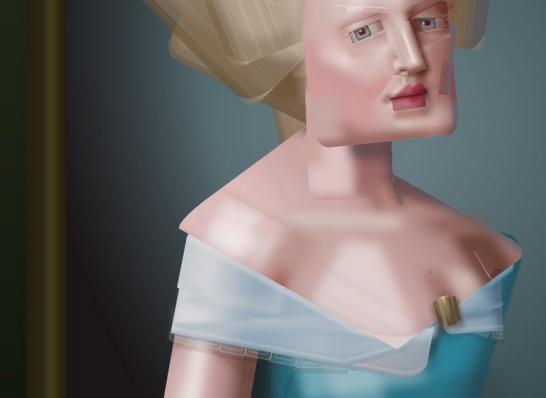
```
7576 bool BytecodeEmitter::emitNullCoalesce(ListNode* node) {  
7577     MOZ_ASSERT(node->isKind(ParseNodeKind::CoalesceExpr));  
7578     TDZCheckCache tdzCache(this);  
7579     JumpList jump;  
7580     for (ParseNode* expr = node->head(); expr = expr->pn_next) {  
7581         // Loop  
7582     }  
7583  
7584     // end  
7585  
7586     return true;  
7587 }
```

Full Parse



<aside>





David Zhou  @dz

i tried [@cyanharlow](#)'s incredible pure css portrait in an old version of opera and well, the disclaimer wasn't lying: "so the live preview will most likely look laughable in anything other than chrome"[github.com/cyanharlow/pur...](https://github.com/cyanharlow/purecss-francine)

7:17 PM - May 1, 2018

 1,269  424 people are talking about this

Pure CSS Francine by Diana Smith

</aside>

```
>> document.all|
```



Image of the programmer at work

```
>> document.all
```



Image of the programmer at work

JSOP_STRICTEQ [-2, +1] (DETECTING, IC)

Value	JSOP_STRICTEQ: 72 (0x48)
Operands	
Length	1
Stack Uses	lval, rval
Stack Defs	(lval OP rval)

Pops the top two values from the stack, then pushes the result of applying the operator to the two values.

```
756 bool BytecodeEmitter::emitNullCoalesce(ListNode* node) {
757 MOZ_ASSERT(node->isKind(ParseNodeKind::CoalesceExpr));
758 TDZCheckCache tdzCache(this);
759 JumpList jump;
760 for (ParseNode* expr = node->head(); expr = expr->pn_next) {
761     if (!emitTree(expr)) { return false; }
762
763     if (!expr->pn_next) { break; }
764
765     if (!emitPushNotUndefinedOrNull()) { return false; }
766
767     if (!emit1(JSOP_NOT)) { return false; }
768
769     if(!this->newSrcNote(SRC_IF)) { return false; }
770
771     if (!emitJump(JSOP_IFEQ, &jump)) { return false; }
772
773     if (!emit1(JSOP_POP)) { return false; }
774 }
775
776     if (!emitJumpTargetAndPatch(jump)) { return false; }
777
778     return true;
779 }
```

Inspecting the byte code

```
js> function foo(a, b) { a ?? b }
js> dis(foo)
flags: CONSTRUCTOR
loc      op
----- --
main:
00000:  getarg 0                      # a
00003:  dup                         # a a
00004:  undefined                   # a a undefined
00005:  strictne                   # a (a != undefined)
00006:  and 20 (+14)                # a (a != undefined)
00011:  jumptarget (ic: 5)          # a (a != undefined)
00016:  pop                          # a
00017:  dup                          # a a
00018:  null                        # a a null
00019:  strictne                   # a (a != null)

# from and @ 00006
00020:  jumptarget (ic: 6)          # a merged<(a != undefined)>
00025:  ifeq 40 (+15)               # a
00030:  jumptarget (ic: 7)          # a
00035:  goto 49 (+14)               # a

# from ifeq @ 00025
00040:  jumptarget (ic: 7)          # a
00045:  pop                         #
00046:  getarg 1                  # b

# from goto @ 00035
00049:  jumptarget (ic: 7)          # merged<a>
00054:  pop                         #
00055:  retrval
```



Image of the programmer at work

JSOP_COALESCE

JSOP_COALESCE bytecode

```
js> dis(foo)
flags: CONSTRUCTOR
loc      op
-----  --
main:
00000: getarg 0                      # a
00003: coalesce 17 (+14)             # <unknown>
00008: jumptarget (ic: 3)            # <unknown>
00013: pop                           #
00014: getarg 1                      # b

# from coalesce @ 00003
00017: jumptarget (ic: 3)            # merged<<unknown>>
00022: return                         #
00023: retrval                       # !!! UNREACHABLE !!!
```

Pulling in tests

Add tests for Nullish Coalesce Expression #2402

[Edit](#)**Merged**rwaldrone merged 4 commits into [tc39:master](#) from [leobalter:nullish-coalesce](#) 28 days ago[Conversation 1](#)[Commits 4](#)[Checks 0](#)[Files changed 26](#)[+1,429 -0](#)

leobalter commented 28 days ago

Member + ...

FYI, the only error using V8 with the matching flag is due to the lack of support for TCO in a test file that requires it.

```
test262-harness -t 32 --hostType=d8 --hostPath=v8 --hostArgs='--harmony-nullish' $(git d  
FAIL test/language/expressions/coalesce/tco-pos-null.js (strict mode)  
    Expected no error, got RangeError: Maximum call stack size exceeded
```

```
FAIL test/language/expressions/coalesce/tco-pos-undefined.js (strict mode)  
    Expected no error, got RangeError: Maximum call stack size exceeded
```

```
Ran 44 tests  
42 passed  
2 failed
```



1



1

Reviewers

rwaldrone

**Assignees**

No one—assign yourself

**Labels**

None yet

**Projects**

None yet

**Milestone**

No milestone



1.3 Runtime Semantics: Evaluation

CoalesceExpression : *CoalesceExpressionHead* **??** *BitwiseORExpression*

1. Let *lref* be the result of evaluating *CoalesceExpressionHead*.
2. Let *lval* be ? *GetValue(lref)*.
3. If *lval* is **undefined** or **null**,
 - a. Let *rref* be the result of evaluating *BitwiseORExpression*.
 - b. Return ? *GetValue(rref)*.
4. Otherwise, return *lval*.

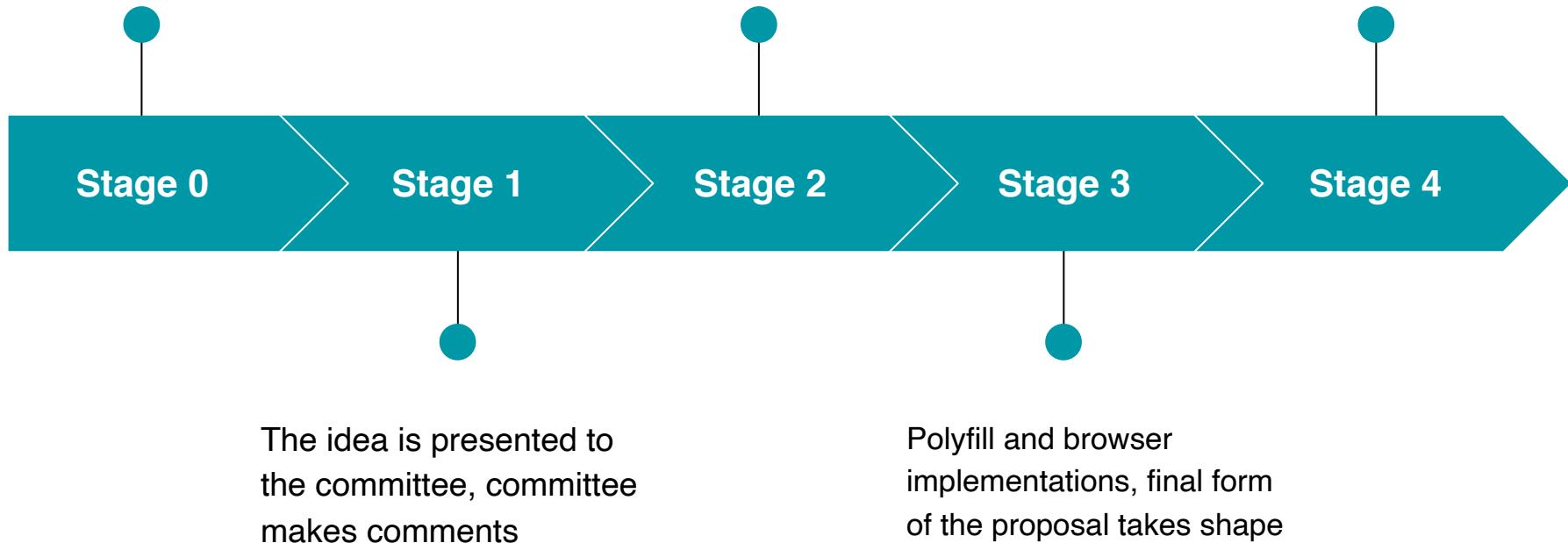
Semantics Covered.

Review

Someone has an idea
and they write it up

Committee discusses if
this feature “should be
in the language”

Proposal is included in
the specification



Syntax

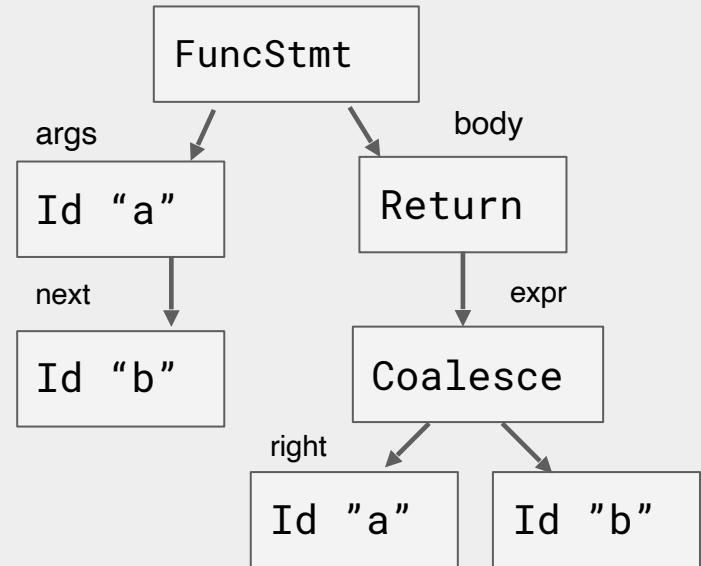
Semantics

Given a Minimal program

....

```
function coalesce(a, b) {  
    return a ?? b;  
}
```

Building the AST

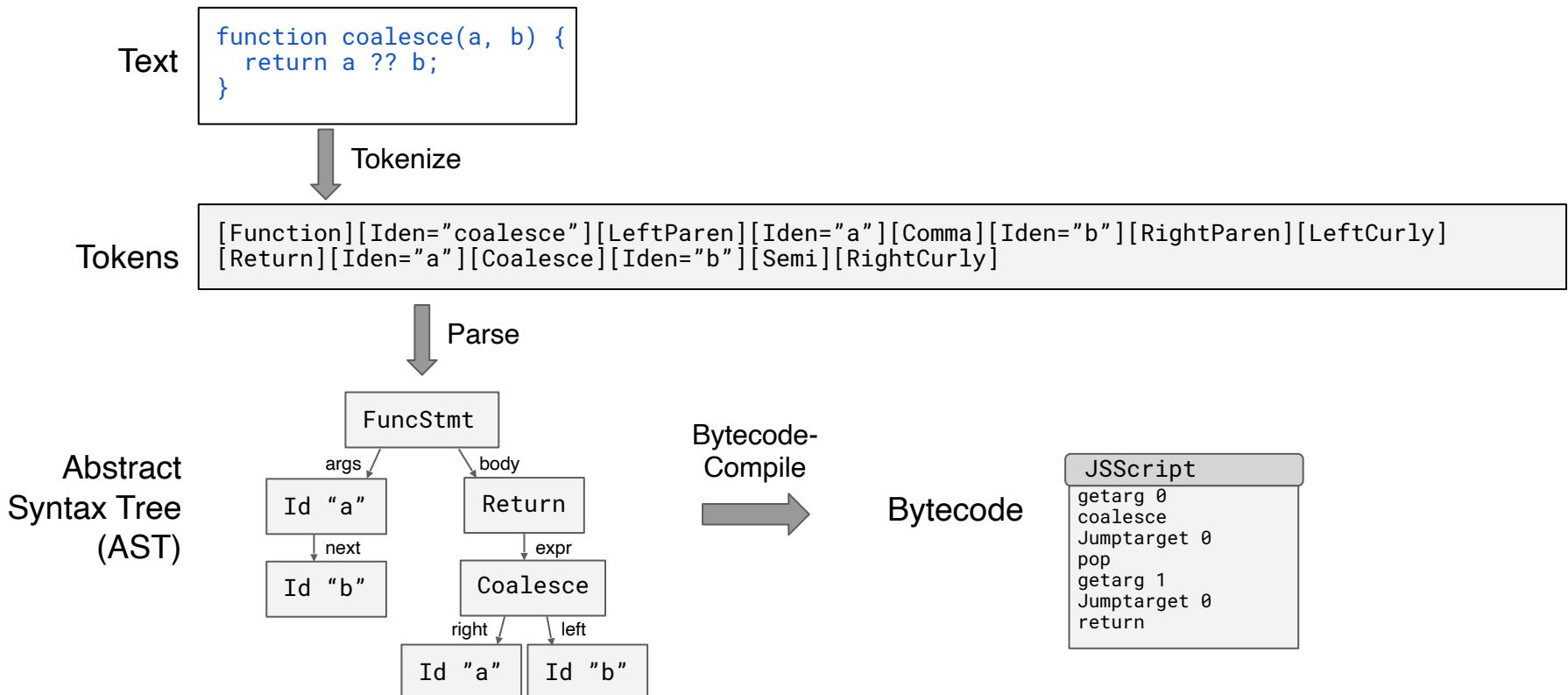


Emit the Bytecode

JSScript

```
getarg 0
coalesce
jumptarget 0
pop
getarg 1
jumptarget 0
return
```

Full Parse



Are we done?

Stage 4

Indicate that the addition is ready for inclusion in the formal ECMAScript standard

Requirements

- [Test262](#) acceptance tests have been written for mainline usage scenarios, and merged
- Two compatible implementations which pass the acceptance tests
- Significant in-the-field experience with shipping implementations, such as that provided by two independent VMs
- A pull request has been sent to [tc39/ecma262](#) with the integrated spec text
- All ECMAScript editors have signed off on the pull request

December 2019?

(this is an Aguaje)

Thanks!



Still here?

Time to do it again!