RMR-Efficient Randomized Abortable Mutual Exclusion*

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August 9, 2012

Abstract

Recent research on mutual exclusion for shared-memory systems has focused on *local spin* algorithms. Performance is measured using the *remote memory references* (RMRs) metric. As common in recent literature, we consider a standard asynchronous shared memory model with N processes, which allows atomic read, write and compare-and-swap (short: CAS) operations.

In such a model, the asymptotically tight upper and lower bounds on the number of RMRs per passage through the Critical Section is $\Theta(\log N)$ for the optimal deterministic algorithms [27, 7]. Recently, several *randomized* algorithms have been devised that break the $\Omega(\log N)$ barrier and need only $o(\log N)$ RMRs per passage in expectation [16, 17, 8]. In this paper we present the first randomized *abortable* mutual exclusion algorithm that achieves a sub-logarithmic expected RMR complexity. More precisely, against a weak adversary (which can make scheduling decisions based on the entire past history, but not the latest coin-flips of each process) every process needs an expected number of $O(\log N/\log\log N)$ RMRs to enter end exit the critical section. If a process receives an abort-signal, it can abort an attempt to enter the critical section within a finite number of its own steps and by incurring $O(\log N/\log\log N)$ RMRs.

1 Introduction

Mutual exclusion, introduced by Dijkstra [11], is a fundamental and well studied problem. A mutual exclusion object (or lock) allows processes to synchronize access to a shared resource. Each process obtains a lock through a *capture protocol* but at any time, at most one process can own the lock. A process is said to own a lock if it participates in a "capture" protocol designed for the object, and completes it. The owner of the lock can access the shared resource, while all other processes wait in their capture protocol for the owner to "release" the lock. The owner of a lock can execute a release protocol which frees up the lock. The capture protocol and release protocol are often denoted *entry* and *exit section*, and a process that owns the lock is in the *critical section*.

In this paper, we consider the standard *cache-coherent* (CC) shared model with N processes that supports atomic read, write, and compare-and-swap (short: `CAS`) operations. In this model, all shared registers are stored in globally accessible shared memory. In addition, each process has a local cache and a cache protocol ensures coherency. A *Remote Memory Reference* (short: RMR) is a shared memory access of a register that cannot be resolved locally (i.e., a cache miss). Mutual exclusion algorithms require processes to busy-wait, so the traditional step complexity measure, which counts the number of shared memory accesses, is not useful.

*This research was supported by a Discovery Grant from the Natural Sciences and Engineering Research Council of Canada (NSERC)

†Supported by Alberta Innovates Technology Futures (AITF)

Early mutual exclusion locks were designed for uniprocessor systems that supported multitasking and time-sharing. A comprehensive survey of these locking algorithms is presented in [25]. One of the biggest shortcomings of these early locking algorithms is that they did not take into account an important hardware technology trend – the steadily growing gap between high processor speeds and the low speed/bandwidth of the processor-memory interconnect [9]. A memory access that traverses the processor-to-memory interconnect, called a *remote memory reference*, takes much more time than a local memory access.

Recent research [5, 24, 2, 23, 3, 7, 10, 21, 22] on mutual exclusion algorithms therefore focusses on minimizing the number of remote memory references (RMR). The maximum number of RMRs that any process requires (in any execution) to capture and release a lock is called the RMR complexity of the mutual exclusion algorithm. RMR complexity is the metric used to analyze the efficiency of mutual exclusion algorithms, as opposed to the traditional metric of counting steps taken by a process (step complexity). Step complexity is problematic, since for mutual exclusion algorithms, a process may perform an unbounded number of memory accesses (each considered a step) while busy-waiting for another process to release the lock [1].

Algorithms that perform all busy-waiting by repeatedly reading *locally accessible* shared variables, achieve bounded RMR complexity and have practical performance benefits [5]. Such algorithms are termed *local spin* algorithms. A comprehensive survey of these algorithms is presented in [4]. Yang and Anderson presented the first $\mathcal{O}(\log N)$ RMRs mutual exclusion algorithm [27] using only reads and writes. Anderson and Kim [2] conjectured that this was optimal, and the conjecture was proved by Attiya, Hendler, and Woelfel [7].

Local spin mutual exclusion locks do not meet a critical demand of many systems [26]. Specifically, the locks employed in database systems and in real time systems must support a "timeout" capability which allows a process that waits "too long" to abort its attempt to acquire the lock. The ability of a thread to abort its lock attempt is crucial in data base systems; for instance in Oracle's Parallel Server and IBM's DB2, this ability serves the dual purpose of recovering from transaction deadlock and tolerating preemption of the thread that holds the lock [26]. In real time systems, the abort capability can be used to avoid overshooting a deadline. Locks that allow a process to abort its attempt to acquire the lock are called abortable locks. Jayanti presented an efficient deterministic abortable lock [21] with worst-case $\mathcal{O}(\log N)$ RMR complexity, which is optimal for deterministic algorithms.

In this paper we present the first randomized abortable mutual exclusion algorithm that achieves a sub-logarithmic RMR complexity. Due to the inherent asynchrony in the system, the RMRs incurred by a process during a lock capture and release depend on how the steps of all the processes in the system were scheduled one after the other. Therefore, the maximum RMRs incurred by any process during any lock attempt are determined by the "worst" schedule that makes some process incur a large number of RMRs. To analyze the RMR complexity of lock algorithms, an adversarial scheduler called the *adversary* is defined. The lower bound of $\Omega(\log N)$ in [7] for mutual exclusion algorithms that use only reads and writes holds for deterministic algorithms where the adversary knows all processes' future steps. The lower bound does not hold for randomized algorithms where processes flip coins to determine their next steps. Randomized algorithms limit the power of an adversary since the adversary cannot know the result of future coin flips. Adversaries of varying powers have been defined. The most common ones are the *oblivious*, the *weak*, and the *adaptive* adversary [6]. An *oblivious* adversary makes all scheduling decisions in advance, before any process flips a coin. This model corresponds to a system, where the coin flips made by processes have no influence on the scheduling. A more realistic model is the *weak* adversary, who sees the coin flip of a process not before that process has taken a step following that coin flip. The *adaptive* adversary models the strongest adversary with reasonable powers, and it can see every coin flip as it appears, and can use that knowledge for any future scheduling decisions. Hendler and Woelfel [16] and later Giakkoupis and Woelfel [12] established a tight bound of $\Theta(\log N/\log\log N)$ expected RMR complexity for randomized mutual exclusion against the adaptive adversary. Recently Bender and Gilbert [8] presented a randomized lock that has amortized $\mathcal{O}(\log^2 \log N)$ expected RMR complexity against the oblivious adversary. Unfortunately, this algorithm is not strictly deadlock-free (processes may deadlock with small probability, so deadlock has to be expected in a long execution). Our randomized abortable mutual exclusion algorithm is deadlock-free, works against the weak adversary and achieves the same epected RMR complexity as the algorithm by Hendler and Woelfel, namely $\mathcal{O}(\log N/\log\log N)$ expected RMR complexity against the weak adversary.

The randomized algorithm we present uses `CAS` objects and read-write registers. Golab, Hadzilacos, Hendler, and Woelfel [14] (see also [13]) presented an $\mathcal{O}(1)$-RMRs implementation of a `CAS` object using only read-write registers. Moreover, they proved that one can simulate any deterministic shared memory algorithm that uses reads, writes, and conditional operations (such as `CAS` operations), with a deterministic algorithm that uses only reads and writes, with only a constant increase in the RMR complexity. Recently in [15], Golab, Higham and Woelfel demonstrated that using linearizable implemented objects in place of atomic objects in randomized algorithms allows the adversary to change the probability distribution of results. Therefore, in order to safely use implemented objects in place of atomic ones in randomized algorithms, it is not enough to simply show that the implemented objects are linearizable. Also in [15], it is proved that there exists no general correctness condition for the weak adversary, and that the weak adversary can gain additional power depending on the linearizable implementation of the object. Therefore, in this paper we assume that `CAS` operations are atomic.

Abortable Mutual Exclusion. We formalize the notion of an abortable lock by specifying two methods, `lock()` and `release()`, that processes can use to capture and release the lock, respectively. The model assumes that a process may receive a signal to abort at any time during its `lock()` call. If that happens, and only then, the process may fail to capture the lock, in which case method `lock()` returns value $\perp$. Otherwise the process captures the lock, and method `lock()` returns a non-$\perp$ value, and the `lock()` call is deemed *successful*. Note that a `lock()` call may succeed even if the process receives a signal to abort during a `lock()` call.

Code executed by a process after a successful `lock()` method call and before a subsequent `release()` invocation is defined to be its Critical Section. If a process executes a successful `lock()` call, then the process's *passage* is defined to be the `lock()` call, and the subsequent Critical Section and `release()` call, in that order. If a process executes an unsuccessful `lock()` call, then it does not execute the Critical Section or a `release()` call, and the process's passage is just the `lock()` call. Code executed by a process outside of any passage is defined to be its Remainder Section.

The *abort-way* is defined to be the steps taken by a process during a passage that begins when the process receives a signal to abort and ends when the process returns to its Remainder Section. Since it makes little sense to have an abort capability where processes have to wait for other processes, the abort-way is required to be bounded wait-free (i.e., processes execute the abort-way in a bounded number of their own steps). This property is known as *bounded abort*. Other properties are defined as follows. *Mutual Exclusion*: At any time there is at most one process in the Critical Section; *Deadlock Freedom*: If all processes in the system take enough steps, then at least one of them will return from its `lock()` call; *Starvation Freedom*: If all processes in the system take enough steps, then every process will return from its `lock()` call. The abortable mutual exclusion problem is to implement an object that provides methods `lock()` and `release()` such that it that satisfies mutual exclusion, deadlock freedom, and bounded abort.

1.1 Model

Our model of computation, the asynchronous shared-memory model [20] with N processes which communicate by executing *operations* on *shared objects*. Every process executes its program by taking *steps*, and does not fail. A step is defined to be the execution of all local computations followed by an operation on a shared object. We consider a system that supports atomic read-write registers and `CAS()` objects.

A read-write register R stores a value from some set and supports two atomic operations R.`Read()` and R.`Write()`. Operation R.`Read()` returns the value of the register and leaves its content unchanged, and operation R.`Write(`v`)` writes the value v into the register and returns nothing. A `CAS` object O stores a value from some set and supports two atomic operations O.`CAS()` and O.`Read()`. Operation O.`Read()` returns the value stored in O. Operation O.`CAS(`exp, new`)` takes two arguments *exp* and *new* and attempts to change the value of O from *exp* to *new*. If the value of O equals *exp* then the operation O.`CAS(`exp, new`)` *succeeds*, and the value of O is changed from *exp* to *new*, and **true** is returned. Otherwise, the operation fails, and the value of O remains unchanged and **false** is returned.

In addition, a process can execute local coin flip operations that returns an integer value distributed uniformly at random from an arbitrary finite set of integers. The scheduling, generated by the *adversary*, can depend on the random values generated by the processes. We assume the weak adversary model (see for example [6]) that decides at each point in time the process that takes the next step. In order to make this decision, it can take all preceding events into account, except the results of the most recent coin flips by processes that are yet to execute a shared memory operation after the coin flip.

As mentioned earlier, we consider the *cache-coherent* (CC) model where each processor has a private cache in which it maintains local copies of shared objects that it accesses. The private cache is logically situated "closer" to the processor than the shared memory, and therefore it can be accessed for free. The shared memory is an external memory accessible to all processors, and is considered remote to all processors. We assume that a hardware protocol ensures cache consistency (i.e., that all copies of the same object in different caches are valid and consistent). A memory access to a shared object that requires access to remote memory is called a *remote memory reference* (RMR). The *RMR complexity* of a algorithm is the maximum number of RMRs that a process can incur during any execution of the algorithm.

1.2 Results

We present several building blocks for our algorithm in Section 2. In Sections 3 and 4 we give an overview of the randomized mutual exclusion algorithm. Our results are summarized by the following theorem.

Theorem 1.1. *There exists a starvation-free randomized abortable N process lock against the weak adversary, where a process incurs $\mathcal{O}(\log N/\log\log N)$ RMRs in expectation per passage. The lock requires $\mathcal{O}(N)$* `CAS` *objects and read-write registers*

2 Building Blocks

A Randomized CAS Counter. A *CAS counter* object with parameter $k \in \mathbb{Z}^+$ complements a `CAS` object by supporting an additional `inc()` operation (apart from `CAS()` and `Read()` operations) that increments the object's value. The object takes values in $\{0,\ldots,k\}$, and initially the object's value is 0. Operation `inc()` takes no arguments, and if the value of the object is in $\{0,\ldots,k-1\}$, then the operation increments the value and returns the previous value. Otherwise, the value of the object is unchanged and the integer k is returned. We will use such an object for $k = 2$ to assign three distinct roles to processes.

Our implementation of the `inc()` operation needs only $O(1)$ RMRs in expectation. A deterministic implementation of a `CAS` counter for $k = 2$ and constant worst-case RMR complexity does not exist: Replacing our randomized `CAS` counter with a deterministic one that has worst-case RMR complexity T yields a deterministic abortable mutual exclusion algorithm with worst-case RMR complexity $\mathcal{O}(T \cdot \log N / \log\log N)$. From the lower bound for deterministic mutual exclusion by Attiya etal. [7], such an algorithm does not exist, unless $T = \Omega(\log\log N)$.[1]

In Appendix A, we describe a randomized CAS counter, called $\mathsf{RCAScounter}_k$, where the `inc()` method is allowed to fail. The idea is, that to increase the value of the object, a process randomly guesses its current value, v, and then executes a `CAS`$(v,v+1)$ operation. An adaptive adversary could intervene between the steps involving the random guess and the subsequent `CAS` operation, thereby affecting the failure probability of an `inc()` method call, but a weak adversary cannot do so.

Lemma 2.1. *Object* $\mathsf{RCAScounter}_k$ *is a randomized wait-free linearizable CAS Counter, where the probability that an* `inc()` *method call fails is* $\frac{k}{k+1}$ *against the weak adversary. Each of the methods of* $\mathsf{RCAScounter}_k$ *has* $\mathcal{O}(1)$ *step complexity.*

A Single-Fast-Multi-Slow Universal Construction. A *universal construction* object provides a linearizable concurrent implementation of any object with a sequential specification that can be given by deterministic code. In Appendix B we devise a universal construction object $\mathsf{SFMSUnivConst}\langle\mathsf{T}\rangle$ for N processes [2] which provides two methods, `doFast`(op) and `doSlow`(op), to perform any operation op on an object of type T. The idea is that `doFast()` methods cannot be called concurrently, but are executed very fast, i.e., they have $\mathcal{O}(1)$ step complexity. On the other hand, `doSlow()` methods need $\mathcal{O}(N)$ steps. The algorithm is based on a helping mechanism in which `doSlow()` methods help a process that wants to execute a `doFast()` method.

Lemma 2.2. *Object* $\mathsf{SFMSUnivConst}\langle\mathsf{T}\rangle$ *is a wait-free universal construction that implements an object* $\mathbb{O}$ *of type* T*, for* N *processes, and an operation op on object* $\mathbb{O}$ *is performed by executing either method* `doFast(op)` *or* `doSlow(op)`*, and no two processes execute method* `doFast()` *concurrently. Methods* `doFast()` *and* `doSlow()` *have* $\mathcal{O}(1)$ *and* $\mathcal{O}(N)$ *step complexity respectively.*

The Abortable Promotion Array. An object O of type $\mathsf{AbortableProArray}_k$ stores a vector of k integer pairs. It provides some specialized operations on the vector, such as conditionally adding/removing elements, and earmarking a process (associated with an element of the vector) for some future activity. Initially the value of $O = (O[0], O[1], \ldots, O[k-1])$ is $(\langle 0, \perp\rangle, \ldots, \langle 0, \perp\rangle)$. The object supports operations `collect()`, `abort()`, `promote()`, `remove()` and `reset()` (see Figure 5 in the appendix). Operation `collect`(X) takes as argument an array $X[0 \ldots k-1]$ of integers, and is used to "register" processes into the array. The operation changes $O[i]$, for all i in $\{0, \ldots, k-1\}$, to value $\langle \mathsf{REG}, X[i]\rangle$ except if $O[i]$ is $\langle \mathsf{ABORT}, s\rangle$, for some $s \in \mathbb{Z}$. In the latter case the value of $O[i]$ is unchanged. Process i is said to be *registered* in the array if a `collect()` operation changes $O[i]$ to value $\langle \mathsf{REG}, s\rangle$, for some $s \in \mathbb{Z}$. The object also allows processes to "abort" themselves from

[1]For the DSM model, this also follows from a result by Golab, Hadzilacos, Hendler, and Woelfel [14]. They established a super-constant lower bound on the RMR complexity of a deterministic bounded counter that can count up to two, and also supports a reset operation.

[2]We use the universal construction object for smaller sets of processes, specifically for sets of size $\mathcal{O}(\log N / \log\log N)$.

the array using the operation `abort()`. Operation `abort`(i, s) takes as argument the integers i and s, where $i \in \{0, \ldots, k-1\}$ and $s \in \mathbb{Z}$. The operation changes $O[i]$ to value $\langle \mathsf{ABORT}, s \rangle$ and returns **true**, only if $O[i]$ is not equal to $\langle \mathsf{PRO}, s' \rangle$, for some $s' \in \mathbb{Z}$. Otherwise the operation returns **false**. Process i *aborts* from the array if it executes an `abort`(i, s) operation that returns **true**. A registered process in the array that has not aborted can be "promoted" using the `promote()` operation. Operation `promote()` takes no arguments, and changes the value of the element in O with the smallest index and that has value $\langle \mathsf{REG}, s \rangle$, for some $s \in \mathbb{Z}$, to value $\langle \mathsf{PRO}, s \rangle$, and returns $\langle i, s \rangle$, where i is the index of that element. If there exists no element in O with value $\langle \mathsf{REG}, s \rangle$, for some $s \in \mathbb{Z}$, then O is unchanged and the value $\langle \perp, \perp \rangle$ is returned. Process i is *promoted* if a `promote()` operation returns $\langle i, s \rangle$, for some $s \in \mathbb{Z}$. Operation `reset()` resets the entire array to its initial state.

Note that an aborted process in the array, cannot be registered into the array or be promoted, until the array is reset. If a process tries to abort itself from the array but finds that it has already been promoted, then the abort fails. This ensures that a promoted process takes responsibility for some activity that other processes expect of it.

In the context of our abortable lock, the i-th element of the array stores the current state of process with ID i, and a sequence number associated with the state. Operation `collect()` is used to register a set of participating processes into the array. Operation `abort`(i, s) is executed only by process i, to abort from the array. Operation `promote()` is used to promote an unaborted registered process from the array, so that the promoted process can fulfill some future obligation.

In our abortable lock of Section 3, we need a wait-free linearizable implementation of type $\mathsf{AbortableProArray}_\Delta$, where Δ is the maximum number of processes that can access the object concurrently, and we achieve this by using object $\mathsf{SFMSUnivConst}\langle \mathsf{AbortableProArray}_\Delta \rangle$. We ensure that no two processes execute operations `collect()`, `promote()`, `reset()` or `remove()` concurrently, and therefore by we get $\mathcal{O}(1)$ step complexity for these operations by using method `doFast()`. Operation `abort()` has $\mathcal{O}(\Delta)$ step complexity since it is performed using method `doSlow()`, which allows processes to call `abort()` concurrently.

3 The Tree Based Abortable Lock

Our abortable lock algorithm is based on an arbitration tree with branching factor approximately $\Theta(\log N / \log \log N)$. For convenience we assume (w.l.o.g.) that $N = \Delta^{\Delta - 1}$ for some positive integer Δ, where N is the maximum number of processes in the system. Then it follows that $\Delta = \Theta(\log N / \log \log N)$.

As in the algorithm by Hendler and Woelfel [16], we consider a tree with N leafs and where each non-leaf node has Δ children. Every non-leaf node is associated with a lock. Each process is assigned a unique leaf in the tree and climbs up the tree by capturing the locks on nodes on its path until it has captured the lock at the root. Once a process locks the root, it can enter the Critical Section.

The main difficulty is that of designing the locks associated with the nodes of the tree. A simple `CAS` object together with an "announce array" as used in [16] does not work. Suppose a process p captures locks of several nodes on its path up to the root and aborts before capturing the root lock. Then it must release all captured node locks and therefore these lock releases cause other processes, which are busy-waiting on these nodes, to incur RMRs. So we need a mechanism to guarantee some progress to these processes, while we also need a mechanism that allows busy-waiting processes to abort their attempts to capture node locks. In [16] progress is achieved as follows: A process p, before releasing a lock on its path, searches(with a random procedure) for other processes that are busy-waiting for the node lock to become free. If p finds such a process, it promotes it into the critical section. This is possible, because at the time of the promotion p owns the root lock and can hand it over to a promoted process. Unfortunately, this promotion mechanism fails for abortable mutual exclusion: When p aborts its own attempt to enter the Critical Section, it may have to release node locks at a time when it doesn't own the root lock. Another problem is that if p finds a process q that is waiting for p to release a node-lock, then q may have already decided to abort. We use a carefully designed synchronization mechanism to deal with such cases.

To ensure that waiting processes make some progress, we desire that p "collect" busy-waiting processes (if any) at a node into an instance of an object of type $\mathsf{AbortableProArray}_{\Delta}$, $\mathsf{PawnSet}$, using the operation `collect()`. Once busy-waiting processes are collected into $\mathsf{PawnSet}$, p can identify a busy-waiting process, if present, using the $\mathsf{PawnSet}$.`promote()` operation, while busy-waiting processes themselves can abort using the $\mathsf{PawnSet}$.`abort()` operation. Note that p may have to read $\mathcal{O}(\Delta)$ registers just to find a single busy-waiting process at a node, where Δ is the branching factor of the arbitration tree. This is problematic since our goal is to bound the number of steps during a passage to $\mathcal{O}(\Delta)$ steps, and thus a process cannot collect at more than one node. For this reason we desire that p transfer all unreleased node locks that it owns to the first busy-waiting process it can find, and then it would be done. And if there are no busy-waiting processes at a node, then p should somehow be able to release the node lock in $\mathcal{O}(1)$ steps. Since there are at most Δ nodes on a path to the root node, p can continue to release captured node locks where there are no busy-waiting processes, and thus not incur more than $\mathcal{O}(\Delta)$ overall. We use an instance of $\mathsf{RCAScounter}_2$, Ctr, to help decide if there are any busy-waiting processes at a node lock. Initially, Ctr is 0, and processes attempt to increase Ctr using the Ctr.`inc()` operation after having registered at the node. Process p attempts to release a node lock by first executing a Ctr.`CAS`$(1, 0)$ operation. If the operation fails then some process q must have further increased Ctr from 1 to 2, and thus p can transfer all unreleased locks to q, if q has not aborted itself. If q has aborted, then q can perform the collect at the node lock for p, since q can afford to incur an additional one-time expense of $\mathcal{O}(\Delta)$ RMRs. If q has not aborted then p can transfer its captured locks to q in $\mathcal{O}(1)$ steps, and thus making sure some process makes progress towards capturing the root lock. We encapsulate these mechanisms in a randomized abortable lock object, $\mathsf{ALockArray}_{\Delta}$.

More generally, we specify an object $\mathsf{ALockArray}_n$ for an arbitrary parameter $n < N$. Object $\mathsf{ALockArray}_n$ provides methods `lock()` and `release()` that can be accessed by at most $n + 1$ processes concurrently. The object is an abortable lock, but with an RMR complexity of $O(n)$ for the abort-way, and constant RMR complexity for `lock()`. The `release()` method is special. If it detects contention (i.e., other processes are busy-waiting), then it takes $O(n)$ RMRs, but helps those other processes to make progress. Otherwise, it takes only $O(1)$ RMRs. Each non-leaf node u in our abritration tree will be associated with a lock $\mathsf{ALockArray}_{\Delta}$ and can only be accessed concurrently by the processes owning locks associated with the children of u and one other process.

Method `lock()` takes a single argument, which we will call pseudo-ID, with value in $\{0, \ldots, n-1\}$. We denote a `lock()` method call with argument i as $\texttt{lock}_i\texttt{()}$, but refer to $\texttt{lock}_i\texttt{()}$ as `lock()` whenever the context of the discussion is not concerned with the value of i. Method `lock()` returns a non-$\perp$ value if a process captures the lock, otherwise it returns a $\perp$ value to indicate a failed `lock()` call. A `lock()` by process p can fail only if p aborts during the method call. Method `release()` takes two arguments, a pseudo-ID $i \in \{0, \ldots, n-1\}$ and an integer j. Method $\texttt{release}_i(j)$ returns **true** if and only if there exists a concurrent call to `lock()` that eventually returns j. Otherwise method $\texttt{release}_i(j)$ returns **false**. The information contained in argument j determines the transfered node locks. Process pseudo-IDs are passed as arguments to the methods to allow the ability for a process to "transfer" the responsibility of releasing the lock to another process. Specifically, we desire that if a process p executes a successful $\texttt{lock}_i\texttt{()}$ call and becomes the owner of the lock, then p does not have to release the lock itself, if it can find some process q to call $\texttt{release}_i\texttt{()}$ on its behalf. In Section 4 we implement object $\mathsf{ALockArray}_n$, and prove its properties in Appendix D.2, and thus we get the following lemma.

Lemma 3.1. *Object $\mathsf{ALockArray}_n$ can be implemented against the weak adversary for the CC model with the following properties using only $\mathcal{O}(n)$ `CAS` objects and read-write registers.*

(a) Mutual exclusion, starvation freedom, bounded exit, and bounded abort.

(b) The abort-way has $\mathcal{O}(n)$ RMR complexity.

(c) If a process does not abort during a `lock()` call, then it incurs $\mathcal{O}(1)$ RMRs in expectation during the call, otherwise it incurs $\mathcal{O}(n)$ RMRs in expectation during the call.

(d) If a process' call to $\texttt{release(}j\texttt{)}$ returns **false**, *then it incurs $\mathcal{O}(1)$ RMRs during the call, otherwise it incurs $\mathcal{O}(n)$ RMRs during the call.*

High Level Description of the Abortable Lock. We use a complete Δ-ary tree $\mathcal{T}$ of height Δ with N leaves, called the arbitration tree. The root has height Δ and the leaves of the tree have height 0. The N processes in the system line up as N unique leaf nodes, such that each process p is associated with a unique leaf leaf_p in the tree. Let path_p denote the path from leaf_p up to root, and h_u denote the height of node u.

Each node of our arbitration tree $\mathcal{T}$ is a structure of type Node that contains a single instance L of the abortable randomized lock object $\mathsf{ALockArray}_\Delta$. This allows processes the ability to abort their attempt at any point in time during their ascent to the root node.

Lock capture protocol - $\texttt{lock}_p\texttt{()}$. During $\texttt{lock}_p\texttt{()}$ a process p attempts to *capture* every node on its path path_p that it does not own, as long as p has not received a signal to abort. Process p attempts to capture a node u by executing a call to $u.\mathsf{L}.\texttt{lock()}$. If p's $u.\mathsf{L}.\texttt{lock()}$ call returns ∞ then p is said to have captured u, and if the call returns an integer j, then p is said to have been *handed over* all nodes from u to v on path_p, where $\mathsf{h}_v = j$. We ensure that $j \geq \mathsf{h}_u$. Process p starts to *own* node u when p captures $u.\mathsf{L}$ or when p is handed over node u from the previous owner of node u. Process p can enter its Critical Section when it owns the root node of $\mathcal{T}$. Process p may receive a signal to abort during a call to $u.\mathsf{L}.\texttt{lock()}$ as a result of which p's call to $u.\mathsf{L}.\texttt{lock()}$ returns either $\perp$ or a non-$\perp$ value. In either case, p then calls $\texttt{release}_p\texttt{()}$ to release all locks of nodes that p has captured in its passage, and then returns from its $\texttt{lock}_p\texttt{()}$ call with value $\perp$.

Lock release protocol - $\texttt{release}_p\texttt{()}$. An exiting process p *releases* all nodes that it owns during $\texttt{release}_p\texttt{()}$. Process p is said to *release* node u if p releases $u.\mathsf{L}$ (by executing $u.\mathsf{L}.\texttt{release()}$ call), or if p hands over node u to some other process. Recall that p hands over node u if p executes a $v.\mathsf{L}.\texttt{release(}j\texttt{)}$ call that returns **true** where $\mathsf{h}_v \leq \mathsf{h}_u \leq j$. Let s be the height of the highest node p owns. During $\texttt{release}_p\texttt{()}$, p climbs up $\mathcal{T}$ and calls $u.\mathsf{L}.\texttt{release}_p\texttt{(}s\texttt{)}$ at every node u that it owns, until a call returns **true**. If a $u.\mathsf{L}.\texttt{release}_p\texttt{(}s\texttt{)}$ call returns **false** (process p incurs $\mathcal{O}(1)$ steps), then p is said to have released lock $u.\mathsf{L}$ (and therefore released node u), and thus p continues on its path. If a $u.\mathsf{L}.\texttt{release}_p\texttt{(}s\texttt{)}$ call returns **true** (process p incurs $\mathcal{O}(\Delta)$ steps), then p has handed over all remaining nodes that it owns to some process that is executing a concurrent $u.\mathsf{L}.\texttt{lock()}$ call at node u, and thus p does not release any more nodes.

Notice that our strategy to release node locks is to climb up the tree until all node locks are released or a hand over of remaining locks is made. Climbing up the tree is necessary (as opposed to climbing down) in order to hand over node locks to a process, say q, such that the handed over nodes lie on path_q.

4 The Array Based Abortable Lock

We specified object $\mathsf{ALockArray}_n$ in Section 3 and now we describe and implement it (see Figures 1 and 2). Let L be an instance of object $\mathsf{ALockArray}_n$.

Registering and Roles at lock L. At the beginning of a `lock()` call processes *register* themselves in the apply array by swapping the value REG atomically into their designated slots (apply[i] for process with pseudo-ID i) using a `CAS` operation. The array apply of n `CAS` objects is used by processes to register and "deregister" themselves from lock L, and to notify each other of certain events at lock L.

On registering in the apply array, processes attempt to increase Ctr, an instance of $\mathsf{RCAScounter}_2$, using operation Ctr.`inc()`. Recall that $\mathsf{RCAScounter}_2$ is a bounded counter, initially 0, and returns values in $\{0, 1, 2\}$ (see Section 2). Each of these values corresponds to a role at lock L. There are four *roles* that a process can assume during its passage of lock L, namely *king*, *queen*, *pawn* and *promoted pawn*, and a role defines the protocol a process follows during a passage. During an execution, Ctr cycles from its initial value 0 to non-0 values and then back to 0, multiple times, and we refer to each such cycle as a Ctr-cycle. The process that increases Ctr from 0 to 1 becomes the king. The process that increases Ctr from 1 to 2 becomes the queen. All processes that attempt to increase Ctr any further, are returned value 2 (by specification of object $\mathsf{RCAScounter}_2$), and they assume the role of a pawn process. A pawn process busy-waits until it gets "promoted" at lock L (a process is said to be *promoted* at lock L if it is promoted in PawnSet), or until it sees the Ctr value decrease, so that it can attempt to increase Ctr again. We ensure that a pawn process repeats an attempt to increase Ctr at most once, before getting promoted. We ensure that at any point in time during the execution, the number of processes that have assumed the role of a king, queen and promoted pawn at lock L, respectively, is at most one, and thus we refer to them as king_L, $\mathsf{queen}_\mathsf{L}$ and $\mathsf{ppawn}_\mathsf{L}$, respectively. We describe the protocol associated with each of the roles in more detail shortly. An array Role of n read-write registers is used by processes to record their role at lock L.

Busy-waiting in lock L. The king process, king_L, becomes the first owner of lock L during the current Ctr-cycle, and can proceed to enter its Critical Section, and thus it does not busy-wait during `lock()`. The queen process, $\mathsf{queen}_\mathsf{L}$, must wait for king_L for a notification of its turn to own lock L. Then $\mathsf{queen}_\mathsf{L}$ spins on `CAS` object Sync1, waiting for king_L to `CAS` some integer value into Sync1. Process king_L attempts to `CAS` an integer j into Sync1 only during its call to `release(`j`)`, after it has executed its Critical Section. The pawn processes wait on their individual slots of the apply array for a notification of their promotion.

A *collect* action at lock L. A collect action is conducted by either king_L during a call to `release()`, or by $\mathsf{queen}_\mathsf{L}$ during a call to `abort()`. A collect action is defined as the sequence of steps executed by a process during a call to `doCollect()`. During a call to `doCollect()`, the collecting process (say q) iterates over the array apply reading every slot, and then creates a local array A from the values read and stores the contents of A in the PawnSet object in using the operation PawnSet.`collect(A)`. A key point to note is that operation PawnSet.`collect(A)` does not overwrite an aborted process's value in PawnSet (a process aborts itself in PawnSet by executing a successful PawnSet.`abort()` operation).

A *promote* action at lock L. Operation PawnSet.`promote()` during a call to method `doPromote()` is defined as a promote action. The operation returns the pseudo-ID of a process that was collected during a collect action, and has not yet aborted from PawnSet. A promote action is conducted at lock L either by king_L, $\mathsf{queen}_\mathsf{L}$ or $\mathsf{ppawn}_\mathsf{L}$.

Lock *handover* from king_L to $\mathsf{queen}_\mathsf{L}$. As mentioned, process $\mathsf{queen}_\mathsf{L}$ waits for king_L to finish its Critical Section and then call `release(`j`)`. During king_L's `release(`j`)` call, king_L attempts to swap integer j into `CAS` object Sync1, that only king_L and $\mathsf{queen}_\mathsf{L}$ access. If $\mathsf{queen}_\mathsf{L}$ has not

Object $\mathsf{ALockArray}_n$

shared:

Ctr: $\mathsf{RCAScounter}_2$ **init** 0;
PawnSet: Object of type $\mathsf{AbortableProArray}_n$ **init** $\varnothing$;
apply: **array** $[0 \ldots n-1]$ **of int** pairs **init** all $\langle \bot, \bot \rangle$;
Role: **array** $[0 \ldots n-1]$ **of int init** $\bot$;
Sync1, Sync2: **int init** $\bot$;
KING, QUEEN, PAWN, PAWN_P, REG, PRO: **const int** $0, 1, 2, 3, 4, 5$ respectively;
`getSequenceNo()`: returns integer k on being called for the k-th time from a call to $\mathtt{lock}_i\mathtt{()}$. (Since calls to $\mathtt{lock}_i\mathtt{()}$ are executed sequentially, a sequential shared counter suffices to implement method `getSequenceNo()`.)

local:

$s, val, seq, dummy$: **int init** $\bot$;
$flag, r$: **boolean init false**;
A: **array** $[0 \ldots n-1]$ **of int init** $\bot$

```
// If process i satisfies the loop condition in line 2, 7, or 14, and i
   has received a signal to abort, then i calls abort_i()
```

Method $\mathtt{lock}_i\mathtt{(\ )}$

```
1  s ← getSequenceNo()
2  await (apply[i].CAS(⟨⊥,⊥⟩, ⟨REG, s⟩))
3  flag ← true
4  repeat
5  |  Role[i] ← Ctr.inc()
6  |  if (Role[i] = PAWN) then
7  |  |  await
   |  |  (apply[i] = ⟨PRO, s⟩ ∨ Ctr.Read() ≠ 2)
8  |  |  if (apply[i] = ⟨PRO, s⟩) then
9  |  |  |  Role[i] ← PAWN_P
10 |  |  end
11 |  end
12 until (Role[i] ∈ {KING, QUEEN, PAWN_P})
13 if (Role[i] = QUEEN) then
14 |  await (Sync1 ≠ ⊥)
15 end
16 apply[i].CAS(⟨REG, s⟩, ⟨PRO, s⟩)
17 if Role[i] = QUEEN then return Sync1 else
   return ∞
```

Method $\mathtt{abort}_i\mathtt{(\ )}$

```
18 if ¬flag then return ⊥
19 apply[i].CAS(⟨REG, s⟩, ⟨PRO, s⟩)
20 if Role[i] = PAWN then
21 |  if ¬PawnSet.abort(i, s) then
22 |  |  Role[i] ← PAWN_P
23 |  |  return ∞
24 |  end
25 else
26 |  if ¬Sync1.CAS(⊥, ∞) then
27 |  |  return Sync1
28 |  end
29 |  doCollect_i()
30 |  helpRelease_i()
31 end
32 apply[i].CAS(⟨PRO, s⟩, ⟨⊥, ⊥⟩)
33 return ⊥
```

Method $\mathtt{doCollect}_i\mathtt{()}$

```
51 for k ← 0 to n − 1 do
52 |  ⟨val, seq⟩ ← apply[k]
53 |  if val = REG then A[k] ← seq else A[k] ← ⊥
54 end
55 PawnSet.collect(A)
```

Figure 1: Implementation of Object $\mathsf{ALockArray}_n$

Method `release_i(int j)`

```
r ← false
if Role[i] = KING then
|   if ¬Ctr.CAS(1,0) then
|   |   r ← Sync1.CAS(⊥, j)
|   |   if r then doCollect_i()
|   |   helpRelease_i()
|   end
end
if Role[i] = QUEEN then
|   helpRelease_i()
end
if Role[i] = PAWN_P then
|   doPromote_i()
end
⟨dummy, s⟩ ← apply[i]
apply[i].CAS(⟨PRO, s⟩, ⟨⊥, ⊥⟩)
return r
```

Method `helpRelease_i()`

```
if ¬Sync2.CAS(⊥, i) then
|   j ← Sync1.Read()
|   Sync1.CAS(j, ⊥)
|   j ← Sync2.Read()
|   Sync2.CAS(j, ⊥)
|   PawnSet.remove(j)
|   doPromote_i()
end
```

Method `doPromote_i()`

```
PawnSet.remove(i)
⟨j, seq⟩ ← PawnSet.promote()
if j = ⊥ then
|   PawnSet.reset()
|   Ctr.CAS(2, 0)
else
|   apply[j].CAS(⟨REG, seq⟩, ⟨PRO, seq⟩)
end
```

Figure 2: Implementation of Object $\mathsf{ALockArray}_n$ (continued)

"aborted", then $\mathsf{king_L}$ successfully swaps j into Sync1, and this serves as a notification to $\mathsf{queen_L}$ that $\mathsf{king_L}$ has completed its Critical Section, and that $\mathsf{queen_L}$ may now proceed to enter its Critical Section.

***Aborting* an attempt at lock L by $\mathsf{queen_L}$.** On receiving a signal to abort, $\mathsf{queen_L}$ abandons its `lock()` call and executes a call to `abort()` instead. $\mathsf{queen_L}$ first changes the value of its slot in the apply array from REG to PRO, to prevent itself from getting collected in future collects. Since $\mathsf{king_L}$ and $\mathsf{queen_L}$ are the first two processes at L, $\mathsf{king_L}$ will eventually try to handover L to $\mathsf{queen_L}$. To prevent $\mathsf{king_L}$ from handing over lock L to $\mathsf{queen_L}$, $\mathsf{queen_L}$ attempts to swap a special value ∞ into Sync1 in one atomic step. If $\mathsf{queen_L}$ fails then this implies that $\mathsf{king_L}$ has already handed over L to $\mathsf{queen_L}$, and thus $\mathsf{queen_L}$ returns from its call to `abort()` with the value written to Sync1 by $\mathsf{king_L}$, and becomes the owner of L. If $\mathsf{queen_L}$ succeeds then $\mathsf{queen_L}$ is said to have successfully aborted, and thus $\mathsf{king_L}$ will eventually fail to hand over lock L. Since $\mathsf{queen_L}$ has aborted, $\mathsf{queen_L}$ now takes on the responsibility of collecting all registered processes in lock L, and storing them into the PawnSet object. After performing a collect, $\mathsf{queen_L}$ then synchronizes with $\mathsf{king_L}$ again, to perform a promote, where one of the collected processes is promoted. After that, $\mathsf{queen_L}$ deregisters from the apply array by resetting its slot to the initial value $\langle \perp, \perp \rangle$.

***Aborting* an attempt at lock L by a pawn process.** On receiving a signal to abort a pawn process (say p) busy-waiting in lock L, abandons its `lock()` call and executes a call to `abort()` instead. Process p first changes the value of its slot in the apply array from REG to PRO, to prevent itself from getting collected in future collects. It then attempts to abort itself in PawnSet by executing the operation PawnSet.`abort`(p)). If p's attempt is unsuccessful then it implies that p has already been promoted in PawnSet, and thus p can assume the role of a promoted pawn, and become the owner of L. In this case, p returns from its `abort()` call with value ∞ and becomes the owner of L. If p's attempt is successful then p cannot be collected or promoted in future collects and promotion events. In this case, p deregisters from the apply array by resetting its slot to the initial value $\langle \perp, \perp \rangle$, and returns $\perp$ from its call to `abort()`.

Releasing lock L. Releasing lock L can be thought of as a group effort between the $\mathsf{king_L}$, $\mathsf{queen_L}$ (if present at all), and the promoted pawns (if present at all). To completely release lock L, the owner of L needs to reset Ctr back to 0 for the next Ctr-cycle to begin. However, the owner also has an obligation to hand over lock L to the next process waiting in line for lock L. We now discuss the individual strategies of releasing lock L, by $\mathsf{king_L}$, $\mathsf{queen_L}$ and the promoted processes. To release lock L, the owner of L executes a call to `release(`j`)`, for some integer j.

Synchronizing the release of lock L by $\mathsf{king_L}$ and $\mathsf{queen_L}$. Process $\mathsf{king_L}$ first attempts to decrease Ctr from 1 to 0 using a CAS operation. If it is successful, then $\mathsf{king_L}$ was able to end the Ctr-cycle before any process could increase Ctr from 1 to 2. Thus, there was no $\mathsf{queen_L}$ process or pawn processes waiting for their turn to own lock L, during that Ctr-cycle. Then $\mathsf{king_L}$ is said to have released lock L.

If $\mathsf{king_L}$'s attempt to decrease Ctr from 1 to 0 fails, then $\mathsf{king_L}$ knows that there exists a $\mathsf{queen_L}$ process that increased Ctr from 1 to 2. Since $\mathsf{queen_L}$ is allowed to abort, releasing lock L is not as straight forward as raising a flag to be read by $\mathsf{queen_L}$. Therefore, $\mathsf{king_L}$ attempts to synchronize with $\mathsf{queen_L}$ by swapping the integer j into the object Sync1 using a Sync1.CAS$(\perp, j)$ operation. Recall that $\mathsf{queen_L}$ also attempts to swap a special value ∞ into object Sync1 using a Sync1.CAS$(\perp, j)$ operation, in order to abort its attempt. Clearly only one of them can succeed. If $\mathsf{king_L}$ succeeds, then $\mathsf{king_L}$ is said to have successfully handed over lock L to $\mathsf{queen_L}$. If $\mathsf{king_L}$ fails, then $\mathsf{king_L}$ knows that $\mathsf{queen_L}$ has aborted and thus $\mathsf{king_L}$ then tries to hand over its lock to one of the waiting pawn processes. The procedure to hand over lock L to one of the waiting pawn processes is to execute a collect action followed by a promote action.

The collect action needs to be executed only once during a Ctr-cycle, and thus we let the process (among $\mathsf{king_L}$ or $\mathsf{queen_L}$) that successfully swaps a value into Sync1, execute the collect action.

If $\mathsf{king_L}$ successfully handed over L to $\mathsf{queen_L}$, it collects the waiting pawn processes, so that eventually when $\mathsf{queen_L}$ is ready to release lock L, $\mathsf{queen_L}$ can simply execute a promote action. Since there is no guarantee that $\mathsf{king_L}$ will finish collecting before $\mathsf{queen_L}$ desires to execute a promote action, the processes synchronize among themselves again, to execute the first promote action of the current Ctr-cycle. They both attempt to swap their pseudo-IDs into an empty CAS object Sync2, and therefore only one can succeed. The process that is unsuccessful, is the second among them, and therefore by that point the collection of the waiting pawn process must be complete. Then the process that is unsuccessful, resets Sync1 and Sync2 to their initial value $\perp$, and then executes the promote action, where a waiting pawn process is promoted and handed over lock L. If no process were collected during the Ctr-cycle, or all collected pawn processes have successfully aborted before the promote action, then the promote action fails, and thus the owner process resets the PawnSet object, and then resets Ctr from 2 to 0 in one atomic step, thus releasing lock L, and resetting the Ctr-cycle.

The release of lock L by $\mathsf{ppawn_L}$. If a process was promoted by $\mathsf{king_L}$ or $\mathsf{queen_L}$ as described above, then the promoted process is said to be handed over the ownership of L, and becomes the first promoted pawn of the Ctr-cycle. Since a collect for this Ctr-cycle has already been executed, process $\mathsf{ppawn_L}$ does not execute any more collects, but simply attempts to hand over lock L to the next collected process by executing a promote action. This sort of promotion and handing over of lock L continues until there are no more collected processes to promote, at which point the last promoted pawn resets the PawnSet object, and then resets Ctr from 2 to 0 in one atomic step, thus releasing lock L, and resetting the Ctr-cycle.

All owner processes also *deregister* themselves from lock L, by resetting their slot in the apply array to the initial value $\langle \perp, \perp \rangle$. This step is the last step of their `release(`j`)` calls, and processes return a boolean to indicate whether they successfully wrote integer j into Sync1 during their `release(`j`)` call. Note that only king_L could possibly return **true** since it is the only process that attempts to do so, during its `release(`j`)` call.

5 Conclusion

We presented the first randomized abortable lock that achieves sub-logartihmic expected RMR complexity. While the speed-up is only a modest $O(\log\log n)$ factor over the most efficient deterministic abortable mutual exclusion algorithm, our result shows that randomization can help in principle, to improve the efficiency of abortable locks. Unfortunately, our algorithm is quite complicated; it would be nice to find a simpler one. It would also be interesting to find an algorithm with sub-logarithmic RMR complexity that works against the stronger adversary. In the weak adversary model, no non-trivial lower bounds for mutual exclusion are known, but it seems hard to improve upon $O(\log n/\log\log n)$ RMR complexity, even without the abortability property.

As shown by Bender and Gilbert, [8], the picture looks different in the oblivious adversary model. However, their algorithm is only lock-free with high probability. It would be interesting to find a mutual exclusion algorithm with $o(\log n/\log\log n)$ RMR complexity against the oblivious adversary that is lock-free with probability one. It would also be interesting to know whether such an algorithm can be made abortable.

Acknowledgement.

We are indebted to Lisa Higham and Bill Sands for their careful reading of an earlier version of the paper and their valuable comments. We also thank the anonymous referees of DISC 2012 for their helpful comments.

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Appendix

A Implementation of Object $\mathsf{RCAScounter}_k$

The sequential specification of the CAS Counter object is presented in Figure 3 in the form of type $\mathsf{CAScounter}_k$. The implementation of our randomized CAS counter object, $\mathsf{RCAScounter}_k$ of type $\mathsf{CAScounter}_k$ is presented in Figure 4. A shared `CAS` object Count is used to store the value of the counter object, and is initialized to 0. The object provides methods `inc()`, `CAS()` and `Read()`, where the `inc()` method is allowed to *fail*, in which case the operation does not change the object state, and returns $\perp$ to indicate the failure.

Type $\mathsf{CAScounter}_k$

x: **int init** 0

Operation `inc()`

1 **if** $x = k$ **then return** x
2 $x \leftarrow x + 1$
3 **return** $x - 1$

Operation `CAS`(old, new)

4 **if** $x \neq old \vee new \notin \{0, \ldots, k\}$ **then return false**
5 $x \leftarrow new$
6 **return true**

Operation `Read()`

7 **return** x

Figure 3: Sequential Specification of Type $\mathsf{CAScounter}_k$

Object $\mathsf{RCAScounter}_k$

shared: Count: **int init** 0
local: β: **int init** 0

Method `inc( )`

8 $\beta \leftarrow \mathtt{random}(0, 1, \ldots, k)$
9 **if** $(\beta = k)$ **then**
10   **if** $(\mathsf{Count}.\mathtt{Read()} = k)$ **then return** k
11 **else**
12   **if** $\mathsf{Count}.\mathtt{CAS}(\beta, \beta + 1)$ **then return** β
13 **end**
14 **return** $\perp$

Method `CAS`(old, new)

15 **if** $new \notin \{0, \ldots, k\}$ **then return false**
16 **return** $\mathsf{Count}.\mathtt{CAS}(old, new)$

Method `Read( )`

17 **return** $\mathsf{Count}.\mathtt{Read()}$

Figure 4: Implementation of Object $\mathsf{RCAScounter}_k$

During the `inc()` method, a process p first makes a guess at the counter's current value by rolling a $(k+1)$-sided dice (in line 8) that returns a value in $\{0, \ldots, k\}$ uniformly at random, and stores the value in local variable β. If $\beta = k$, then p performs a `Read()` on Count(in line 10) to verify the correctness of its guess. If p's guess is correct, then it returns k, otherwise it returns $\perp$ (in line 14) to indicate a failed `inc()` method call. If $\beta \in \{0, \ldots, k-1\}$, then p performs a $\mathsf{Count}.\mathtt{CAS}(\beta, \beta + 1)$ operation (in line 10) in order to verify the correctness of its guess and to increment Count in one atomic step. If p's guess is correct, then the CAS operation succeeds and the `inc()` method returns the previous value. Otherwise the `inc()` method returns $\perp$ (in line 14) to indicate a failed `inc()` method call.

Method `Read()` simply reads the current value of Count using a Count.`Read()` operation (line 17) and returns the result of the operation. Method `CAS()` takes two integer parameters old, new, and in line 15 performs a safety check, where it checks whether the value of new is in $\{0, \ldots, k\}$. If the safety check fails, then the method simply returns **false**. Otherwise, it attempts to change the value of Count from old to new using the Count.CAS(old, new) operation (in line 16) and returns the result of the operation.

A.1 Analysis and Properties of Object $\mathsf{RCAScounter}_k$

Consider an instance of the $\mathsf{RCAScounter}_k$ object. Let H be an arbitrary history that consists of all method calls on the instance, except failed `inc()` calls and pending calls that are yet to execute line 10 (Read operation), line 12 (CAS operation), line 16 (CAS operation) or line 17 (Read operation). If a failed `inc()` is in the history, it can be linearized at an arbitrary point between its invocation and response, as it does not affect the validity of any other operations. Therefore, it suffices to prove that the history without failed `inc()` operations is linearizable, and then linearizability of the original history follows. The same argument applies to omitting the selected pending method calls. Since the selected pending method calls do not change any shared object, they cannot affect the validity of any other operations.

We define a point $pt(u)$ for every method u in H. Let $I(u)$ be the interval between u's invocation and response. Let S be the sequential history obtained by ordering the method calls in H according to the points $pt(u)$. To show that $\mathsf{RCAScounter}_k$ is a randomized linearizable implementation of the type $\mathsf{CAScounter}_k$, we need to show that the sequential history S is valid, i.e., S lies in the specification of type $\mathsf{CAScounter}_k$ object, and that $pt(u)$ lies in $I(u)$. Let $\mathcal{C}$ be an object of type $\mathsf{CAScounter}_k$, and let S_v be the sequential history obtained when the operations of S are executed sequentially on object $\mathcal{C}$ in the order as given in S. Clearly, S_v is a valid sequential history in the specification of type $\mathsf{CAScounter}_k$ by construction. Then to show that S is valid, we show that $S = S_v$.

Lemma A.1. *Object* $\mathsf{RCAScounter}_k$ *is a randomized linearizable implementation of type* $\mathsf{CAScounter}_k$*.*

Proof. Let A be an instance of the $\mathsf{RCAScounter}_k$ object. Consider an arbitrary history H that consists of all completed method calls on A, except failed `inc()` calls, and all pending method calls on A that have executed a successful CAS operation. We now define point $pt(u)$ for every method u in H.

If u is a `Read()` method call then define $pt(u)$ to be the point in time when the Read operation in line 17 is executed.

If u is an `inc()` method call that returns from line 10 then $pt(u)$ is the point in time of the Read operation in line 10, and if u's CAS operation in line 12 succeeds then $pt(u)$ is the point in time of the CAS operation in line 12. By construction, a Read or CAS operation has been executed during every `inc()` call in H, and no failed `inc()` calls are in H. Then it follows that we have defined $pt(u)$ for every `inc()` call u in H.

If u is a `CAS()` method call that returns from line 15 then $pt(u)$ is any arbitrary point during $I(u)$, and if u returns from line 16 then $pt(u)$ is the point in time of the CAS operation in line 16.

Clearly $pt(u) \in I(u)$ for every method u in H.

Let u_i be the i-th operation in S and v_i be the i-th operation in S_v. Let $\mathsf{Count}(u_i)^+$ denote the value of object Count immediately after $pt(u_i)$, and let $\mathcal{C}(v_i)^+$ denote the value of object $\mathcal{C}$ after operation v_i in S_v. We assume that u_0 is a method call that does not change the state of any shared object of instance A (such as a `Read()` method) and returns the initial value of the object. This assumption can be made without loss of generality, because the removal of a method call that does not change the state of the object from a linearizable history always leaves a history that is also linearizable. The purpose of the assumption is to simplify the base case of our induction hypothesis.

We now prove by induction on integer i, that $\mathsf{Count}(u_i)^+ = \mathcal{C}(v_i)^+$, and that the return value of u_i matches the value returned by v_i, thereby proving $S = S_v$.

Basis ($i = 0$) Since initially the value of object Count and the value of the atomic $\mathsf{CAScounter}_k$ object is 0, it follows from the definition of the method call u_0, that $\mathsf{Count}(u_0)^+ = \mathcal{C}(v_0)^+ = 0$, and the return value of u_0 matches that of v_0.

Induction Step ($i > 0$) From the induction hypothesis, $\mathsf{Count}(u_{i-1})^+ = \mathcal{C}(v_{i-1})^+$.

Case a - u_i is an `inc()` method call that executes a successful `CAS()` operation in line 12. Then $pt(u_i)$ is when object Count is incremented from β to $\beta+1$ by a successful $\mathsf{Count}.\mathtt{CAS}(\beta, \beta+1)$ operation in line 12, and thus $\mathsf{Count}(u_{i-1})^+ = \beta$ holds. Also, u_i returns $\beta = \mathsf{Count}(u_{i-1})^+$. Since u_i fails the if-condition of line 9, $\beta \neq k$ and therefore $\mathsf{Count}(u_{i-1})^+ = \beta \neq k$ holds. Now consider operation v_i in S_v. Since $\mathcal{C}(v_{i-1})^+ = \mathsf{Count}(u_{i-1})^+ \neq k$, the if-condition of line 1 fails, and the value of the atomic $\mathsf{CAScounter}_k$ is incremented in line 2 and $\mathcal{C}(v_{i-1})^+$ returned in line 3. Hence $\mathsf{Count}(u_i)^+ = \mathcal{C}(v_i)^+$ and the return values match.

Case b - u_i is an `inc()` method call that returns from line 10. Then $pt(u_i)$ is when the `Read()` operation on the object Count is executed in line 10. Clearly, the value returned by the `Read()` operation on the object Count at $pt(u_i)$ is $\mathsf{Count}(u_{i-1})^+$. Since the if-condition of line 10 is satisfied, $\mathsf{Count}(u_{i-1})^+ = k$ and u_i returns integer k without changing object Count. Now consider operation v_i in S_v. Since $\mathcal{C}(v_{i-1})^+ = \mathsf{Count}(u_{i-1})^+$ and $\mathsf{Count}(u_{i-1})^+ = k$, the if-condition of line 1 is satisfied and integer k is returned without changing the atomic $\mathsf{CAScounter}_k$ object. Hence $\mathsf{Count}(u_i)^+ = \mathcal{C}(v_i)^+$ and the return values match.

Case c - u_i is a `CAS()` method call that returns from line 15. Then the if-condition of line 15 is satisfied and thus $new \notin \{0, 1, \ldots, k\}$ and u_i returns **false** without changing Count. Now consider operation v_i in S_v. Since $new \notin \{0, 1, \ldots, k\}$, the if-condition of line 4 will be satisfied and the Boolean value **false** is returned without changing the value of object $\mathcal{C}$. Hence $\mathsf{Count}(u_i)^+ = \mathcal{C}(v_i)^+$ and the return values match.

Case d - u_i is a `CAS()` method call that returns from line 16. Then $pt(u_i)$ is when the `CAS` operation on the object Count is executed in line 16, and u_i returns the result of this `CAS` operation. The `CAS` operation attempts to change the value of Count from *old* to *new*, therefore if $\mathsf{Count}(u_{i-1})^+ = old$ then $\mathsf{Count}(u_i)^+ = new$ and u_i returns **true**, or else Count remains unchanged and u_i returns **false**. Now consider operation v_i in S_v. From the code structure, if $\mathcal{C}(v_{i-1})^+ = old$ then $\mathcal{C}(v_i)^+ = new$ and the Boolean value **true** is returned. And if $\mathcal{C}(v_{i-1})^+ \neq old$ then the value of object $\mathcal{C}$ remains unchanged and the Boolean value **false** is returned. Hence $\mathsf{Count}(u_i)^+ = \mathcal{C}(v_i)^+$ and the return values match. ☐

Lemma A.2. *The probability that an* `inc()` *method call returns* $\perp$ *is* $k/(k+1)$ *against the weak adversary.*

Proof. Let the process calling the `inc()` method call (say u) be p and let the value of the object Count immediately before p executes line 8 be z. Since the adversary is weak, no other process executes a shared memory operation after p chooses β in line 8 and before p finishes executing its next shared memory operation. From the code structure, p returns $\perp$ during u (in line 14) if and only if $z \neq \beta$. Since

$$\text{Prob}(z \neq \beta) = 1 - \text{Prob}(z = \beta) = 1 - \frac{1}{k+1} = \frac{k}{k+1},$$

the claim follows. $\square$

The following claim follows immediately from an inspection of the code.

Lemma A.3. *Each of the methods of* $\mathsf{RCAScounter}_k$ *has step complexity* $\mathcal{O}(1)$*, and is wait-free.*

Lemma 2.1 follows from Lemmas A.1, A.2 and A.3.

B Specification of Type $\mathsf{AbortableProArray}_k$

Type $\mathsf{AbortableProArray}_k$ is presented in Figure 5.

```
Type AbortableProArray_k

A: array [0...k-1] of int pairs init all ⟨⊥,⊥⟩;    REG, PRO, ABORT: int init 1,2,3

Operation collect(int[] X)
1  for i ← 0 to k-1 do
2      ⟨v,s⟩ ← A[i]
3      if v ≠ ABORT ∧ X[i] ≠ ⊥ then A[i] ← ⟨REG, X[i]⟩
4  end

Operation abort(int i, int seq)
5  ⟨v,s⟩ ← A[i]
6  if v = PRO then return false
7  A[i] ← ⟨ABORT, seq⟩
8  return true

Operation reset()
9  for i ← 0 to k-1 do A[i] ← ⟨0,⊥⟩

Operation promote()
10 for i ← 0 to k-1 do
11     ⟨v,s⟩ ← A[i]
12     if v = REG then
13         A[i] ← ⟨PRO, s⟩
14         return ⟨i, s⟩
15     end
16 end
17 return ⟨⊥,⊥⟩

Operation remove(int i)
18 ⟨v,s⟩ ← A[i]
19 A[i] ← (ABORT, s)
```

Figure 5: Sequential Specification of Type $\mathsf{AbortableProArray}_k$

C The Single-Fast-Multi-Slow Universal Construction

In this section, rather than implementing object $\mathsf{SFMSUnivConst}\langle\mathsf{T}\rangle$, we implement a *lock-free* universal construction object $\mathsf{SFMSUnivConstWeak}\langle\mathsf{T}\rangle$, with slightly weaker properties than $\mathsf{SFMSUnivConst}\langle\mathsf{T}\rangle$. An object implementation is lock-free, if in any infinite history H where processes continue to take steps, and H contains only operations on that object, some operation finishes. Object SFMSUnivConstWeak⟨T⟩ has the same properties as object SFMSUnivConst⟨T⟩ except method `doFast()` is lock-free with unbounded step-complexity.

There is a standard technique called *operation combining* [18] that can be applied to transform our lock-free object SFMSUnivConstWeak⟨T⟩ to the wait-free object SFMSUnivConst⟨T⟩ with $\mathcal{O}(N)$ step complexity for method `doFast()`.

By applying the technique of operation combining we can transform our lock-free universal construction SFMSUnivConstWeak⟨T⟩ into our wait-free object SFMSUnivConst⟨T⟩. We however do not provide a proof of its properties. Doing so would be repeating the same "standard" proof ideas from [18], and would result in increasing the size of the paper without contributing to the main ideas of this paper. We do provide proofs for our lock-free universal construction SFMSUnivConstWeak⟨T⟩, and the proofs illustrate the main idea from this section, i.e., how to achieve a linearizable concurrent implementation with support for a `doFast()` method of $\mathcal{O}(1)$ step complexity. We now present the implementation of object SFMSUnivConstWeak⟨T⟩ (see Figure 6).

Object SFMSUnivConstWeak⟨T⟩

shared: mReg: **int init** $(s_0, \perp, 0, 0)$; fastOp: **int init** $(\perp, 0)$;
local: $state, res, fc, sc, s1, s1, r1, r2, seq$: **int init** 0

Method `doFast(op)`

$(state, res, fc, sc) \leftarrow$ mReg.`Read()`
fastOp $\leftarrow (op, fc + 1)$
if $\neg$`helpFast()` **then** `helpFast()`
$(state, res, fc, sc) \leftarrow$ mReg.`Read()`
return res

Method `helpFast()`

$(s1, r1, fc, sc) \leftarrow$ mReg.`Read()`
$(op, seq) \leftarrow$ fastOp.`Read()`
if $fc \geq seq$ **then** **return true**
$(s2, r2) \leftarrow$ `f`$(s1, op)$
return mReg.`CAS`$((s1, r1, fc, sc), (s2, r2, seq, sc))$

Method `f`$(state_1, op)$

$state_2 \leftarrow$ state generated when op is applied to object O with state $state_1$
$res \leftarrow$ result when op is applied to object O with state $state_1$
return $(state_2, res)$

Method `performSlow(op)`

repeat
  $(s1, r1, fc, sc) \leftarrow$ mReg.`Read()`
  $(s2, r2) \leftarrow$ `f`$(s1, op)$
  if $s2 = s1$ **then** **return** $r2$
  `helpFast()`
until mReg.`CAS`$((s1, r1, fc, sc), (s2, r1, fc, sc + 1))$
return $r2$

Figure 6: Implementation of Object SFMSUnivConstWeak⟨T⟩.

Shared Data. A shared register mReg stores a 4-tuple (m_0, m_1, m_2, m_3). We use the notation mReg$[i]$ to refer to the $(i + 1)$-th tuple element, m_i, stored in register mReg. Element mReg[0] stores the state of object O. Element mReg[1] stores the result of the most recent fast operation performed. Elements mReg[2] and mReg[3] store counts of the number of fast and slow operations performed respectively. Initially mReg[0] stores the initial state of O, mReg[1] has value $\perp$ and (mReg[2],mReg[3]) is $(0, 0)$.

A shared register fastOp is used to *announce* a fast operation to be performed in a pair (s_0, s_1). Element fastOp[0] stores the complete description of a fast operation to be performed. Element fastOp[1] stores a sequence number indicating the number of fast operations that have been announced in the past. This sequence number is used by processes to determine whether an announced fast operation is pending execution. Initially fastOp is $(\perp, 0)$. The methods `doFast()` and `doSlow()` make use of two private methods `helpFast()` and `f()` (see Figure 6).

Description of the `f()` method. Method `f()` is implemented using the specification provided by type T. The method takes two arguments $state_1$ and op, where $state_1$ is a state of object O and op is the complete description of an operation to be applied on object O. The method computes the new state $state_2$ and the result $result$, when operation op is applied on object O with state $state_1$. The method then returns the pair $(state_2, result)$. Since no shared memory operations are executed during the method, the method has 0 step complexity.

Description of the `doFast()` method. Let p be a process that executes `doFast(`op`)`. In line 1, process p first copies the 4-tuple read from register mReg to its local variables $state, res, fc$ and sc. Then p announces the operation op by writing the pair $(op, fc + 1)$ to register fastOp in line 2. After announcing the operation, process p *helps* perform the announced operation by calling the private method `helpFast()` in line 3. If the call to `helpFast()` returns `false`, then p concludes that the announced operation may not have been performed yet. In this case p makes another call to `helpFast()` in line 3 to be sure that the announced operation is performed (we prove later that at most two calls to `helpFast()` are required to perform an announced operation). Process p then reads and returns the result of the performed operation stored in mReg[1] in line 4 and 5, respectively. Since method `doFast()` is not executed concurrently (by assumption), the result of p's operation stored in register mReg is not overwritten before the end of p's `doFast(op)` call.

Description of the `helpFast()` method. Let q be a process that calls and executes `helpFast()`. In line 9, process q first copies the 4-tuple read from register mReg into its local variables $s1, r1, fc$ and sc. The value read from mReg[0] constitutes the state of object O, to which q will attempt to apply the announced operation if required. The value read from mReg[1] is the result of the last fast operation performed on object O. The value read from mReg[2] and mReg[3] is the count of the number of fast and slow operations performed respectively. Process q then reads fastOp in line 10 to find out the announced operation op and the announced sequence number seq. Process q then determines whether the announced operation has already been performed, by checking whether seq is less than or equal to fc in line 11. If so, q concludes that operation op has been performed and returns **true**, otherwise it attempts to perform op in lines 12 and 13. In line 12 process q calls the private method `f()` to compute the new state $s2$ and the result $r2$ when operation op is applied to object O with state $s1$. In line 13, process p attempts to perform op by swapping the 4-tuple $(s1, r1, fc, sc)$ with $(s2, r2, fc + 1, sc)$ using a `CAS` operation on mReg. If the `CAS` is unsuccessful then no changes are made to mReg. This can happen only if some other process performs an announced fast operation in line 13 or a slow operation in line 19. The result of the `CAS` operation of line 13 is returned in either case.

Description of the `doSlow()` method. Let p be a process that calls and executes `doSlow(`op`)`. During the method, p repeats the while-loop of lines 15-19 until p is able to successfully apply its operation op. In line 15, process p first copies the 4-tuple read from register mReg to its local variables $s1, r1, fc$ and sc. In line 16 process q calls the private method `f()` to compute the new state $s2$ and the result $r2$ when operation op is applied to object O with state $s1$. In the case that operation op does not cause a state change in object O, i.e., $s1 = s2$, then p returns result $r2$ in line 17. Otherwise p attempts to apply operation op in line 19 by swapping the 4-tuple

$(s1, r1, fc, sc)$ with $(s2, r2, fc, sc + 1)$ using a `CAS` operation on register mReg. Before attempting to apply its own operation in line 19 p makes a call to `helpFast()` in line 18 to help perform an announced fast operation (if any). On completing the while-loop, p would have successfully applied its operation *op*, and thus p returns the result of the applied operation in line 20.

The following lemma (proven in Section C.2) summarizes the properties of object SFMSUnivConstWeak$\langle$T$\rangle$.

Lemma C.1. *Object* SFMSUnivConstWeak$\langle$T$\rangle$ *is a lock-free universal construction object that implements an object* $\mathbb{O}$ *of type* T*, for* n *processes, where* n *is the maximum number of processes that can access object* SFMSUnivConstWeak$\langle$T$\rangle$ *concurrently and operations on object* $\mathbb{O}$ *are performed using either method* `doFast()` *or* `doSlow()`*, and no two processes execute method* `doFast()` *concurrently. Method* `doFast()` *has* $\mathcal{O}(1)$ *step complexity.*

C.1 Operation Combining Technique

In principle the technique works as follows: Processes maintain an N-element array, say announce, where process i "owns" slot i, and processes store in their respective slots the operation that they want to apply. When a process p wants to apply an operation it first "announces" its operation by writing the operation to the p-th element of the array. Then p attempts to *help* the "next" operation in the announce array by attempting to apply that operation if it has not been applied, yet. An index to the "next" operation to be applied is maintained in the same register that stores the state of the concurrent object. Every time an announced operation is applied, the index is also incremented modulo N in one atomic step. The response of applied operations is stored in another N-element array, say response, which can sometimes be combined with the announce array. Sequence numbers are used to ensure that an announced operation is not applied more than once. Since the index of the "next" operation cycles the announce array, a process needs to help announced operations $\mathcal{O}(N)$ times before its own announced operation is applied, at which point it can stop.

Herlihy [18] introduced this technique as a general methodology to transform lock-free universal constructions to wait-free ones. Herlihy presents another example [19] that employs the technique of operation combining to transform a lock-free universal construction to a wait-free one, where the step complexity of the method that performs the operation is bounded to $\mathcal{O}(N)$.

On applying the standard technique of operation combining [18] to object SFMSUnivConstWeak$\langle$T$\rangle$ we obtain object SFMSUnivConst$\langle$T$\rangle$ and Lemma 2.2

C.2 Analysis and Proofs of Correctness of Object SFMSUnivConstWeak$\langle$T$\rangle$

Let a `helpFast()` method call that returns `true` in line 13 (on executing a successful `CAS` operation) be called a *successful* `helpFast()`.

Claim C.2. *(a) The value of* fastOp[1] *changes only in line 2.*

(b) The value of mReg[3] *increases by one with every successful* `CAS` *operation in line 19 and no other operation changes* mReg[3]*.*

(c) The value of mReg[2] *increases with every successful* `CAS` *operation in line 13 (during a successful* `helpFast()`*), and no other operation changes* mReg[2]*.*

Proof. Part (a) follows immediately from an inspection of the code. Register mReg is changed only when a process executes a successful `CAS` operation in lines 13 or 19. Furthermore, in line 13 mReg[3] is not changed and in line 19 mReg[2] is not changed. Since, in line 19 mReg[3] is incremented

Part (b) follows immediately. Now, for a process to execute line 13, the if-condition of line 11 must fail, hence mReg[2] is increased from its previous value and Part (c) follows. □

Consider an arbitrary history H where processes access an SFMSUnivConstWeak⟨T⟩ object but no two `doFast()` method calls are executed concurrently. Since the fast operations are executed sequentially the happens before order on all `doFast()` method calls in H is a total order.

Claim C.3. *Let u_t be the t-th* `doFast()` *method call in history H being executed by process p_t. For $t \geq 1$ let α_t be the point in time when p_t executes line 2 during u_t and γ_t be the point when p_t is poised to execute line 4. Let u_t's* helpers *be the processes that call* `helpFast()` *such that the value read by the processes in line 10 is the value written to register* fastOp *at α_t. Let β_t be the first point in time when a helper's call to* `helpFast()` *succeeds after α_t. Let $\alpha_0, \beta_0, \gamma_0$ be the start of execution H. Then the following claims hold for all $t \geq 0$:*

(S_1) β_t exists and β_t is in (α_t, γ_t)

(S_2) Throughout (α_t, β_t) : $\mathsf{fastOp}[1] = \mathsf{mReg}[2] + 1 = t$

(S_3) Throughout (β_t, α_{t+1}) : $\mathsf{fastOp}[1] = \mathsf{mReg}[2] = t$

Proof. We prove claims $(S_1), (S_2)$ and (S_3) by induction over t.

Basis: For $t = 0$, (S_1) and (S_2) are trivially true. By assumption the initial value of fastOp[1] and mReg[2] is 0. Consider the interval (β_0, α_1). From Claim C.2(a) it follows that fastOp is written for the first time at α_1. The first point when one of the invariants (S_3) is destroyed is if a process (say p) executes a successful `CAS` operation in line 13 during (β_0, α_1). Then p read the value 0 from register fastOp[1] in line 10, since the initial value of fastOp[1] is 0 and fastOp[1] is written to for the first time at α_1. Since mReg[2] is never decremented (from Claim C.2(c)) and mReg[2] initially has value 0, p satisfies the if-condition of line 11 and p's `helpFast()` call returns `true` in line 11. Therefore, p does not execute line 13, which is a contradiction.

Induction Step: For $t \geq 1$:

Proof of (S_1): Consider the interval (α_t, γ_t). To show that (S_1) holds for t, we need to show that mReg[2] is changed during (α_t, γ_t). Consider p_t's first call to `helpFast()` in line 3 during u_t. From induction hypothesis (S_3) for $t-1$, it follows that $\mathsf{fastOp}[1] = \mathsf{mReg}[2] = t - 1$ during (β_{t-1}, α_t). Then p_t reads value $t-1$ from mReg[2] in line 1 and writes value t to fastOp[1] in line 2. Since fastOp[1] is changed only at α_{t+1} after α_t, it follows that p_t reads t from register fastOp[1] in line 10.

Case a - p_t returns from line 11: Then p_t read a value from mReg[2] in line 9 that is at least t. Since $\mathsf{mReg}[2] = t-1$ holds immediately before α_t some process changed mReg[2] in line 13 during (α_t, γ_t). Hence, (S_1) for t holds.

Case b - p_t returns `true` from line 13: Then p_t has changed mReg[2] and hence (S_1) holds for t.

Case c - p_t returns `false` from line 13: Then some process q changed register mReg after p_t read mReg in line 9. Now, register mReg is written to only in line 13 or line 19 (from an inspection of the code).

Subcase c1 - q changed mReg by executing line 13: Then q has changed mReg[2] and hence (S_1) holds for t.

Subcase c2 - q changed mReg by executing line 19: Then p_t executes a second call to `helpFast()` in line 3. Let m be the value of mReg[3] read by p_t in line 9. If p_t's second `helpFast()` call satisfies case (a) or (b) then we get that (S_1) holds for t.

If p_t's second `helpFast()` call returns `false` from line 13, then some process changed mReg after p_t read mReg in line 9. If some process changed mReg by executing line 13 then we get that (S_1) holds for t. Then some process changed mReg by executing line 19 after p_t read mReg in line 9 and let r be the first process to do so. Therefore, r changes the value of mReg[3] from m to $m+1$ in line 19. Then r executed line 15 after q executed a successful `CAS` operation in line 19. Then r completed a call to `helpFast()` in line 18 after α_t. Since r reads mReg after α_t, r satisfied the if-condition of line 11 and executed line 13. If r successfully executes the `CAS` operation in line 13 then we get that (S_1) holds for t. Then some process s must have changed mReg after r read mReg in line 9. Since the value of mReg[3] is only incremented (by Claim C.2(b)) and r changes the value of mReg[3] from m to $m+1$, it follows that s changed mReg in line 13 and hence (S_1) holds for t.

Proof of (S_2) and (S_3): From (S_1) for t it follows that β_t exists and $\alpha_t < \beta_t < \gamma_t < \alpha_{t+1}$. From the induction hypothesis invariants (S_2) and (S_3) are true until α_t. Now, one of the invariants (S_2) or (S_3) can be destroyed only if some process executes a successful `CAS` operation in line 13 and changes mReg[2]. By definition of β_t, mReg[2] is unchanged during (α_t, β_t). Then invariants (S_2) and (S_3) continue to hold until β_t. Therefore, claim (S_2) holds for t. It still remains to be shown that claim (S_3) holds for t.

Let p be the process that executes a successful `CAS` operation in line 13 and changes mReg[2] at β_t. Since mReg$[2] = t-1$ immediately before β_t and p executes a successful `CAS` operation in line 13 at β_t, $p.fc = t-1$. Then p executed lines 9 and 10 during (α_t, β_t) and $p.seq = t$. Therefore, invariant (S_3) is true immediately after β_t.

Now, assume another process (say q) destroys one of the invariants (S_3) or S_4 by executing a successful `CAS` operation in line 13 during (β_t, α_{t+1}). Then q must have read register mReg[2] and fastOp[1] after β_t, and therefore q must read the value t from both of them. Then q must have satisfied the if-condition of line 11 and returned `true`. Hence, q does not execute line 13, which is a contradiction. Therefore, invariant (S_3) is true up to α_{t+1}, and thus claim (S_3) holds for t. ☐

Let H' be a history that consists of all completed method calls in H and all pending method calls that executed line 2 (`Write` operation on register fastOp), or which executed a successful `CAS` operation in line 13 or line 19. We omit all other pending method calls, since during those method calls no operations are executed that changes the state of any shared object, and hence those pending method calls cannot affect the validity of any other operation. Therefore, to prove that history H is linearizable it suffices to prove that history H' is linearizable.

For each method call u in H', we define a point $pt(u)$ and an interval $I(u)$. Let $I(u)$ denote the interval between u's invocation and response. If u is a `doSlow()` method call that returns from line 17 then $pt(u)$ is the point in time of the `Read` operation in line 15, otherwise, $pt(u)$ is the point in time of the `CAS` operation in line 19. If u is a `doFast()` method call, let v be a successful `helpFast()` method call such that v's line 13 is executed after u's line 2 and before u returns. We define $pt(u)$ to be the point of the successful `CAS` operation in v's line 13.

Claim C.4. *For every method call u in H, $pt(u)$ exists and lies in $I(u)$.*

Proof. There are two types of method calls in H, `doFast()` and `doSlow()`.

Case a - u is a `doFast()` method call.

From Claim C.3 it follows that exactly one of u's helpers (see Claim C.3 for definition) succeeds and the helper performs a successful `CAS` operation in line 13 at some point in $I(u)$. Therefore, point $pt(u)$ exists and lies in $I(u)$.

Case b - u is a `doSlow()` method call. By definition $pt(u)$ is assigned to a line of u's code, therefore $pt(u)$ exists and lies in $I(u)$. ☐

Let S be the sequential history obtained by ordering all method calls u in H' according to the points $pt(u)$. To show that $\mathsf{SFMSUnivConstWeak}\langle\mathsf{T}\rangle$ is a linearizable implementation of an object O of type T, we need to show that the sequential history S is valid, i.e., S lies in the specification of type T, and that $pt(u)$ lies in $I(u)$ (already shown in Claim C.4). Let S_v be the sequential history obtained when the operations of S are executed sequentially on object O, as per their order in S. Clearly, S_v is a valid sequential history in the specification of type T by construction. Then to show that S is valid, we show that $S = S_v$.

Let v_t be the t-th operation in S_v and let u_t be the t-th method call in S. Let UC_t^- and UC_t^+ denote the value of $\mathsf{mReg}[0]$ immediately before and after $pt(u_t)$, respectively. Let O_t^- and O_t^+ denote the state of object O immediately before and after operation v_t, respectively. Let α_t and β_t denote the value returned by u_t and v_t, respectively. Define $\mathsf{UC}_0^+ = \mathsf{UC}_1^-$ and $\mathsf{O}_0^+ = \mathsf{O}_1^-$. Define $\alpha_0 = \beta_0 = \perp$.

Claim C.5. *Suppose a process calls method* `f(`x_1, op`)` *and the method returns the value pair* (x_2, y)*. If* $x_1 = \mathsf{O}_t^-$ *then* $x_2 = \mathsf{O}_t^+$ *and* $y = \beta_t$*.*

Proof. By definition, a call to method `f(`x_1, op`)` returns the value pair (x_2, y) such that x_2 is the state of O when operation op is applied to O while at state x_1 and y is the result of the operation. Then if $x_1 = \mathsf{O}_t^-$ then $x_2 = \mathsf{O}_t^+$ and $y = \beta_t$. ☐

Claim C.6. *For all* $t \geq 1$*.*

(S_1) $\mathsf{O}_t^+ = \mathsf{O}_{t+1}^-$ *and* $\mathsf{UC}_t^+ = \mathsf{UC}_{t+1}^-$

(S_2) $\mathsf{UC}_t^- = \mathsf{O}_t^-$

(S_3) $\mathsf{UC}_{t-1}^+ = \mathsf{O}_{t-1}^+$ *and* $\alpha_{t-1} = \beta_{t-1}$

Proof. **Proof of** (S_1)**:** Since operations in S_v are executed sequentially, it follows that $\mathsf{O}_t^+ = \mathsf{O}_{t+1}^-$. We now show that $\mathsf{UC}_t^+ = \mathsf{UC}_{t+1}^-$. Assume $\mathsf{UC}_t^+ \neq \mathsf{UC}_{t+1}^-$. Then some process p changed the value of $\mathsf{mReg}[0]$ by executing a successful `CAS` operation in line 13 or line 19 at some point during the interval $(pt(u_t), pt(u_{t+1}))$. By definition, p's successful `CAS` operation in line 13 or line 19 is $pt(u_\ell)$ for some method call u_ℓ where u_ℓ is the ℓ-th method call in H'. Thus, ℓ is an integer and $t < \ell < t+1$ holds, which is a contradiction.

Proof of (S_2) **and** (S_3)**:** We prove (S_2) and (S_3) by induction over t.

Basis $(t = 1)$ **-** By assumption, initially, $\mathsf{mReg}[0]$ is the initial state of O, hence, $\mathsf{UC}_1^- = \mathsf{O}_1^-$. Hence, (S_2) is true. (S_3) is true trivially.

Induction Step - We assume (S_2) and (S_3) for t are true and prove that (S_2) and (S_3) for $t+1$ are true. From (S_1) we have, $\mathsf{O}_t^+ = \mathsf{O}_{t+1}^-$ and $\mathsf{UC}_t^+ = \mathsf{UC}_{t+1}^-$. From (S_3) for t we have $\mathsf{UC}_t^+ = \mathsf{O}_t^+$. Therefore, it follows that $\mathsf{UC}_{t+1}^- = \mathsf{O}_{t+1}^-$ and thus (S_2) for $t+1$ is true.

To show (S_3) for $t+1$ is true, we need to show $\mathsf{UC}_t^+ = \mathsf{O}_t^+$ and $\alpha_t = \beta_t$. By Claim (S_2) for t, $\mathsf{UC}_t^- = \mathsf{O}_t^-$ holds. Let p_t be the process executing u_t.

Case a - u_t is a `doSlow(`op`)` method call: Let x_1 be the most recent value read by p_t from $\mathsf{mReg}[0]$ in line 15 and let (x_2, y) be the value returned when p_t executes line 16. From the code structure, $\alpha_t = y$.

Subcase (a1) - p_t returns from line 17: Then $pt(u_t)$ is the point when p_t executes a successful `Read` operation on register mReg in line 15. Since p satisfies the if-condition of line 17, $x_2 = x_1$. Thus, $\mathsf{UC}_t^- = \mathsf{UC}_t^+ = x_1$.

Subcase (a2) - p_t returns from line 20: Then $pt(u_t)$ is the point when p_t executes a successful `CAS` operation on register mReg in line 19. From the definition of a `CAS` operation, it follows that $\mathsf{UC}_t^- = x_1$ and $\mathsf{UC}_t^+ = x_2$.

For both subcases **(a1)** and **(a2)**, $x_1 = \mathsf{UC}_t^- = \mathsf{O}_t^-$ holds. Then from Claim C.5 it follows that $x_2 = \mathsf{O}_t^+$ and $y = \beta_t$. Since $x_2 = \mathsf{UC}_t^+$ and $y = \alpha_t$, $\mathsf{O}_t^+ = \mathsf{UC}_t^+$ and $\alpha_t = \beta_t$.

Case b - u_t is a `doFast(`op`)` method call: Then $pt(u_t)$ is the point when a successful `CAS` operation on register mReg is executed in line 13 of method call w where w is the first successful `helpFast()` method call that begins after u_t's line 2 is executed. Let q be the process executing w. Let x_1 be the value read by q from $\mathsf{mReg}[0]$ in line 9 and let (x_2, y) be the value returned when q executes line 12. From the definition of a `CAS` operation, it follows that $\mathsf{UC}_t^- = x_1$ and $\mathsf{UC}_t^+ = x_2$. Since $x_1 = \mathsf{UC}_t^- = \mathsf{O}_t^-$, from Claim C.5 it follows that $x_2 = \mathsf{O}_t^+$ and $y = \beta_{u_t}$. Since $x_2 = \mathsf{UC}_t^+$, it follows that $\mathsf{O}_t^+ = \mathsf{UC}_t^+$.

From Claim C.3 if follows that $\mathsf{mReg}[1]$ is changed exactly once during u_t, specifically at $pt(u_t)$, where q writes the value y to it. Thus, p reads the value y from $\mathsf{mReg}[1]$ in line 4 since p executes line 4 after $pt(u_t)$ (Claim C.3). Therefore, it follows that $\alpha_{u_t} = y = \beta_{u_t}$. ☐

Lemma C.7. *History H' has a linearization in the specification of* T.

Proof. By Claim C.4, for each method call u in H', $pt(u)$ exists and lies in $I(u)$. Thus, to show that H' is linearizable we only need to show that S lies in the specification of type T. Thus, we need to show that for all $t \geq 1$, the value returned by v_t matches that value returned by u_t. From Claim C.6 (S_3) it follows that for all $t \geq 1$, $\alpha_t = \beta_t$. ☐

Lemma C.8. *Object* $\mathsf{SFMSUnivConstWeak}\langle\mathsf{T}\rangle$ *is lock-free.*

Proof. Suppose not. I.e., there exists an infinite history H during which processes take steps but no method call finishes. It is clear from an inspection of method `doFast()` and private method `helpFast()`, that both methods are wait-free. Then if H contains steps executed by a process that executes a call to `doFast()` then the `doFast()` method call finishes since processes continue to take steps in history H – a contradiction. Now consider the only other case, where history H contains steps executed by processes only on `doFast()` method calls. Consider a process p that takes steps in history H and fails to complete its `doSlow()` method call. Then during p's execution p reads register mReg in line 15 and fails its `CAS` operation in line 19 during an iteration of the loop of lines 14-19. Now p's `CAS` operation can fail only if some process executes a successful `CAS` operation in line 13 or line 19 between p's `Read()` and `CAS` operation.

Case a - Some process q executes a successful `CAS` operation in line 19. Then q breaks out of the loop of lines 14-19. Since processes continue to take steps in our infinite history H, q eventually returns from its `doSlow()` method call – a contradiction.

Case b - Some process q executes a successful `CAS` operation in line 13. Then q has performed a successful `helpFast()` method call and incremented $\mathsf{mReg}[2]$. Let the value of $\mathsf{mReg}[2]$ after the increment be z. Now consider the next iteration of the loop by process p, where p's `CAS` operation in line 19 fails again. Since **Case a** leads to a contradiction, some process r executed a successful `CAS` operation in line 13. Then r read incremented $\mathsf{mReg}[2]$ to some value greater than z in line 13. From the code structure of the `helpFast()` method, r failed the if-condition of line 11, and therefore r read $seq = \mathsf{fastOp}[1] > z$ in line 10. Since $\mathsf{fastOp}[1]$ is incremented only in line 2 during a `doFast()` method call, it follows that a `doFast()` method was called after q incremented $\mathsf{mReg}[2]$ to z in line 13. This is a contradiction to the assumption that processes take steps executing only method `doSlow()` during our history H. ☐

Lemma C.1 follows from Lemma C.7 and C.8.

D The Array Based Randomized Abortable Lock

D.1 Implementation / Low Level Description

We now describe the implementation of our algorithm in detail. (See Figure 1 and 2). We now describe the method calls in detail and illustrate the use of each of the internal objects as and when we require them.

The `lock()` method. Suppose p executes a call to $\texttt{lock}_i\texttt{()}$. Process p first receives a sequence number using a call to `getSequenceNo()` in line 1 and stores it in its local variable s. Method `getSequenceNo()` returns integer k on being called for the k-th time from a call to $\texttt{lock}_i\texttt{()}$. Since calls to $\texttt{lock}_i\texttt{()}$ are executed sequentially, a sequential shared counter suffices to implement method `getSequenceNo()`. Method `getSequenceNo()` is used to return unique sequence number which helps solve the classic ABA problem. The ABA problem is as follows: If a process reads an object twice and reads the value of the object to be 'A' both times, then it is unable to differentiate this scenario from a scenario where the object was changed to value 'B' in between the two reads of the object. Process p then spins on $\mathsf{apply}[\mathrm{i}]$ in line 2 until p *registers* itself by swapping the value $\langle \mathsf{REG}, s\rangle$ into $\mathsf{apply}[\mathrm{i}]$ using a `CAS` operation. Processes write the value REG in the apply array to announce their presence at lock L.

Process p then executes the *role-loop*, lines 4-12, until p either increases the value of Ctr to 1 or 2, or until p is notified of its promotion. Process p begins an iteration of the role-loop by calling the $\mathsf{Ctr}.\texttt{inc()}$ operation in line 5 and stores the returned value into $\mathsf{Role}[i]$. The returned value determines p's current role at lock L. The shared array Role is used by process p to store its role in slot $\mathsf{Role}[\mathrm{i}]$, which can later be read to determine the actions to perform at lock L. This is important because we want to allow the behavior of transferring locks. Specifically, to enable a process q to call $\texttt{release}_i\texttt{()}$ on behalf of p, q needs to determine p's role at lock L, which is possible by reading $\mathsf{Role}[\mathrm{i}]$.

If the $\mathsf{Ctr}.\texttt{inc()}$ operation in line 5 fails, i.e., it returns $\perp$, then p repeats the role-loop. Such repeats can happen only a constant number of times in expectation (by Claim A.2). If the value returned in line 5 is 0 or 1, then p has incremented the value of Ctr (from the semantics of a $\mathsf{RCAScounter}_2$ object), and it becomes king_L or $\mathsf{queen}_\mathsf{L}$, respectively, and breaks out of the role-loop in line 12.

If p becomes king_L in line 5, then p fails the if-condition of line 13 and proceeds to execute lines 16-17. In line 16, p changes $\mathsf{apply}[\mathrm{i}]$ to the value $\langle \mathsf{PRO}, s\rangle$, to prevent itself from getting promoted in future promote actions. In line 17, p returns from its `lock()` call by returning the special value ∞ (a non-$\perp$ value indicating a successful `lock()` call), since p is king_L.

If p becomes $\mathsf{queen}_\mathsf{L}$ in line 5, then p knows that there exists a king process at lock L, and thus $\mathsf{queen}_\mathsf{L}$ proceeds to spin on $\mathsf{Sync1}$ in line 14 awaiting a notification from king_L. Recall that king_L notifies $\mathsf{queen}_\mathsf{L}$ of $\mathsf{queen}_\mathsf{L}$'s turn to own lock L by writing the integer j into $\mathsf{Sync1}$ during a $\texttt{release(}j\texttt{)}$ call. Once p receives king_L's notification (by reading a non-$\perp$ value in $\mathsf{Sync1}$ in line 14), p breaks out of the spin loop of line 14, and proceeds to execute lines 16-17. In line 16, p changes $\mathsf{apply}[\mathrm{i}]$ to the value $\langle \mathsf{PRO}, s\rangle$, to prevent itself from getting promoted in future promote actions. In line 17, p returns from its `lock()` call by returning the integer value stored in $\mathsf{Sync1}$ (a non-$\perp$ value indicating a successful `lock()` call).

If the value returned in line 5 is 2, then p does not become king_L or $\mathsf{queen}_\mathsf{L}$, and thus p assumes the role of a pawn. Process p then waits for a notification of its own promotion, or, for the Ctr value to decrease from 2, by spinning on $\mathsf{apply}[\mathrm{i}]$ and Ctr in line 7. When p breaks out of this spin lock, it determines in line 8 whether it was promoted by checking whether the value of $\mathsf{apply}[\mathrm{i}]$ was changed to $\langle \mathsf{PRO}, s\rangle$. A process is promoted only by a king_L, $\mathsf{queen}_\mathsf{L}$ or a $\mathsf{ppawn}_\mathsf{L}$ during their `release()` call. If p finds that it was not promoted, then p is said to have been *missed* during a Ctr-cycle, and thus p repeats the role-loop. We later show that a process gets missed during at most one Ctr-cycle.

If p was promoted, then it writes a constant value $\mathsf{PAWN\_P} = 3$ into $\mathsf{Role}[i]$ in line 9 and becomes $\mathsf{ppawn}_\mathsf{L}$. Since p has been promoted, p knows that both king_L and $\mathsf{queen}_\mathsf{L}$ are no longer executing their entry or Critical Section, and thus p owns lock L now. Then p goes on to break out of the role-loop in line 12, and proceeds to return from its `lock()` call by returning the special value ∞ (a non-$\perp$ value indicating a successful `lock()` call), since p is $\mathsf{ppawn}_\mathsf{L}$.

The `release()` method. Suppose p executes a call to $\mathtt{release}_i(j)$ with an integer argument j. We restrict the execution such that a process calls a $\mathtt{release}_i(j)$ method only after a call to a successful $\mathtt{lock}_i\mathtt{()}$ has been completed.

In line 34, p initializes the local variable r to the boolean value **false**. Local variable r is returned later in line 50 to indicate whether the integer j was successfully written to $\mathsf{Sync1}$ during the release method call. In lines 35, 42 and 45 process p determines its role at the node and the action to perform. In line 49, process p deregisters itself from lock L by swapping $\langle\perp,\perp\rangle$ into apply[i]. At the end of the method call a boolean is returned in line 50, indicating whether the integer j was written to $\mathsf{Sync1}$.

If p determines that it is king_L, then it attempts to decrease Ctr from 1 to 0 in line 36. This decrement operation will only fail if there exists a queen process at lock L which increased the Ctr to 2 during its `lock()` call. If the decrement operation fails then p has determined that there exists a queen process at lock L and it now synchronizes with $\mathsf{queen}_\mathsf{L}$ to perform the collect action. Recall that `CAS` object $\mathsf{Sync1}$ is used by king_L and $\mathsf{queen}_\mathsf{L}$ to determine which process performs a collect. In line 37, p attempts to swap integer j into $\mathsf{Sync1}$ by executing a $\mathsf{Sync1}.\mathtt{CAS}(\perp, j)$ operation and stores the result of the operation in local variable r. If p is successful then it performs the collect action by executing a call to $\mathtt{doCollect}_i\mathtt{()}$ in line 38. If p is unsuccessful then it knows that $\mathsf{queen}_\mathsf{L}$ will perform a collect. In line 39 p calls the $\mathtt{helpRelease}_i\mathtt{()}$ method to synchronize the release of lock L with $\mathsf{queen}_\mathsf{L}$. We describe the method `helpRelease()` shortly.

If p determines that it is $\mathsf{queen}_\mathsf{L}$, then it calls the $\mathtt{helpRelease}_i\mathtt{()}$ method call in line 43 to synchronize the release of lock L with king_L.

If p determines that it is a promoted pawn, then it attempts to promote a waiting pawn by making a call to `doPromote()` in line 46.

The `doCollect()` method. Suppose a process p executing a $\mathtt{doCollect}_i\mathtt{()}$ method call. The collect action consists of reading the apply array (left to right), and creating a vector A of n values, where the k-th element is either $\perp$ (to indicate that the process with pseudo-ID k is not a candidate for promotion) or an integer sequence number (to indicate that the process with pseudo-ID k is a candidate for promotion). The vector A is stored in the $\mathsf{AbortableProArray}_n$ instance $\mathsf{PawnSet}$ in line 55 using a $\mathsf{PawnSet}.\mathtt{collect}(A)$ operation. The $\mathsf{PawnSet}.\mathtt{collect}(A)$ operation ensures that if the k-th element of $\mathsf{PawnSet}$ has value $3 = \mathsf{ABORT}$ (written during a $\mathsf{PawnSet}.\mathtt{abort}(k, \cdot)$ operation), then the k-th element is not overwritten during the $\mathsf{PawnSet}.\mathtt{collect}(A)$ operation. This is required to ensure that processes that have expressed a desire to abort are not collected and subsequently promoted.

The `helpRelease()` method. Suppose king_L calls $\mathtt{helpRelease}_i\mathtt{()}$ and $\mathsf{queen}_\mathsf{L}$ calls $\mathtt{helpRelease}_k\mathtt{()}$. During the course of these method calls, king_L and $\mathsf{queen}_\mathsf{L}$ synchronize with each other in order to reset `CAS` objects $\mathsf{Sync1}$ and $\mathsf{Sync2}$, remove themselves from $\mathsf{PawnSet}$, promote a collected process and notify the promoted process. If no process is found in $\mathsf{PawnSet}$ that can be promoted, then the $\mathsf{PawnSet}$ object is reset to its initial state and Ctr reset to 0. Recall that `CAS` object $\mathsf{Sync2}$ is used as a synchronization primitive by king_L and $\mathsf{queen}_\mathsf{L}$ to determine which process exits last among them, and thus performs all pending release work. In line 56, the process which swaps value i or k into Sync2 by executing a successful `CAS` operation, exits, and the other process performs the pending release work in lines 57 - 63. Let us now refer to this other process as the releasing process. In lines 57 - 58, the releasing process resets Sync1 to its initial value $\perp$. In line 59, the releasing process reads the pseudo-ID written to Sync2 by the exited process (process that executed a successful `CAS` operation on Sync1). The pseudo-ID written to Sync2 is required to remove the exited process from getting promoted in a future promote in case it was collected in PawnSet. In line 61, the releasing process removes the exited process from PawnSet. `CAS` object Sync2 is reset to its initial value $\perp$ in line 60. In line 62, the releasing process calls `doPromote()` to promote a collected process.

The `doPromote()` method. Suppose p executes a call to $\texttt{doPromote}_i\texttt{()}$. In line 64, p removes itself from PawnSet by executing a PawnSet.`remove`(i) operation. It does so to prevent itself from getting promoted in case it was collected earlier. In line 65, p performs a promote action by executing a PawnSet.`promote()` operation. If a process was collected and the process has not aborted then its corresponding element (k-th element for a process with pseudo-ID k) in PawnSet will have the value $\langle \mathsf{REG}, \cdot \rangle$. If a process has aborted then its corresponding element in PawnSet will have the value $\langle \mathsf{PRO}, \cdot \rangle$.

If a successful `promote()` operation is executed then an element in PawnSet is changed from $\langle \mathsf{REG}, s \rangle$ to $\langle \mathsf{PRO}, s \rangle$, where $s \in \mathbb{N}$, and the pair $\langle k, s \rangle$ is returned, where k is the index of that element in PawnSet. In this case we say that process with pseudo-ID k was *promoted*. If an unsuccessful `promote()` operation is executed, then no element in PawnSet has the value $\langle \mathsf{REG}, s \rangle$, where $s \in \mathbb{N}$, and thus the special value $\langle \perp, \perp \rangle$ is returned. We then say that no process was promoted. The returned pair is stored in local variables $\langle j, seq \rangle$ in line 65.

If no process was promoted, then p resets PawnSet to its initial value in line 67 using the `reset()` operation, and decreases Ctr from 2 to 0 in line 68. If a process was found and promoted in PawnSet, then that process is notified of its promotion, by swapping its corresponding apply array element's value from REG to PRO using a `CAS` operation in line 70.

Recall that, while executing a `lock()` method call a process may receive a signal to abort. Suppose a process p receives a signal to abort while executing a $\texttt{lock}_i\texttt{()}$ method call. If process p is busy-waiting in lines 2, 7 or 14, then p stops executing $\texttt{lock}_i\texttt{()}$, and instead executes a call to $\texttt{abort}_i\texttt{()}$. If p is poised to execute any line 16 or 17 then it completes its call to $\texttt{lock}_i\texttt{()}$. If p is poised to execute any other line then it continues executing $\texttt{lock}_i\texttt{()}$ until it begins to busy-wait in lines 2, 7 or 14, at which point it stops and calls $\texttt{abort}_i\texttt{()}$. If p does not begin to busy-wait in lines 2, 7 or 14 then it completes its $\texttt{lock}_i\texttt{()}$ call.

The `abort()` method. Suppose p executes a call to $\texttt{abort}_i\texttt{()}$. Process p first determines whether it quit $\texttt{lock}_i\texttt{()}$ while busy-waiting on apply[i] in line 2, and if so, p returns $\perp$ in line 18. If not, then p changes apply[i] to the value PRO in line 19, to prevent itself from getting collected in future collect actions. In line 20, process p determines whether it quit $\texttt{lock}_i\texttt{()}$ while busy-waiting on apply[i] in line 7 or 14, or while busy-waiting on Sync1 in line 14. If p quit while busy-waiting on apply[i] then clearly it is a pawn process, and if it quit while busy-waiting on Sync1 then it is a queen process.

If process p determines that it is a pawn then it attempts to remove itself from PawnSet by executing a PawnSet.`abort`(i, s) operation in line 21, where s was the sequence number returned in line 1. If p has not been promoted yet, then the operation succeeds and p's corresponding element in PawnSet is changed to a value $\langle \mathsf{ABORT}, s \rangle$, thus making sure that p can not be collected or promoted anymore. If p has already been promoted then the operation fails and p now knows that it is has been promoted, and assumes the role of a promoted pawn, and in line 22, p writes PAWN_P into Role[i] and returns the special value ∞ in line 23.

If process p determines that it is $\mathsf{queen_L}$ then it first attempts to swap a special value ∞ into

$\mathsf{Sync1}$ in line 26 by executing a $\mathsf{Sync1}.\mathtt{CAS}(\perp,\infty)$ operation to indicate its desire to abort. If p is successful then p has determined that it is the first (among $\mathsf{king}_{\mathsf{L}}$ and itself) to exit, and therefore p performs the collect action by calling $\mathtt{doCollect}_i\mathtt{()}$ in line 29. Process p then makes a call to $\mathtt{helpRelease}_i\mathtt{()}$ in line 30 to help release lock L by synchronizing with $\mathsf{king}_{\mathsf{L}}$.

If p was unsuccessful at swapping value ∞ into $\mathsf{Sync1}$ then it knows the $\mathsf{king}_{\mathsf{L}}$ is executing `release()`, and $\mathsf{king}_{\mathsf{L}}$ will eventually perform the collect action. Then p has determined that it is the current owner of lock L, and returns the integer value stored in $\mathsf{Sync1}$ in line 27.

Process p executes line 32 only if p successfully aborted earlier in its `abort()` call, and thus it deregisters itself from lock L by swapping $\langle\perp,\perp\rangle$ into apply[i]. Finally, in line 33, p returns $\perp$ to indicate a successful abort (i.e., a failed `lock()` call).

D.2 Analysis and Proofs of Correctness

Let H be an arbitrary history of an algorithm that accesses an instance, L, of object $\mathsf{ALockArray}_n$, where the following safety conditions hold.

Condition D.1. *(a) No two* $\mathtt{lock}_i\mathtt{()}$ *calls are executed concurrently for the same* i*, where* $i \in \{0,\ldots,n-1\}$.

(b) If a process p *executes a successful* $\mathtt{lock}_i\mathtt{()}$ *call, then some process* q *eventually executes a* $\mathtt{release}_i\mathtt{()}$ *call where the invocation of* $\mathtt{release}_i\mathtt{()}$ *happens after the response of* $\mathtt{lock}_i\mathtt{()}$ *(assuming the scheduler is such that* q *continues to make progress until its* $\mathtt{release}_i\mathtt{()}$ *call happens).*

(c) For every $\mathtt{release}_i\mathtt{()}$ *call, there must exist a unique successful* $\mathtt{lock}_i\mathtt{()}$ *call that completed before the invocation of the* $\mathtt{release}_i\mathtt{()}$ *call.*

Then the following claims hold for history H.

Lemma D.2. *Methods* $\mathtt{release}_i\mathtt{(}j\mathtt{)}$, $\mathtt{abort}_i\mathtt{()}$, $\mathtt{helpRelease}_i\mathtt{()}$, $\mathtt{doCollect}_i\mathtt{()}$, $\mathtt{doPromote}_i\mathtt{()}$ *are wait-free.*

Proof. Follows from an inspection of these methods. □

Claim D.3. *No two* $\mathtt{release}_i\mathtt{()}$ *calls where a shared memory step is pending, are executed concurrently for the same* i*, where* $i \in \{0,\ldots,n-1\}$.

Proof. Assume for the purpose of a contradiction that two processes are executing a call to $\mathtt{release}_i\mathtt{()}$ concurrently for the first time at time t. Then from Condition D.1(b)-(c), it follows that two successful calls to $\mathtt{lock}_i\mathtt{()}$ were executed before t. From condition D.1(a) it follows that the two successful $\mathtt{lock}_i\mathtt{()}$ calls did not overlap. Consider the first successful $\mathtt{lock}_i\mathtt{()}$ call executed by some process p. Since the $\mathtt{lock}_i\mathtt{()}$ call returned a non-$\perp$ value, the method did not return from line 18. Then p did not abort while busy-waiting in line 2, and thus $\mathsf{apply}[i]$ was set to a non-$\langle\perp,\perp\rangle$ value in line 2 during the first $\mathtt{lock}_i\mathtt{()}$ call. Let t' be the point in time when $\mathsf{apply}[i]$ was set to a non-$\langle\perp,\perp\rangle$ value in line 2. We now show that the $\mathsf{apply}[i] \neq \langle\perp,\perp\rangle$ in the duration between $[t',t]$. Suppose not, i.e., some process resets $\mathsf{apply}[i]$ to $\langle\perp,\perp\rangle$ during $[t',t]$. Now, $\mathsf{apply}[i]$ is reset to a $\langle\perp,\perp\rangle$ value only in line 32 during $\mathtt{abort}_i\mathtt{()}$ or in line 49 during $\mathtt{release}_i\mathtt{()}$.

Case a - $\mathsf{apply}[i]$ reset to $\langle\perp,\perp\rangle$ in line 49 during $\mathtt{release}_i\mathtt{()}$. Then the last shared memory step of the $\mathtt{release}_i\mathtt{()}$ has been executed, and the call has ended for the purposes of the claim. Then the two $\mathtt{release}_i\mathtt{()}$ calls are not concurrent at t, a contradiction.

Case b - $\mathsf{apply}[i]$ reset to $\langle\bot,\bot\rangle$ in line 32 during $\mathtt{abort}_i\mathtt{()}$. Since the two $\mathtt{lock}_i\mathtt{()}$ calls are not concurrent it follows that $\mathsf{apply}[i] \neq \langle\bot,\bot\rangle$ at the end of the first $\mathtt{lock}_i\mathtt{()}$ call, and thus $\mathsf{apply}[i]$ is reset to $\langle\bot,\bot\rangle$ in line 32 during the second successful $\mathtt{lock}_i\mathtt{()}$ call. Now consider the second successful $\mathtt{lock}_i\mathtt{()}$ call executed by some process q. Then q would repeatedly fail the $\mathsf{apply}[i].\mathtt{CAS}(\langle\bot,\bot\rangle,\cdot)$ operation of line 2, and the only way q's $\mathtt{lock}_i\mathtt{()}$ call could finish, is if q aborts the busy-wait loop of line 2. In which case q executes $\mathtt{abort}_i\mathtt{()}$, and satisfies the if-condition of line 18 and return $\bot$ in line 18. Then the second $\mathtt{lock}_i\mathtt{()}$ does not reset $\mathsf{apply}[i]$ in line 32 during $\mathtt{abort}_i\mathtt{()}$ – a contradiction.

Since $\mathsf{apply}[i] \neq \langle\bot,\bot\rangle$ throughout $[t',t]$, it then follows from the same argument of **Case b**, that the second $\mathtt{lock}_i\mathtt{()}$ call is unsuccessful, and thus a contradiction. □

From Claim D.3 and Condition D.1(a) it follows that no two calls to $\mathtt{lock}_p\mathtt{()}$ or $\mathtt{release}_p\mathtt{()}$ are executed concurrently for the same p, where $p \in \{0,\ldots,n-1\}$. Then we can label the process executing a $\mathtt{lock}_p\mathtt{()}$ or $\mathtt{release}_p\mathtt{()}$ call, simply p, without loss of generality. We do so to make the rest of the proofs easier to follow.

Helpful claims based on variable usage.

Claim D.4. *(a)* $\mathsf{Role}[p]$ *is changed by process q, only if $q = p$.*

(b) $\mathsf{Role}[p]$ *is unchanged during* $\mathtt{release}_p\mathtt{()}$*.*

(c) $\mathsf{Role}[p]$ *can be set to value* KING*,* QUEEN *or* PAWN *only when p executes line 5 during* $\mathtt{lock}_p\mathtt{()}$*.*

(d) $\mathsf{Role}[p]$ *is set to value* PAWN_P *only when p executes line 9 during* $\mathtt{lock}_p\mathtt{()}$ *or when p executes line 22 during* $\mathtt{abort}_p\mathtt{()}$*.*

Proof. All claims follow from an inspection of the code. □

Claim D.5. *(a) The only operations on* PawnSet *are* $\mathtt{collect}(A)$*,* $\mathtt{promote()}$*,* $\mathtt{remove}(i)$*,* $\mathtt{remove}(j)$*,* $\mathtt{abort}(k,s)$ *and* $\mathtt{reset()}$ *(in lines 55, 65, 64, 61, 21 and 67, respectively) where A is a vector with values in $\{\bot\}\cup\mathbb{N}$, and $i,j,k \in \{0,1,\ldots,n-1\}$, and $s \in \mathbb{N}$.*

(b) The i-th entry of PawnSet *can be changed to $\langle \mathsf{REG}, s\rangle = \langle 1, s\rangle$, where $s \in \mathbb{N}$, only when a process executes a* $\mathsf{PawnSet}.\mathtt{collect}(A)$ *operation in line 55 where $A[i] = s$.*

(c) The i-th entry of PawnSet *can be changed to $\langle \mathsf{PRO}, s\rangle = \langle 2, s\rangle$, where $s \in \mathbb{N}$, only when a process executes a* $\mathsf{PawnSet}.\mathtt{promote()}$ *operation in line 65.*

(d) The i-th entry of PawnSet *can be changed to $\langle \mathsf{ABORT}, s\rangle = \langle 3, s\rangle$, where $s \in \mathbb{N}$, only when a process executes a* $\mathsf{PawnSet}.\mathtt{remove}(i)$*,* $\mathsf{PawnSet}.\mathtt{remove}(j)$ *or* $\mathsf{PawnSet}.\mathtt{abort}(k,s)$ *operation in lines 64, 61 or 21, respectively.*

Proof. Part (a) follows from an inspection of the code. Parts (b), (c) and (d) follow from Part (a) and the semantics of type $\mathsf{AbortableProArray}_n$. □

Claim D.6. *Let $s \in \mathbb{N}$.*

(a) $\mathsf{apply}[p]$ *is changed from $\langle\bot,\bot\rangle$ to a non-$\langle\bot,s\rangle$ value only when process p executes a successful* $\mathsf{apply}[p].\mathtt{CAS}(\langle\bot,\bot\rangle,\langle\mathsf{REG},s\rangle)$ *operation in line 2.*

(b) $\mathsf{apply}[p]$ *is changed to value $\langle\mathsf{REG},s\rangle$ only when process p executes a successful* $\mathsf{apply}[p].\mathtt{CAS}(\langle\bot,\bot\rangle,\langle\mathsf{REG},s\rangle)$ *operation in line 2.*

(c) $\mathsf{apply}[p]$ *is changed to a* $\langle\perp,\perp\rangle$ *value only when* p *executes a successful* $\mathsf{apply}[p].\mathtt{CAS}(\langle\mathsf{PRO},s\rangle,\langle\perp,\perp\rangle)$ *operation either in line 32 or line 49.*

Proof. Parts (a), (b) and (c) follow from an inspection of the code. □

Helpful Notations and Definitions. We now establish a notion of time for our history H. Let the i-th step in H occur at time i. Then every point in time during H is in $\mathbb{N}$.

Let t_p^i denote the point in time immediately after process p has finished executing line i, and no process has taken a step since p has executed the last operation of line i (This operation can be the response of a method call made in line i). Since some private methods are invoked from more than one place in the code, the point in time t_p^i, where i is a line in the method, does not refer to a unique point in time in history H. In those cases we make sure that it is clear from the context of the discussion, which point t_p^i refers to. Let t_p^{i-} denote the point in time when p is poised to execute line i, and no other process takes steps before p executes line i.

Let p be an arbitrary process and s be an arbitrary integer. We say process p *registers*, when it executes a successful $\mathsf{apply}[p].\mathtt{CAS}(\langle\perp,\perp\rangle,\langle\mathsf{REG},s\rangle)$ operation in line 16. Process p *captures* and *wins* lock L when it returns from $\mathtt{lock}_p\mathtt{()}$ with a non-$\perp$ value. Process p is said to *promote* another process q if p executes a $\mathsf{PawnSet}.\mathtt{promote()}$ operation in line 65 that returns a value $\langle q,s\rangle$, where $s\in\mathbb{N}$. A process p is said to be *promoted* at lock L, if some process q executes a $\mathsf{PawnSet}.\mathtt{promote()}$ operation that returns value $\langle p,s\rangle$, where $s\in\mathbb{N}$.

Process p is said to *hand over* lock L to process q if it executes a successful $\mathtt{CAS}$ operation $\mathsf{L.Sync1}.\mathtt{CAS}(\perp,j)$ in line 37, where q is the process that last increased Ctr from 1 to 2. Process p is said to have *released* lock L by executing a successful $\mathsf{Ctr}.\mathtt{CAS}(1,0)$ operation in line 36, or by executing a successful $\mathsf{Ctr}.\mathtt{CAS}(2,0)$ operation in line 68. Process p either hands over, promotes a process, or releases lock L during a call to $\mathsf{L}.\mathtt{release}_p(j)$ where j is an arbitrary integer. A process *ceases to own* a lock either by releasing lock L or by promoting another process, or by handing over lock L to some other process. Process p is *deregistered* when p executes a successful $\mathsf{apply}[p].\mathtt{CAS}(\langle\mathsf{PRO},s\rangle,\langle\perp,\perp\rangle)$ operation in line 32 or 49. A process p is said to be *not registered in* $\mathsf{PawnSet}$ if the p-th entry of $\mathsf{PawnSet}$ is not value $\langle\mathsf{REG},s\rangle$, where $s\in\mathbb{N}$. The repeat-until loop starting at line 4 and ending at line 12 is called *role-loop.*

In some of the proofs we use represent an execution using diagrams, and the legend for the symbols used in the diagrams is given in Figure 7.

Releasers of lock and Cease-release events.

A process p becomes a *releaser* of lock L at time t when

(R1) p increases Ctr to 1 (i.e., $\mathsf{Ctr}.\mathtt{inc()}$ returns $0=\mathsf{KING}$) or 2 (i.e., $\mathsf{Ctr}.\mathtt{inc()}$ returns $1=\mathsf{QUEEN}$), or when

(R2) p is promoted at lock L by some process q .

Claim D.7. *(a)* p *executes a* $\mathsf{Ctr}.\mathtt{CAS}(1,0)$ *operation only in line 36 during* $\mathtt{release}_p(j)$.

(b) p *executes a* $\mathsf{Sync2}.\mathtt{CAS}(\perp,p)$ *operation only in line 56 during* p*'s call to* $\mathtt{helpRelease}_p\mathtt{()}$.

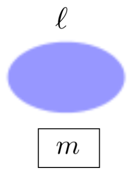
(c) p *executes a* $\mathsf{PawnSet}.\mathtt{promote()}$ *operation only in line 65 during* p*'s call to* $\mathtt{doPromote}_p\mathtt{()}$.

(d) p *executes a* $\mathsf{Ctr}.\mathtt{CAS}(2,0)$ *operation only in line 68 during* p*'s call to* $\mathtt{doPromote}_p\mathtt{()}$.

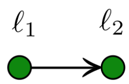
Proof. All claims follows from an inspection of the code. □

We now define the following *cease-release* events with respect to p :

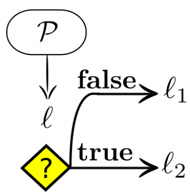
Atomic shared memory operation executed in line ℓ.



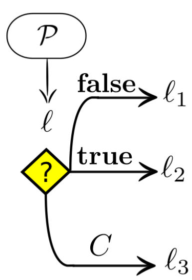
Method call m executed in line ℓ.



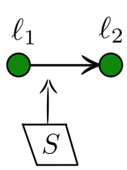
Operation in line ℓ_2 is executed only after the operation in line ℓ_1.



Predicate $\mathcal{P}$ is evaluated in line ℓ.
The next line executed is ℓ_1 if and only if $\mathcal{P}$ is true.
The next line executed is ℓ_2 if and only if $\mathcal{P}$ is false.

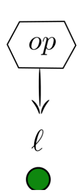


Predicate $\mathcal{P}$ is evaluated in line ℓ.
The next line executed is ℓ_1 if and only if $\mathcal{P}$ is true.
The next line executed is ℓ_2 if and only if $\mathcal{P}$ is false and condition C is false.
The next line executed is ℓ_3 if and only if $\mathcal{P}$ is false and condition C is true.



Statement S holds after line ℓ_1 is executed and before line ℓ_2 is executed.

$S \longrightarrow$ Helpful statement S to improve the readability of the figure.



Operation op executed in line ℓ

Figure 7: Legend for Figures 8 to 16

ϕ_p: p executes a successful $\mathsf{Ctr.CAS}(1,0)$ (at t_p^{36} during $\mathtt{release}_p(j)$).

τ_p: p executes a successful $\mathsf{Sync2.CAS}(\perp,p)$ (at t_p^{56} during $\mathtt{helpRelease}_p()$).

π_p: p promotes some process q (at t_p^{65} during $\mathtt{doPromote}_p()$).

θ_p: p executes an operation $\mathsf{Ctr.CAS}(2,0)$ (at t_p^{68} during $\mathtt{doPromote}_p()$).

Process p *ceases* to be a releaser of lock L when one of p's cease-release events occurs. We say process p is a releaser of lock L at any point after it becomes a releaser and before it ceases to be a releaser.

Claim D.8. *(a) Method* $\mathtt{doCollect}_p()$ *is called only by process p in lines 29 and 38.*

(b) Method $\mathtt{helpRelease}_p()$ *is called only by process p in lines 39, 43 and 30.*

(c) Method $\mathtt{doPromote}_p()$ *is called only by process p in line 46 and in line 62 (during* $\mathtt{helpRelease}_p()$*).*

(d) If cease-release event ϕ_p *occurs then p is executing* $\mathtt{release}_p(j)$.

(e) If cease-release event τ_p *occurs then p is executing* $\mathtt{helpRelease}_p()$.

(f) If cease-release event π_p *or* θ_p *occurs then p is executing* $\mathtt{helpRelease}_p()$ *or* $\mathtt{doPromote}_p()$.

Proof. Parts (a), (b) and (c) follow from an inspection of the code. By definition, cease-release event ϕ_p occurs when p executes a successful $\mathsf{Ctr.CAS}(1,0)$ operation in line 36 during $\mathtt{release}_p(j)$, and thus (d) follows immediately. By definition, cease-release event τ_p occurs when p executes a successful $\mathsf{Sync2.CAS}(\perp,p)$ in line 56 during $\mathtt{helpRelease}_p()$, and thus (e) follows immediately. By definition, cease-release event π_p occurs only when p executes a $\mathsf{PawnSet.promote()}$ operation that returns a non-$\langle\perp,\perp\rangle$ value in line 65, and cease-release event θ_p occurs only when p executes a $\mathsf{Ctr.CAS}(2,0)$ operation in line 68. Then if cease-release event π_p or θ_p occurs then p is executing $\mathtt{doPromote}_p()$. From (c), p could also call $\mathtt{doPromote}_p()$ from line 62 during $\mathtt{helpRelease}_p()$. Then if cease-release event π_p or θ_p occurs then p is executing $\mathtt{doPromote}_p()$ or $\mathtt{helpRelease}_p()$. Thus, (f) holds. ☐

Claim D.9. *Consider p's k-th passage, where* $k \in \mathbb{N}$*. Note that* $s = k$*. If* $\mathsf{Role}[p] = \mathsf{PAWN\_P}$ *at some point in time t during p's call to* $\mathtt{lock}_p()$*, then some process q promoted p at* t_q^{65} *and p became releaser of* L *by condition (R2) at* $t_q^{65} < t$.

Proof. From Claim D.4(d), p changes $\mathsf{Role}[p]$ to $\mathsf{PAWN\_P}$ only in line 9 or line 22.

Case a - p changed $\mathsf{Role}[p]$ to $\mathsf{PAWN\_P}$ in line 22: Then p's call to $\mathsf{PawnSet.abort}(p,s)$ returned $\mathtt{false}$ in line 21. From the semantics of the $\mathsf{AbortableProArray}_n$ object, it follows that the p-th entry of $\mathsf{PawnSet}$ was set to value $\langle \mathsf{PRO}, s\rangle = \langle 2, s\rangle$. From Claim D.5(c), the p-th entry of $\mathsf{PawnSet}$ is set to value $\langle \mathsf{PRO}, s\rangle$ only when a $\mathsf{PawnSet.promote()}$ operation returns $\langle p, s\rangle$ in line 65. Then some process q promoted p at t_q^{65} and p became a releaser of L by condition (R2) at $t_q^{65} < t$.

Case b - p changed $\mathsf{Role}[p]$ to $\mathsf{PAWN\_P}$ in line 9: Then p broke out of the spin loop of line 2, and thus $\mathsf{apply}[p] = \langle \mathsf{REG}, s\rangle \neq \langle \mathsf{PRO}, s\rangle$ at t_p^2. Since p satisfied the if-condition of line 8, it follows that $\mathsf{apply}[p] = \langle \mathsf{PRO}, s\rangle$ at t_p^8. Since p does not change $\mathsf{apply}[p]$ to value $\langle \mathsf{PRO}, s\rangle$ during $[t_p^2, t_p^8]$ it follows that some other process changed $\mathsf{apply}[p]$ to value $\langle \mathsf{PRO}, s\rangle$. Now, $\mathsf{apply}[p]$ is changed to value $\langle \mathsf{PRO}, s\rangle$ by some other process (say q) only in line 70 and thus, from the code structure, q also executed a $\mathsf{PawnSet.promote()}$ operation that returned $\langle p, s\rangle$ in line 65. Then q promoted p at t_q^{65} and p became a releaser of L by condition (R2) at $t_q^{65} < t$. ☐

Claim D.10. *Consider p's k-th passage, where $k \in \mathbb{N}$. If $t \in \{ [t_p^{18-}, t_p^{33}], [t_p^{37-}, t_p^{39}], [t_p^{43-}, t_p^{43}], [t_p^{46-}, t_p^{46}] \}$, then cease-release event ϕ_p does not occur before time t.*

Proof. By definition, cease-release event ϕ_p occurs when p executes a successful $\mathsf{Ctr}.\texttt{CAS}(1,0)$ operation in line 36. From Claim D.8(d) cease-release event ϕ_p occurs only during $\texttt{release}_p(j)$.

Case a - $t \in [t_p^{18-}, t_p^{33}]$: Then p is executing $\texttt{abort}_p\texttt{()}$ and has not yet executed a call to $\texttt{release}_p\texttt{()}$. Since cease-release event ϕ_p can occur only during $\texttt{release}_p\texttt{()}$, cease-release event ϕ_p did not occur before time t.

Case b - $t \in [t_p^{37-}, t_p^{39}]$: Then p must have failed the if-condition of line 36, and thus p executed an unsuccessful $\mathsf{Ctr}.\texttt{CAS}(1,0)$ operation in line 36, and cease-release event ϕ_p did not occur before time t.

Case c - $t \in \{ [t_p^{43-}, t_p^{43}], [t_p^{46-}, t_p^{46}] \}$: From Claim D.11, $\mathsf{Role}[p] \in \{\mathsf{QUEEN}, \mathsf{PAWN\_P}\}$ at t. Since $\mathsf{Role}[p]$ is unchanged during $\texttt{release}_p\texttt{()}$ (Claim D.4(b)), it follows that $\mathsf{Role}[p] \neq \mathsf{KING}$ at t_p^{35-}. Then p fails the if-condition of line 35, and does not execute line 36 and thus cease-release event ϕ_p did not occur before time t. $\square$

The proof of the following claim has been moved to Appendix F since the proof is long and straight forward.

Claim D.11. *The value of $\mathsf{Role}[p]$ at various points in time during p's k-th passage, where $k \in \mathbb{N}$, is as follows.*

Time	*Value of* $\mathsf{Role}[p]$
t_p^5	$\{\bot, \mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN}\}$
$[t_p^7, t_p^8]$	PAWN
t_p^9	$\mathsf{PAWN\_P}$
t_p^{13-}	$\{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$
t_p^{14}	QUEEN
$[t_p^{16}, t_p^{17}]$	$\{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$
$[t_p^{19}, t_p^{20-}]$	$\{\mathsf{QUEEN}, \mathsf{PAWN}\}$
t_p^{21}	PAWN
$[t_p^{22}, t_p^{23}]$	$\mathsf{PAWN\_P}$
$[t_p^{26-}, t_p^{30}]$	QUEEN

Time	*Value of* $\mathsf{Role}[p]$
$[t_p^{34-}, t_p^{35-}]$	$\{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$
$[t_p^{36-}, t_p^{39}]$	KING
t_p^{43-}	QUEEN
t_p^{46-}	$\mathsf{PAWN\_P}$
$[t_p^{49-}, t_p^{50}]$	$\{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$
$[t_p^{51-}, t_p^{55}]$	$\{\mathsf{KING}, \mathsf{QUEEN}\}$
$[t_p^{56-}, t_p^{63}]$	$\{\mathsf{KING}, \mathsf{QUEEN}\}$
$[t_p^{65-}, t_p^{71}]$	$\{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$

Claim D.12. *Consider p's k-th passage, where $k \in \mathbb{N}$.*

(a) If process p calls $\texttt{helpRelease}_p\texttt{()}$ or $\texttt{doPromote}_p\texttt{()}$ during $\texttt{abort}_p\texttt{()}$ then it does not call $\texttt{release}_p(j)$.

(b) Process p calls $\texttt{helpRelease}_p\texttt{()}$ at most once.

(c) Process p calls $\texttt{doPromote}_p\texttt{()}$ at most once.

Proof. **Proof of (a):** The following observations follow from an inspection of the code. If p executes $\texttt{doPromote}_p\texttt{()}$ during $\texttt{abort}_p\texttt{()}$, then it does so during a call to $\texttt{helpRelease}_p\texttt{()}$ in line 62. If p executes $\texttt{helpRelease}_p\texttt{()}$ during $\texttt{abort}_p\texttt{()}$, then it does so by executing line 30. Then p calls $\texttt{helpRelease}_p\texttt{()}$ or $\texttt{doPromote}_p\texttt{()}$ during $\texttt{abort}_p\texttt{()}$ in line 30 and goes on to return value $\bot$ in line 33. Then p's call to $\texttt{lock}_p\texttt{()}$ returns value $\bot$ and p does not call $\texttt{release}_p\texttt{()}$ (follows from conditions b and d).

Proof of (b): From Part (a), if $\texttt{helpRelease}_p\texttt{()}$ is executed during $\texttt{abort}_p\texttt{()}$ then $\texttt{release}_p\texttt{(}j\texttt{)}$ is not executed. Then to prove our claim we need to show that $\texttt{helpRelease}_p\texttt{()}$ is called at most once during $\texttt{abort}_p\texttt{()}$ and $\texttt{release}_p\texttt{(}j\texttt{)}$, respectively. From Claim D.8(b), method $\texttt{helpRelease}_p\texttt{()}$ is called by p only in lines 39, 43 and 30. Since $\texttt{helpRelease}_p\texttt{()}$ is called only once during $\texttt{abort}_p\texttt{()}$ (specifically in line 30), it follows immediately that p executes $\texttt{helpRelease}_p\texttt{()}$ at most once during $\texttt{abort}_p\texttt{()}$. From Claim D.11, $\mathsf{Role}[p] \in \{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$ at t_p^{34-}. Since $\mathsf{Role}[p]$ is unchanged during $\texttt{release}_p\texttt{()}$ (Claim D.4(b)), it follows that p satisfies exactly one of the if-conditions of lines 35, 42 and 45, and thus p does not execute both lines 39 and 43. Then p executes $\texttt{helpRelease}_p\texttt{()}$ at most once during $\texttt{release}_p\texttt{(}j\texttt{)}$.

Proof of (c): From Part (a), if $\texttt{doPromote}_p\texttt{()}$ is executed during $\texttt{abort}_p\texttt{()}$ then $\texttt{release}_p\texttt{(}j\texttt{)}$ is not executed. Then to prove our claim we need to show that $\texttt{doPromote}_p\texttt{()}$ is called at most once during $\texttt{abort}_p\texttt{()}$ and $\texttt{release}_p\texttt{(}j\texttt{)}$, respectively. From Claim D.8(c), method $\texttt{doPromote}_p\texttt{()}$ is called by p only in line 46 and in line 62 (during $\texttt{helpRelease}_p\texttt{()}$).

Case a - p called $\texttt{doPromote}_p\texttt{()}$ in line 62 (during $\texttt{helpRelease}_p\texttt{()}$). Then p is executing $\texttt{helpRelease}_p\texttt{()}$. From Claim D.8(b), method $\texttt{helpRelease}_p\texttt{()}$ is called by p only in lines 39, 43 and 30. Then p called $\texttt{helpRelease}_p\texttt{()}$ either in line 39, 43 or 30

Case a(i) - p called $\texttt{helpRelease}_p\texttt{()}$ in line 39 or 43 (during $\texttt{release}_p\texttt{(}j\texttt{)}$). Then p is executing $\texttt{release}_p\texttt{()}$, and since p called $\texttt{helpRelease}_p\texttt{()}$ in lines 39 or 43, p satisfied the if-conditions of lines 35 or 42, and thus $\mathsf{Role}[p] = \mathsf{KING}$ at t_p^{35-} or $\mathsf{Role}[p] = \mathsf{QUEEN}$ at t_p^{42-}, respectively. Since $\mathsf{Role}[p]$ is unchanged during $\texttt{release}_p\texttt{()}$ (Claim D.4(b)), it follows that $\mathsf{Role}[p] \in \{\mathsf{KING}, \mathsf{QUEEN}\}$ during $\texttt{release}_p\texttt{()}$. Then p fails the if-condition of line 45 and does not execute $\texttt{doPromote}_p\texttt{()}$ in line 46. Hence, p executes $\texttt{doPromote}_p\texttt{()}$ at most once during $\texttt{release}_p\texttt{()}$.

Case a(ii) - p called $\texttt{helpRelease}_p\texttt{()}$ in line 30. Then p is executing $\texttt{abort}_p\texttt{()}$ and it goes on to return value $\perp$ in line 33. Then p's call to $\texttt{lock}_p\texttt{()}$ returns value $\perp$ and p does not call $\texttt{release}_p\texttt{(}j\texttt{)}$ (follows from conditions b and d). Hence, p executes $\texttt{doPromote}_p\texttt{()}$ at most once during $\texttt{abort}_p\texttt{()}$.

Case b - p called $\texttt{doPromote}_p\texttt{()}$ in line 46. Then p is executing $\texttt{helpRelease}_p\texttt{()}$ and p satisfied the if-condition of lines 45, and thus $\mathsf{Role}[p] = \mathsf{PAWN\_P}$ at t_p^{45-}. Since $\mathsf{Role}[p]$ is unchanged during $\texttt{release}_p\texttt{()}$ (Claim D.4(b)), it follows that $\mathsf{Role}[p] = \mathsf{PAWN\_P}$ during $\texttt{release}_p\texttt{()}$. Then p failed the if-condition of lines 35 and 42 and p did not execute $\texttt{helpRelease}_p\texttt{()}$ in lines 39 and 43. Hence, p executes $\texttt{doPromote}_p\texttt{()}$ at most once during $\texttt{release}_p\texttt{()}$. ☐

Claim D.13. *Consider p's k-th passage, where $k \in \mathbb{N}$. Let t be a point in time at which either p is poised to execute* $\texttt{release}_p\texttt{(}j\texttt{)}$, *or* $t \in \{\ [t_p^{26-}, t_p^{29}],\ [t_p^{37-}, t_p^{38}],\ [t_p^{51-}, t_p^{55}]\ ,\ t_p^{56-}, [t_p^{57-}, t_p^{62-}],\ t_p^{65-},\ [t_p^{67-}, t_p^{68-}]\ \}$. *Then*

(a) none of p's cease-release events have occurred before time t, and

(b) p is a releaser of lock L at time t

Proof. **Proof of (a):** First note that if $t \in [t_p^{51-}, t_p^{55}]$ then p is executing $\texttt{doCollect()}$. From Claim D.8(a), p calls $\texttt{doCollect()}$ only in lines 29 and 38. Then if $t \in [t_p^{51-}, t_p^{55}]$ then $t \in [t_p^{29-}, t_p^{29}]$ or $t \in [t_p^{38-}, t_p^{38}]$. Therefore, assume now $t \in [t_p^{26-}, t_p^{29}]$ or $t \in [t_p^{37-}, t_p^{38}]$.

Case a - $t \in \left\{[t_p^{26-}, t_p^{29}], [t_p^{37-}, t_p^{38}]\right\}$: If $t \in [t_p^{26-}, t_p^{29}]$ then from a code inspection, p is executing $\texttt{abort}_p\texttt{()}$ and p did not execute a call to $\texttt{doPromote}_p\texttt{()}$ or $\texttt{helpRelease}_p\texttt{()}$ before time t. If $t \in [t_p^{37-}, t_p^{38}]$ then p is executing $\texttt{release}_p\texttt{(}j\texttt{)}$ and then from a code inspection and Claim D.12(a) it follows that p did not execute a call to $\texttt{doPromote}_p\texttt{()}$ or $\texttt{helpRelease}_p\texttt{()}$ before time t. Then from Claims D.8(e) and D.8(f) it follows that events τ_p, π_p and θ_p did not occur before time t. Since $t \in [t_p^{26-}, t_p^{29}]$ or $t \in [t_p^{37-}, t_p^{38}]$, it follows from Claim D.10 that cease-release event ϕ_p did not occur before time t.

Case b - $t \in \{\ t_p^{56-},\ [t_p^{57-}, t_p^{62-}]\ \}$: Then p is executing $\texttt{helpRelease}_p\texttt{()}$. Then from Claim D.8(b) it follows that p is executing a call to $\texttt{helpRelease}_p\texttt{()}$ in line 39, 43 or 30. Then from Claim D.10 it follows that cease-release event ϕ_p did not occur before time t. From Claim D.12(b), it follows that this is p's only call to $\texttt{helpRelease}_p\texttt{()}$. From Claim D.8(c), p calls $\texttt{doPromote}_p\texttt{()}$ only in line 46 and in line 62 (during $\texttt{helpRelease}_p\texttt{()}$). Since p has not yet executed line 46 and this is the only call to $\texttt{helpRelease}_p\texttt{()}$, p has not called $\texttt{doPromote}_p\texttt{()}$ before time t. Then from Claim D.8(f) it follows that events π_p and θ_p did not occur before time t. By definition, cease-release event τ_p occurs when p executes a successful $\mathsf{Sync2}.\texttt{CAS(}\bot, p\texttt{)}$ in line 56. If $t = t_p^{56-}$, then clearly cease-release event τ_p did not occur before time t. If $t \in [t_p^{57-}, t_p^{62-}]$, then p satisfied the if-condition of line 56, and thus p executed an unsuccessful $\mathsf{Sync2}.\texttt{CAS(}\bot, p\texttt{)}$ operation in line 56, and thus cease-release event τ_p did not occur before time t.

Case c - $t \in \{\ t_p^{65-},\ [t_p^{67-}, t_p^{68-}]\ \}$: Then p is executing $\texttt{doPromote}_p\texttt{()}$. From Claim D.12(c), it follows that this is the only call to $\texttt{doPromote}_p\texttt{()}$. By definition, cease-release event θ_p occurs only when p executes a $\mathsf{Ctr}.\texttt{CAS(}2, 0\texttt{)}$ operation in line 68 of $\texttt{doPromote}_p\texttt{()}$. Event θ_p did not occur before time t since $t < t_p^{68}$ and this is p's only call to $\texttt{doPromote}_p\texttt{()}$. By definition, cease-release event π_p occurs only when p executes a $\mathsf{PawnSet}.\texttt{promote()}$ operation that returns a non-$\langle\bot,\bot\rangle$ value in line 65 of $\texttt{doPromote}_p\texttt{()}$. If $t = t_p^{65-}$, then cease-release event π_p did not occur before time t since $t_p^{65-} < t_p^{65}$ (and since this is p's only call to $\texttt{doPromote}_p\texttt{()}$). If $t \in [t_p^{67-}, t_p^{68-}]$, then p satisfied the if-condition of line 65, and thus p's $\mathsf{PawnSet}.\texttt{promote()}$ operation returned value $\langle\bot,\bot\rangle$, and thus cease-release event π_p did not occur before time t. Since p calls $\texttt{doPromote}_p\texttt{()}$ only in line 46 and line 62 (during $\texttt{helpRelease}_p\texttt{()}$), p is executing line 46, 39, 43 or 30. Then from Claim D.10 it follows that cease-release event ϕ_p did not occur before time t.

We now show that cease-release event τ_p did not occur before time t thus completing the proof.

Subcase c(i) - p called $\texttt{doPromote}_p\texttt{()}$ during $\texttt{helpRelease}_p\texttt{()}$: Then p satisfied the if-condition of line 56, and thus p executed an unsuccessful $\mathsf{Sync2}.\texttt{CAS(}\bot, p\texttt{)}$ operation in line 56, and cease-release event τ_p did not occur before time t.

Subcase c(ii) - p called $\texttt{doPromote}_p\texttt{()}$ in line 46: From Claim D.11, $\mathsf{Role}[p] \in \mathsf{PAWN\_P}$ at t_p^{46-}. Since $\mathsf{Role}[p]$ is unchanged during $\texttt{release}_p\texttt{()}$ (Claim D.4(b)), it follows that $\mathsf{Role}[p] = \mathsf{PAWN\_P}$ at t_p^{35-} and t_p^{42-}. Then p fails the if-conditions of lines 35 and 42, and does not execute a call to $\texttt{helpRelease}_p\texttt{()}$ before time t. Then from Claim D.8(e) it follows that cease-release event τ_p did not occur before time t.

Proof of (b): From Part (a), p does not cease to be releaser of L before t. Therefore, to prove our claim we need to show that p becomes a releaser of L at some point $t' < t$. We first show that $\mathsf{Role}[p] \in \{\ \mathsf{KING},\ \mathsf{QUEEN},\ \mathsf{PAWN\_P}\ \}$ at time t. Let t' be the point when p is poised to execute $\texttt{release}_p\texttt{(}j\texttt{)}$. From the inspection of the various points in time chosen for t (including t_p^{34-}, but excluding t') and the table in Claim D.11, it follows that $\mathsf{Role}[p] \in \{\ \mathsf{KING},\ \mathsf{QUEEN},\ \mathsf{PAWN\_P}\ \}$ at time t (including t_p^{34-}, but excluding t'). Clearly $\mathsf{Role}[p]$ is unchanged during $[t', t_p^{34-}]$. Then the value of $\mathsf{Role}[p]$ at t' is the same as that at t_p^{34-}, i.e., $\mathsf{Role}[p] \in \{\ \mathsf{KING},\ \mathsf{QUEEN},\ \mathsf{PAWN\_P}\ \}$.

Case a - $\mathsf{Role}[p] \in \{\mathsf{KING}, \mathsf{QUEEN}\}$ at time t: From Claim D.4(c), $\mathsf{Role}[p]$ is set to KING or QUEEN only when p executes line 5. Then p changed $\mathsf{Role}[p]$ to KING or QUEEN at t_p^5, and thus p became a releaser of lock L by condition (R1) at $t_p^5 = t' < t$.

Case b - $\mathsf{Role}[p] = \mathsf{PAWN\_P}$ at time t: From Claim D.9, it follows that some process q

promoted p at t_q^{65} and p became a releaser of L by condition (R2) at $t_q^{65} = t' < t$. $\square$

Claim D.14. *Consider p's k-th passage, where $k \in \mathbb{N}$. If any of process p's cease-release events occurs at time t then p ceases to be the releaser of lock L at time t.*

Proof. To prove our claim we need to show that p is a releaser of L immediately before time t, since by definition p ceases to be a releaser of L when any of p's cease-release events occurs. By definition, cease-release event ϕ_p occurs when p executes a successful $\mathsf{Ctr}.\mathtt{CAS}(1,0)$ operation in line 36, cease-release event τ_p occurs when p executes a successful $\mathsf{Sync2}.\mathtt{CAS}(\perp, p)$ in line 56, cease-release event π_p occurs only when p executes a $\mathsf{PawnSet}.\mathtt{promote()}$ operation that returns a non-$\langle\perp,\perp\rangle$ value in line 65, cease-release event θ_p occurs only when p executes a $\mathsf{Ctr}.\mathtt{CAS}(2,0)$ operation in line 68. From Claim D.13(b), p is a releaser of L at t_p^{36-}, t_p^{56-}, t_p^{65-} and t_p^{68-}. Hence, the claim follows. $\square$

We say a process has *write-access* to objects $\mathsf{Sync1}$ and $\mathsf{Sync2}$, respectively, if the process can write a value to $\mathsf{Sync1}$ and $\mathsf{Sync2}$, respectively. We say a process has *registration-access* to object $\mathsf{PawnSet}$, if the process can execute an operation on $\mathsf{PawnSet}$ that can write values in $\{\langle a,b\rangle | a \in \{0,1,2\} = \{0, \mathsf{REG}, \mathsf{PRO}\}, b \in \mathbb{N}\}$ to some entry of $\mathsf{PawnSet}$. We say a process has *deregistration-access* to object $\mathsf{PawnSet}$, if the process can execute an operation on $\mathsf{PawnSet}$ that can write value $\langle \mathsf{ABORT}, s\rangle = \langle 3, s\rangle$, where $s \in \mathbb{N}$, to some entry of $\mathsf{PawnSet}$. Object $\mathsf{PawnSet}$ is said to be *candidate-empty* if no entry of $\mathsf{PawnSet}$ has value $\langle \mathsf{REG}, \cdot\rangle$ or $\langle \mathsf{PRO}, \cdot\rangle$.

Claim D.15. *Only releasers of* L *have write-access to* $\mathsf{Sync1}$, $\mathsf{Sync2}$ *and registration-access to* $\mathsf{PawnSet}$.

Proof. The following observations follow from an inspection of the code. A value can be written to $\mathsf{Sync1}$ only in lines 26, 37 and 58. A value can be written to $\mathsf{Sync2}$ only in lines 56 and 60. From the semantics of the $\mathsf{AbortableProArray}_n$ object, only operations $\mathtt{collect()}$, $\mathtt{promote()}$, and $\mathtt{reset()}$ can write values in $\{\langle a,b\rangle | a \in \{0, \mathsf{REG}, \mathsf{PRO}\} = \{0,1,2\}, b \in \mathbb{N}\}$ to $\mathsf{PawnSet}$. From Claim D.5(a), the operations $\mathtt{collect()}$, $\mathtt{promote()}$, and $\mathtt{reset()}$ are executed on $\mathsf{PawnSet}$ only in lines 55, 65, and 67, respectively.

Suppose an arbitrary process p writes a value to $\mathsf{Sync1}$ or $\mathsf{Sync2}$, or a value in $\{\langle a,b\rangle | a \in \{0,1,2\}, b \in \mathbb{N}\}$ to an entry of $\mathsf{PawnSet}$. From Claim D.13(b), p is a releaser of L at t_p^{26-}, t_p^{37-}, t_p^{58-}, t_p^{56-}, t_p^{60-}, t_p^{65-}, t_p^{67-} and t_p^{55-}. Hence, the claim follows. $\square$

Claim D.16. *The i-entry of* $\mathsf{PawnSet}$ *can be changed only by process i or a releaser of* L.

Proof. The values that can be written to $\mathsf{PawnSet}$ are in $\{\langle a,b\rangle | a \in \{0,1,2,3\}, b \in \mathbb{N}\}$. A process that can write values in $\{\langle a,b\rangle | a \in \{0,1,2\}, b \in \mathbb{N}\}$ to any entry of $\mathsf{PawnSet}$ is said to have registration-access to $\mathsf{PawnSet}$. From Claim D.15 it follows that only a releaser of L has registration-access to $\mathsf{PawnSet}$, therefore only a releaser of L can write values in $\{\langle a,b\rangle | a \in \{0,1,2\}, b \in \mathbb{N}\}$ to the i-th entry of $\mathsf{PawnSet}$. From Claim D.5(d) the value $\langle \mathsf{ABORT}, s\rangle = \langle 3, s\rangle$, where $s \in \mathbb{N}$, can be written to the i-th entry of $\mathsf{PawnSet}$ only when a process executes a $\mathtt{remove}(i)$, $\mathtt{remove}(i)$ or $\mathsf{PawnSet}.\mathtt{abort}(i,s)$ operation in line 64, 61 or 21, respectively. From Claim D.13(b), it follows that a process executing lines 64 and 61 is a releaser of L. Since a $\mathsf{PawnSet}.\mathtt{abort}(i,s)$ operation in line 21 is executed only by process i, our claim follows. $\square$

Claim D.17. $\mathsf{Sync2}$ *is changed to a non-$\perp$ value only by a releaser of* L *(say r) in line 56 which triggers the cease-release event τ_r.*

Proof. By definition, cease-release event τ_p occurs when p executes a successful Sync2.CAS($\perp, p$) in line 56. From a code inspection, Sync2 is changed to a non-$\perp$ value only when some process (say r) executes a successful Sync2.CAS($\perp, r$) operation in line 56. From Claim D.15 it follows that Sync2 is changed only by a releaser of L. Then r is a releaser of L when it changes Sync2 to a non-$\perp$ value in line 56 and doing so triggers the cease-release event τ_r. ☐

Claim D.18. *A* PawnSet.promote() *operation is executed only by a releaser of* L *(say r), and if the value returned is non-$\langle \perp, \perp \rangle$ the cease-release event π_r is triggered.*

Proof. By definition, cease-release event π_p occurs only when p executes a PawnSet.promote() operation that returns a non-$\langle \perp, \perp \rangle$ value in line 65. From a code inspection, a PawnSet.promote() operation is executed only when some process (say r) executes line 56. From Claim D.15 it follows that PawnSet is changed only by a releaser of L. Then r is a releaser of L when it executes a PawnSet.promote() operation, and if the operation returns a non-$\langle \perp, \perp \rangle$ value then the cease-release event π_r is triggered. ☐

Claim D.19. *During an execution of* doPromote$_p$() *exactly one of the events π_p and θ_p occurs.*

Proof. By definition, cease-release event π_p occurs when p executes a PawnSet.promote() operation in line 65 that returns a non-$\langle \perp, \perp \rangle$ value, and cease-release event θ_p occurs when p executes a Ctr.CAS($2, 0$) operation in line 68 during doPromote$_p$().

Case a - the PawnSet.promote() operation in line 65 returns a non-$\langle \perp, \perp \rangle$ value, and thus cease-release event π_p occurs: Then p fails the if-condition of line 66 and line 68 is not executed. Therefore, cease-release event θ_p does not occur.

Case b - the PawnSet.promote() operation in line 65 returns $\langle \perp, \perp \rangle$, and thus cease-release event π_p does not occur: Then p satisfies the if-condition of line 66, and executes a Ctr.CAS($2, 0$) operation in line 68. Hence, cease-release event θ_p occurs. ☐

Claim D.20. *During an execution of* helpRelease$_p$() *exactly one of the events τ_p, π_p and θ_p occurs.*

Proof. By Claim D.7, events π_p and θ_p can only occur during p's call to doPromote$_p$(), and cease-release event τ_p occurs when p executes a successful Sync2.CAS($\perp, p$) operation in line 56.

Case a - p executes a successful Sync2.CAS($\perp, p$) operation in line 56, and thus cease-release event τ_p occurs: Then p fails the if-condition of line 56, and returns immediately from its call to helpRelease$_i$(). Therefore, events π_p and θ_p do not occur.

Case b - p executes an unsuccessful Sync2.CAS($\perp, p$) operation in line 56, and thus cease-release event τ_p does not occur. Then p satisfies the if-condition of line 56, and calls doPromote$_p$() in line 62. From Claim D.19, exactly one of the events π_p and θ_p occurs during p's call to doPromote$_p$(). ☐

Claim D.21. *The value of* Ctr *can change only when a* Ctr.inc(), Ctr.CAS($2, 0$) *or* Ctr.CAS($1, 0$) *operation is executed in lines 5, 68 or 36.*

Proof. From the semantics of the RCAScounter$_2$ object, if Ctr is increased to value i by a Ctr.inc() operation, then its value was $i - 1$ immediately before the operation was executed. Then all claims follow from an inspection of the code. ☐

Claim D.22. *If the value of* Ctr *changes, it either increases by* 1 *or decreases to* 0*. Moreover its values are in* $\{0, 1, 2\}$*.*

Proof. From the semantics of the $\mathsf{RCAScounter}_2$ object, a $\mathsf{Ctr}.\mathtt{inc()}$ operation changes the value of Ctr from i to $i+1$ only if $i \in \{0,1\}$. From Claims D.21, the value of Ctr can change only when a $\mathsf{Ctr}.\mathtt{inc()}$, $\mathsf{Ctr}.\mathtt{CAS}(2,0)$ or $\mathsf{Ctr}.\mathtt{CAS}(1,0)$ operation is executed (in lines 5, 68 or 36). Then it follows that the values of Ctr are in$\{0,1,2\}$. It also follows that the value of Ctr either changes from 0 to 1 and back to 0, or it changes from 0 to 1 to 2 and back to 0. ☐

Ctr-Cycle Interval T. Let $T = [t_s, t_e)$ be a time interval where t_s is a point when Ctr is 0 and t_e is the next point in time when Ctr is decreased to 0. For $i \in \{0,1,2\}$ let $I_i = \{t \in T | \mathsf{Ctr} = i \text{ at } t\}$ and let time $I_i^- = \mathtt{min}(I_i)$ and time $I_i^+ = \mathtt{max}(I_i)$. From Claim D.22, it follows immediately that during T the set $I_i, i \in \{0,1,2\}$, forms an interval $[I_i^-, I_i^+]$, and $I_2 = \varnothing$ if and only if Ctr is never increased to 2 during T. Moreover, $t_s = I_0^-$ and I_0 *is immediately followed by* I_1 (i.e., $\mathtt{min}(I_1) = \mathtt{max}(I_0) + 1$). If $I_2 \neq \varnothing$ then I_2 follows immediately after I_1. The Ctr-cycle interval T ends either at time I_1^+ if $I_2 = \varnothing$, or at time I_2^+ if $I_2 \neq \varnothing$.

Then it also follows that exactly one process changes Ctr from 0 to 1 during T, and it does so at time I_1^-. Let $\mathcal{K}$ be the process that increases Ctr to 1 at time I_1^-. And if $I_2 \neq \varnothing$ then exactly one process changes Ctr from 1 to 2 during T, and it does so at time I_2^-. If $I_2 \neq \varnothing$ let $\mathcal{Q}$ be the process that increases Ctr to 2 at time I_2^-. Let $R(t)$ denote the set of processes that are the releasers of lock L at time $t \in T$.

Claim D.23. *If $R(I_0^-) = \varnothing$ and at I_0^-, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$ and $\mathsf{PawnSet}$ is candidate-empty, then the following holds:*

(a) $\forall_{t \in I_0} : R(t) = \varnothing$ *and throughout* I_0, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$ *and* $\mathsf{PawnSet}$ *is candidate-empty.*

(b) $R(I_1^-) = \{\mathcal{K}\}$ *and at time* I_1^-, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$ *and* $\mathsf{PawnSet}$ *is candidate-empty.*

(c) $\mathcal{K}$ *executes lines of code of* $\mathtt{lock}_{\mathcal{K}}\mathtt{()}$ *starting with line 2 as depicted in Figure 8. (A legend for the figure is given in Figure 7.)*

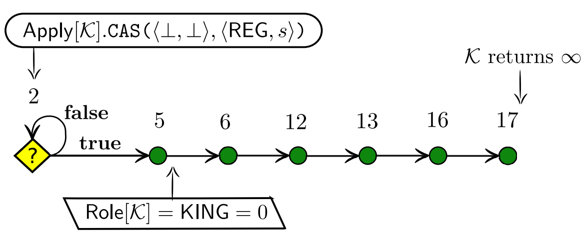


Figure 8: $\mathcal{K}$'s call to $\mathtt{lock}_{\mathcal{K}}\mathtt{()}$

(d) $\mathcal{K}$*'s call to* $\mathtt{lock}_{\mathcal{K}}\mathtt{()}$ *returns* ∞ *and* $\mathsf{Role}[\mathcal{K}] = \mathsf{KING}$ *throughout* $[t_{\mathcal{K}}^{5}, t_{\mathcal{K}}^{17}]$.

(e) $\mathcal{K}$ *executes a* $\mathsf{Ctr}.\mathtt{CAS}(1,0)$ *operation in line 36 during* T*, and* $\mathcal{K}$ *does not change* $\mathsf{Sync1}, \mathsf{Sync2}$ *or* $\mathsf{PawnSet}$ *throughout* $[I_1^-, t_{\mathcal{K}}^{36}]$.

(f) $\forall_{t \in I_1} : R(t) = \{\mathcal{K}\}$.

(g) Throughout I_1, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$ *and* $\mathsf{PawnSet}$ *is candidate-empty.*

Proof. **Proof of (a):** Consider the claim $R(t) = \varnothing$ where $t \in I_0$. Since $R(I_0^-) = \varnothing$ holds by assumption, the claim holds at $t = I_0^-$. For the purpose of a contradiction assume the claim fails to hold for the first time at some point t' during I_0. Then some process p becomes a releaser of lock L at time t'. Process p cannot become a releaser of L by p increasing Ctr to 1 or 2 (condition (R1)) at time t', since $\mathsf{Ctr} = 0$ throughout I_0. Therefore, assume it becomes a releaser of L when some process q promotes p (condition (R2)) at t'. By Claim D.14, q ceases to be a releaser of lock L at t'. This is a contradiction to our assumption that p is the first process during I_0 to become a releaser of L.

By assumption the variables $\mathsf{Sync1}, \mathsf{Sync2}$ and $\mathsf{PawnSet}$ are at their initial value at I_0^-. Since the values of these variables are only changed by a releaser of lock L (by Claim D.15) and for all $t \in I_0$, $R(t) = \varnothing$, it follows that the variables are unchanged throughout I_0.

Proof of (b): At time I_1^- Ctr is increased from 0 to 1, and thus the only operation executed is a $\mathsf{Ctr.inc()}$ operation by process $\mathcal{K}$. Then $\mathcal{K}$ becomes a releaser of lock L at time I_1^- by condition (R1). Since for all $t \in I_0$, $R(t) = \varnothing$ (Part (a)), it follows that $R(I_1^-) = \{\mathcal{K}\}$. Since $\mathsf{Sync1} = \mathsf{Sync2} = \perp$ and $\mathsf{PawnSet}$ is candidate-empty throughout I_0 (Part (a)), and the only operation at time I_1^- is the $\mathsf{Ctr.inc()}$ operation, it follows that $\mathsf{Sync1} = \mathsf{Sync2} = \perp$ and $\mathsf{PawnSet}$ is candidate-empty at time I_1^- .

Proof of (c) and (d): Since $\mathcal{K}$ is the process that increased Ctr from 0 to 1 at time I_1^-, and since $\mathcal{K}$ can increase Ctr only by executing a $\mathsf{Ctr.inc()}$ operation in line 5 (by Claim D.21) $\mathcal{K}$ set $\mathsf{Role}[\mathcal{K}] = 0 = \mathsf{KING}$ at $t_{\mathcal{K}}^5$. Then from the code structure, $\mathcal{K}$ does not execute lines 7-9, and does not repeat the role-loop, and does not busy-wait in the spin loop of line 14; instead $\mathcal{K}$ proceeds to execute lines 16 - 17 and returns value ∞ in line 17. Since $\mathcal{K}$ does not change $\mathsf{Role}[\mathcal{K}]$ during $[t_{\mathcal{K}}^5, t_{\mathcal{K}}^{17}]$, $\mathsf{Role}[\mathcal{K}] = \mathsf{KING}$ throughout $[t_{\mathcal{K}}^5, t_{\mathcal{K}}^{17}]$.

Proof of (e): Since $\mathcal{K}$ is the process that increased Ctr from 0 to 1 at time I_1^-, and since $\mathcal{K}$ can increase Ctr only by executing a $\mathsf{Ctr.inc()}$ operation in line 5 (by Claim D.21) $\mathcal{K}$ set $\mathsf{Role}[\mathcal{K}] = 0 = \mathsf{KING}$ at $t_{\mathcal{K}}^5$. From Part (d), $\mathcal{K}$ returns from $\mathtt{lock}_{\mathcal{K}}\mathtt{()}$ with value ∞ in line 17, and thus $\mathcal{K}$ consequently calls $\mathtt{release}_{\mathcal{K}}(j)$ (follows from conditions (b) and (d)). Note that $\mathcal{K}$ has not executed any operations on $\mathsf{Sync1}, \mathsf{Sync2}$ and $\mathsf{PawnSet}$ in the process. Then $\mathsf{Role}[\mathcal{K}] = \mathsf{KING}$ at $t_{\mathcal{K}}^{35-}$ and thus p satisfies the if-condition of line 35 and executes the $\mathsf{Ctr.CAS}(1,0)$ operation in line 36 during T without having executed any operations on $\mathsf{Sync1}, \mathsf{Sync2}$ and $\mathsf{PawnSet}$ in the process. Thus $\mathcal{K}$ did not change $\mathsf{Sync1}, \mathsf{Sync2}$ or $\mathsf{PawnSet}$ during $[I_1^-, t_{\mathcal{K}}^{36}]$.

Proof of (f): Since $R(I_1^-) = \{\mathcal{K}\}$ (Part (b)), to prove our claim we need to show that during I_1 $\mathcal{K}$ does not cease to be a releaser and no process becomes a releaser. Suppose not, i.e., the claim $R(t) = \{\mathcal{K}\}$ fails to hold for the first time at some point t' in I_1.

Case a - Process $\mathcal{K}$ ceases to be a releaser of L at t': By definition, cease-release event $\phi_{\mathcal{K}}$ occurs when $\mathcal{K}$ executes a successful $\mathsf{Ctr.CAS}(1,0)$ operation in line 36, From Part (e), $\mathcal{K}$ executes a $\mathsf{Ctr.CAS}(1,0)$ operation in line 36. If $\mathcal{K}$ executes a successful $\mathsf{Ctr.CAS}(1,0)$ operation in line 36 then, by definition, cease-release event $\phi_{\mathcal{K}}$ occurs and by Claim D.14 $\mathcal{K}$ ceases to be the releaser of L. Thus, $t' = t_{\mathcal{K}}^{36}$ and Ctr changes to value of 0 at t'. But since $t' \in I_1$ and $\mathsf{Ctr} = 1$ throughout I_1, we have a contradiction. If $\mathcal{K}$ executes an unsuccessful $\mathsf{Ctr.CAS}(1,0)$ operation in line 36, then $\mathsf{Ctr} \neq 1$ at $t_{\mathcal{K}}^{36-}$. Since p did not cease to a releaser at $t_{\mathcal{K}}^{36-}$, $t_{\mathcal{K}}^{36-} < t'$. Since $I_1^- = t_{\mathcal{K}}^5 < t_{\mathcal{K}}^{36} < t' < I_1^+$ and $\mathsf{Ctr} = 1$ throughout I_1, $\mathsf{Ctr} = 1$ at $t_{\mathcal{K}}^{36-}$, and thus we have a contradiction.

Case b - Some process q becomes a releaser of L at t': Since Ctr is not increased during I_1, it follows from conditions (R1) and (R2) that some process r promoted q at time t'. Then by definition, event π_r occurs at t', and thus from Claim D.14 it follows that r is a releaser of L immediately before t'. Since $\mathcal{K}$ is the only releaser immediately before t', $r = \mathcal{K}$. Then cease-release event $\pi_{\mathcal{K}}$ occurred at t' and $\mathcal{K}$ ceases to be a releaser at t'. As was shown in **Case a**, this leads to a contradiction.

Proof of (g): At time I_1^- the claim $\mathsf{Sync1} = \mathsf{Sync2} = \perp$ and $\mathsf{PawnSet}$ is candidate-empty holds by Part (b). Suppose some process p changes $\mathsf{Sync2}$ or $\mathsf{Sync1}$ or $\mathsf{PawnSet}$ for the first time at some point t' during I_1. From Claim D.15 it follows that p is a releaser of lock L at time t'. Since for all $t \in I_1$, $R(t) = \{\mathcal{K}\}$ (Part (f)), it follows that $p = \mathcal{K}$. From Part (e), $\mathcal{K}$ does not change any of the variables before the point when it executes a $\mathsf{Ctr.CAS}(1,0)$ operation in line 36, i.e., $t_{\mathcal{K}}^{36-} < t'$. If $\mathcal{K}$ executes a successful $\mathsf{Ctr.CAS}(1,0)$ operation in line 36 then the interval I_1 ends and clearly $t' \notin I_1$, hence a contradiction. If $\mathcal{K}$ executes an unsuccessful $\mathsf{Ctr.CAS}(1,0)$ operation in line 36 then $\mathsf{Ctr} \neq 1$ at $t_{\mathcal{K}}^{36-}$. Since $I_1^- = t_{\mathcal{K}}^{5-} < t_{\mathcal{K}}^{36-} < t' < I_1^+$ and $\mathsf{Ctr} = 1$ throughout I_1, we have a contradiction. ☐

Claim D.24. *If $I_2 \neq \varnothing$ and $R(I_0^-) = \varnothing$ and at I_0^-, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$ and $\mathsf{PawnSet}$ is candidate-empty, then the following claims hold:*

(a) $R(I_2^-) = \{\mathcal{K}, \mathcal{Q}\}$ and at time I_2^-, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$ and $\mathsf{PawnSet}$ is candidate-empty.

(b) $\mathcal{K}$ and $\mathcal{Q}$ are the first two releasers of L.

(c) During $(I_2^-, I_2^+]$ a process can become a releaser of L only if it gets promoted by a releaser of L.

(d) If $\mathcal{K}$ takes enough steps, $\mathcal{K}$ executes lines of code of `release`$_{\mathcal{K}}$`()` *starting with line 34 as depicted in Figure 9.*

(e) $\mathcal{K}$ executes an unsuccessful $\mathsf{Ctr.CAS}(1,0)$ *operation in line 36, and calls* `helpRelease`$_{\mathcal{K}}$`()` *in line 39 such that $I_2^- < t_{\mathcal{K}}^{36-} < t_{\mathcal{K}}^{39-}$.*

(f) If $\mathcal{K}$ and $\mathcal{Q}$ take enough steps, $\mathcal{Q}$ finishes `lock`$_{\mathcal{Q}}$`()` *during T.*

(g) If $\mathcal{K}$ and $\mathcal{Q}$ take enough steps, $\mathcal{Q}$ executes lines of code of `lock`$_{\mathcal{Q}}$`()` *starting with line 2 as depicted in Figure 10.*

(h) If $\mathcal{Q}$ calls `release`$_{\mathcal{Q}}$`()`*, it executes lines of code of* `release`$_{\mathcal{Q}}$`()` *starting with line 34 as depicted in Figure 11.*

(i) $\mathcal{Q}$ calls `helpRelease`$_{\mathcal{Q}}$`()` *either in line 30 or in line 43, after time I_2^-.*

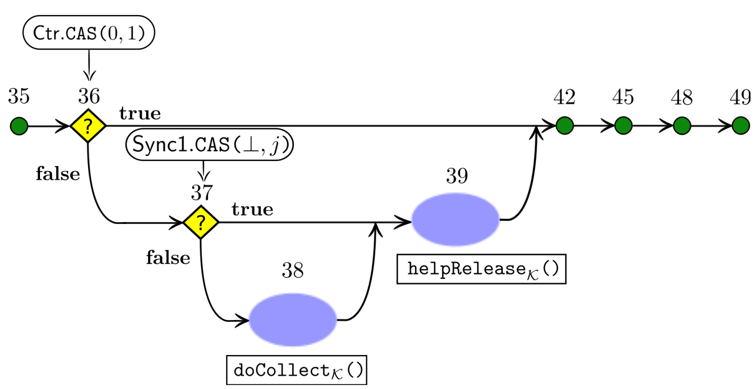


Figure 9: $\mathcal{K}$'s call to `release`$_{\mathcal{K}}$`(`j`)`

Proof. **Proof of (a) and (b):** Since $\mathcal{Q}$ is the process that increases Ctr from 1 to 2 at time I_2^-, and since $\mathcal{Q}$ can increase Ctr only by executing a `Ctr.inc()` operation in line 5 (by Claim D.21) $\mathcal{Q}$ becomes a releaser of lock L by condition (R1) at $I_2^- = t_{\mathcal{Q}}^{5}$. Since for all $t \in I_1$, $R(t) = \{K\}$

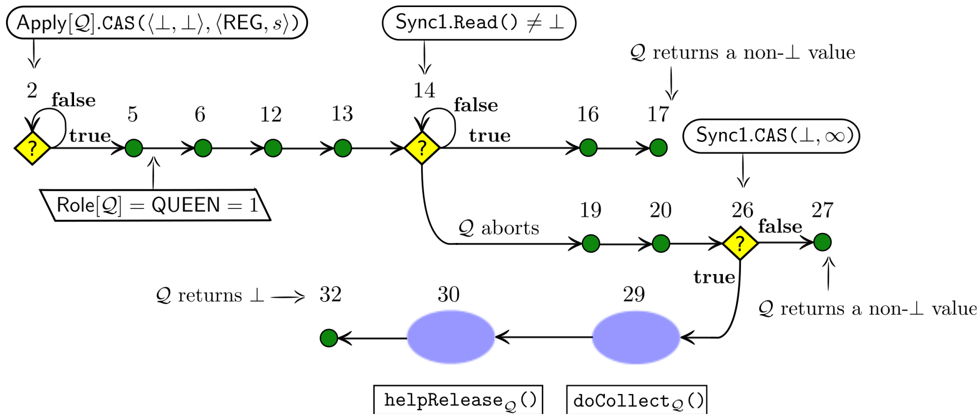


Figure 10: $\mathcal{Q}$'s call to $\texttt{lock}_\mathcal{Q}()$

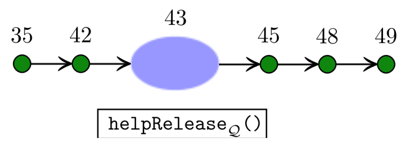


Figure 11: $\mathcal{Q}$'s call to $\texttt{release}_\mathcal{Q}()$

(Claim D.23(f)), it follows that $R(I_2^-) = \{\mathcal{K}, \mathcal{Q}\}$. By claim D.23(g), throughout I_1, Sync1 = Sync2 = $\perp$ and PawnSet is candidate-empty, and since the only operation executed at time I_2^- is Ctr.inc(), it follows that at time I_2^-, Sync1 = Sync2 = $\perp$ and PawnSet is candidate-empty. Hence Part (b) holds. Clearly $\mathcal{K}$ and $\mathcal{Q}$ are the first two releasers of L, hence Part (b) holds.

Proof of (c): From conditions (R1) and (R2), a process can become a releaser of L either by increasing Ctr to 1 or 2 or by getting promoted. Since Ctr is not increased during $(I_2^-, I_2^+]$, it follows that during $(I_2^-, I_2^+]$ a process becomes a releaser of L only if it gets promoted. By definition, a process can be promoted only when a PawnSet.promote() operation is executed in line 65 and from Claim D.18 only a releaser of L can execute this operation. Then during $(I_2^-, I_2^+]$ a process becomes a releaser of L only if it gets promoted by a releaser of L.

Proof of (d) and (e): From Claim D.23(e), $\mathcal{K}$ executes the Ctr.CAS$(1, 0)$ operation in line 36 during T. If $\mathcal{K}$'s Ctr.CAS$(1, 0)$ operation is successful then the value of Ctr decreases from 1 to 0 and the Ctr-cycle interval T ends and thus $I_2 = \varnothing$, which is a contradiction to our assumption that $I_2 \neq \varnothing$. Then $\mathcal{K}$'s Ctr.CAS$(1, 0)$ operation is unsuccessful.

Since $\mathcal{K}$ executes an unsuccessful Ctr.CAS$(1, 0)$ operation in line 36, $\mathcal{K}$ satisfies the if-condition of line 36, executes lines 37-38 and calls $\texttt{helpRelease}_\mathcal{K}()$ in line 39, and then executes lines 49-50.

Since $\mathcal{K}$ executes an unsuccessful Ctr.CAS$(1, 0)$ operation in line 36, it follows that Ctr was changed from 1 to 2 at time I_2^- (by definition), and thus $I_2^- < t_\mathcal{K}^{36}$. Since $t_\mathcal{K}^{36} < t_\mathcal{K}^{39}$, it follows that $I_2^- < t_\mathcal{K}^{36} < t_\mathcal{K}^{39}$.

Proof of (f), (g) and (h): Since $\mathcal{Q}$ is the process that increases Ctr from 1 to 2 at time I_2^-, and since $\mathcal{Q}$ can increase Ctr only by executing a Ctr.inc() operation in line 5 (by Claim D.21) $\mathcal{Q}$ set Role[$\mathcal{K}$] = 1 = QUEEN at $t_\mathcal{Q}^5 = I_2^-$. Then from the code structure, $\mathcal{Q}$ does not execute lines 7-9, and does not repeat the role-loop, instead, it proceeds to line 13 and then proceeds to busy-wait in the spin loop of line 14. Then $\mathcal{Q}$ does not finish $\texttt{lock}_\mathcal{Q}()$ only if it spins indefinitely in line 14 and does not receive a signal to abort.

For the purpose of a contradiction assume that $\mathcal{Q}$ does not finish $\texttt{lock}_\mathcal{Q}()$. Then $\mathcal{Q}$ reads the value $\perp$ from Sync1 in line 14 indefinitely. From Part (e) it follows that $\mathcal{K}$ executes a Sync1.CAS($\perp, j$) operation in line 37 during $(I_2^-, I_2^+]$. Since Sync1 $= \perp$ at time I_2^- (Part (a)), and only a releaser can change Sync1 (Claim D.15), and $\mathcal{Q}$ is busy-waiting in line 14, it follows that the only other releaser, $\mathcal{K}$, executed a successful Sync1.CAS($\perp, j$) operation in line 37 during $(I_2^-, I_2^+]$ and changed Sync1 to a non-$\perp$ value. Then for $\mathcal{Q}$ to read $\perp$ from Sync1 in line 14 indefinitely, some process must reset Sync1 to $\perp$ before $\mathcal{Q}$ reads Sync1 again.

Case a - $\mathcal{K}$ resets Sync1 in line 58 before $\mathcal{Q}$ reads Sync1 again: For $\mathcal{K}$ to reset Sync1 in line 58, $\mathcal{K}$ must satisfy the if-condition of line 56 and thus $\mathcal{K}$ must execute an unsuccessful Sync2.CAS($\perp, \mathcal{K}$) operation in line 56. Since Sync2 $= \perp$ at time I_2^- (Part (a)), and only a releaser can change Sync2 (Claim D.15), and $\mathcal{Q}$ is busy-waiting in line 14, it follows that Sync2 $= \perp$ at $t_{\mathcal{K}}^{56-}$. Thus $\mathcal{K}$'s Sync2.CAS($\perp, \mathcal{K}$) operation in line 56 is successful and we get a contradiction.

Case b - some other process becomes a releaser and resets Sync1 before $\mathcal{Q}$ reads Sync1 again: From Part (c) it follows that during $(I_2^-, I_2^+]$ a process can become a releaser of L only if it is promoted (by condition (R2)). Since a process is promoted only by a releaser of L and $\mathcal{K}$ is the only other releaser of L apart from $\mathcal{Q}$, it follows that $\mathcal{K}$ promotes some process before $\mathcal{Q}$ reads Sync1 again. As argued in **Case a**, $\mathcal{K}$ executes a successful Sync2.CAS($\perp, \mathcal{K}$) operation in line 56. Then from the code structure, $\mathcal{K}$ does not call doPromote$_{\mathcal{K}}$() in line 62, and thus $\mathcal{K}$ does not promote any process. Hence, we have a contradiction.

Proof of (i): Since $\mathcal{Q}$ is the process that increases Ctr from 1 to 2 at time I_2^-, and since $\mathcal{Q}$ can increase Ctr only by executing a Ctr.inc() operation in line 5 (by Claim D.21) $\mathcal{Q}$ set Role[$\mathcal{K}$] $= 1 =$ QUEEN at $t_{\mathcal{Q}}^{5}$. Then from the code structure, $\mathcal{Q}$ does not execute lines 7-9, and does not repeat the role-loopp; instead, it proceeds to line 13 and then proceeds to busy-wait in the spin loop of line 14.

Case a - $\mathcal{Q}$ does not receive a signal to abort while busy-waiting in line 14: From Part (f), $\mathcal{Q}$ does not busy-wait indefinitely in line 14 and eventually breaks out. Since $\mathcal{Q}$ breaks out of the spin loop of line 14 it reads non-$\perp$ from Sync1 and then from the code structure it follows that $\mathcal{Q}$ goes on to return that non-$\perp$ value in line 17. Consequently $\mathcal{Q}$ calls release$_{\mathcal{Q}}$(j) (follows from conditions b and d). Consider $\mathcal{Q}$'s call to release$_{\mathcal{Q}}$(j). Since $\mathcal{Q}$ last changed Role[$\mathcal{Q}$] only in line 5, Role[$\mathcal{Q}$] $=$ QUEEN at $t_{\mathcal{Q}}^{34-}$. Since Role[$\mathcal{Q}$] is unchanged during release$_{\mathcal{Q}}$() (Claim D.4(b)), it follows that Role[$\mathcal{Q}$] $=$ QUEEN throughout release$_{\mathcal{Q}}$(). Then from the code structure it follows that $\mathcal{Q}$ executes only lines 34-35, 42-45 and 49-50. Then $\mathcal{Q}$ calls helpRelease$_{\mathcal{Q}}$() only in line 43, and since $I_2^- = t_{\mathcal{Q}}^{5} < t_{\mathcal{Q}}^{43}$, our claim holds.

Case b - $\mathcal{Q}$ receives a signal to abort while busy-waiting in line 14: Then $\mathcal{Q}$ calls abort$_{\mathcal{Q}}$(), and from the code structure $\mathcal{Q}$ executes lines 18-20, and then line 26. If $\mathcal{Q}$ fails the Sync1.CAS($\perp, \infty$) operation of line 26, then Sync1 $\neq \perp$ at $t_{\mathcal{Q}}^{26-}$. From Claim D.15, only a releasers of L can change Sync1 to a non-$\perp$ value, and since $\mathcal{K}$ and $\mathcal{Q}$ are the only releasers of L, it follows that $\mathcal{K}$ changed Sync1 to a non-$\perp$ value. Then $\mathcal{Q}$ satisfies the if-condition of line 26 and returns the non-$\perp$ value written by $\mathcal{K}$ to Sync1 in line 27. Consequently $\mathcal{Q}$ calls release$_{\mathcal{Q}}$(j) (follows from conditions b and d), and as argued in **Case a**, $\mathcal{Q}$ executes only lines 34-35, 42-45 and 49-50, and $\mathcal{Q}$ calls helpRelease$_{\mathcal{Q}}$() only in line 43. Since $I_2^- = t_{\mathcal{Q}}^{5} < t_{\mathcal{Q}}^{43}$, our claim holds.

If $\mathcal{Q}$'s Sync1.CAS($\perp, \infty$) operation is successful, then $\mathcal{Q}$ goes on to call doCollect$_{\mathcal{Q}}$() in line 29, calls helpRelease$_{\mathcal{Q}}$() in line 30, then executes lines 32-33, and finally returns $\perp$ in line 33. Since $I_2^- = t_{\mathcal{Q}}^{5} < t_{\mathcal{Q}}^{30}$, our claim holds. ☐

Define λ to be the first point in time when Sync2 is changed to a non-$\perp$ value, and if Sync2 is never changed to non-$\perp$ then $\lambda = \infty$. Define γ to be the first point in time when a PawnSet.promote() operation is executed, and if a PawnSet.promote() operation is never executed then $\gamma = \infty$. From Claims D.24(e) and D.24(i), both $\mathcal{K}$ and $\mathcal{Q}$ execute $\texttt{helpRelease}_{\mathcal{K}}\texttt{()}$ and $\texttt{helpRelease}_{\mathcal{Q}}\texttt{()}$, respectively, after time I_2^-. Let $\mathcal{A} \in \{\mathcal{K}, \mathcal{Q}\}$ be the first process among them to execute line 56, and let $\mathcal{B} \in \{\mathcal{K}, \mathcal{Q}\} - \{\mathcal{A}\}$ be the other process, i.e., $t_{\mathcal{A}}^{56} < t_{\mathcal{B}}^{56}$.

Claim D.25. *If $I_2 \neq \varnothing$ and $R(I_0^-) = \varnothing$ and at I_0^-, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$ and $\mathsf{PawnSet}$ is candidate-empty, then the following claims hold:*

(a) $I_2^- < \lambda = t_{\mathcal{A}}^{56}$ *and for all* $t \in [I_2^-, \lambda)$, $R(t) = \{\mathcal{K}, \mathcal{Q}\}$ *and* $\mathsf{Sync2} = \bot$ *throughout* $[I_2^-, \lambda)$, *and cease-release event* $\tau_{\mathcal{A}}$ *occurs at* λ.

(b) If $\mathcal{K}$ and $\mathcal{Q}$ take enough steps, then $\mathcal{A}$ executes lines of code of $\texttt{helpRelease}_{\mathcal{A}}\texttt{()}$ *starting with line 56 as depicted in Figure 12.*

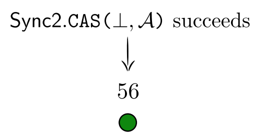


Figure 12: $\mathcal{A}$'s call to $\texttt{helpRelease}_{\mathcal{A}}\texttt{()}$

(c) If $\mathcal{K}$ and $\mathcal{Q}$ take enough steps, then $\mathcal{B}$ executes lines of code of $\texttt{helpRelease}_{\mathcal{B}}\texttt{()}$ *and* $\texttt{doPromote}_{\mathcal{B}}\texttt{()}$ *as depicted in Figures 13 and 14, respectively.*

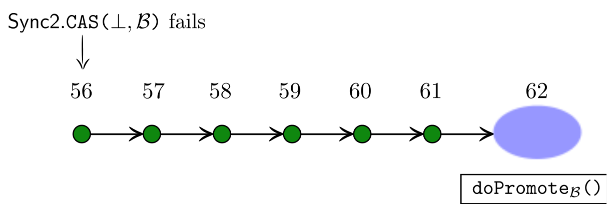


Figure 13: $\mathcal{B}$'s call to $\texttt{helpRelease}_{\mathcal{B}}\texttt{()}$

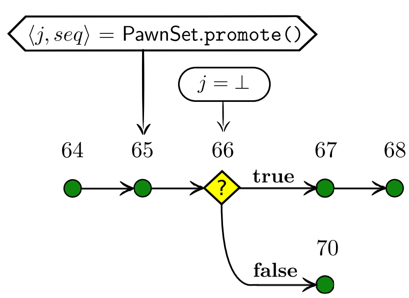


Figure 14: $\mathcal{B}$'s call to $\texttt{doPromote}_{\mathcal{B}}\texttt{()}$

(d) $\lambda < \gamma = t_{\mathcal{B}}^{65}$.

(e) $\forall_{t \in [\lambda, \gamma)}$, $R(t) = \{\mathcal{B}\}$.

(f) At time γ, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$.

(g) No promotion event occurs at lock L *during* $[I_2^-, \gamma)$.

(h) The PawnSet.promote() *operation at time γ does not return a value in $\{\langle a,b\rangle | a \in \{\mathcal{K},\mathcal{Q}\}, b \in \mathbb{N}\}$.*

(i) If the PawnSet.promote() *operation at time γ returns a non-$\langle\bot,\bot\rangle$ value then $\mathcal{B}$'s cease-release event $\pi_{\mathcal{B}}$ occurs at time γ.*

(j) If the PawnSet.promote() *operation at time γ returns value $\langle\bot,\bot\rangle$ then $\mathcal{B}$'s cease-release event $\theta_{\mathcal{B}}$ occurs at $t' = t_{\mathcal{B}}^{68} \geq \gamma$, and throughout $[\gamma, t']$ no process is promoted, and $\forall_{t\in[\gamma,t')}$, $R(t) = \{\mathcal{B}\}$.*

(k) Either $\mathcal{K}$ or $\mathcal{Q}$ calls `doCollect()`*, specifically during $[I_2^-, \gamma]$.*

Proof. **Proof of (a):** We first show that for all $t \in [I_2^-, t_{\mathcal{A}}^{56-}]$, $R(t) = \{\mathcal{K},\mathcal{Q}\}$ and then show that $\lambda = t_{\mathcal{A}}^{56}$. From Claims D.24(e) $\mathcal{K}$ calls $\texttt{helpRelease}_{\mathcal{K}}\texttt{()}$ in line 39 after time I_2^-. From Claim D.24(a), $\mathcal{K}$ is a releaser of L at time I_2^-. From an inspection of Figures 8 and 9, throughout $[I_1, t_K^{39-}]$ $\mathcal{K}$ does not execute a call to $\texttt{helpRelease}_{\mathcal{K}}\texttt{()}$ or $\texttt{doPromote}_{\mathcal{K}}\texttt{()}$. Also from an inspection, $\mathcal{K}$ fails to decrease Ctr from 1 to 0 at $t_{\mathcal{K}}^{36}$, thus $\mathcal{K}$'s cease-release event $\phi_{\mathcal{K}}$ does not occur. Since $\mathcal{K}$'s cease-release events $\tau_{\mathcal{K}}, \pi_{\mathcal{K}}$ and $\theta_{\mathcal{K}}$ only occur during $\texttt{helpRelease}_{\mathcal{K}}\texttt{()}$ or $\texttt{doPromote}_{\mathcal{K}}\texttt{()}$ (Claims D.7(e) and D.7(f)), it follows that $\mathcal{K}$ is a releaser of L throughout $[I_1^-, t_{\mathcal{K}}^{56-}]$.

From Claim D.24(i), $\mathcal{Q}$ calls $\texttt{helpRelease}_{\mathcal{Q}}\texttt{()}$, respectively either in line 30 or line 43, after time I_2^-. From Claim D.24(a), $\mathcal{Q}$ is a releaser of L at time I_2^-. From an inspection of Figures 10 and 11, throughout $[I_2, t_Q^{56-}]$ $\mathcal{Q}$ does not execute a call to $\texttt{helpRelease}_{\mathcal{Q}}\texttt{()}$ or $\texttt{doPromote}_{\mathcal{Q}}\texttt{()}$. Also from an inspection, $\mathcal{Q}$ does not execute a $\mathsf{Ctr}.\texttt{CAS}(1,0)$ operation in line 36, and thus $\mathcal{Q}$'s cease release event $\phi_{\mathcal{Q}}$ does not occur. Since $\mathcal{Q}$'s cease-release events $\tau_{\mathcal{Q}}, \pi_{\mathcal{Q}}$ and $\theta_{\mathcal{Q}}$ only occur during $\texttt{helpRelease}_{\mathcal{Q}}\texttt{()}$ or $\texttt{doPromote}_{\mathcal{Q}}\texttt{()}$ (Claims D.7(e) and D.7(f)), it follows that $\mathcal{Q}$ is a releaser of L throughout $[I_2^-, t_{\mathcal{Q}}^{56-}]$.

Then for all $t \in [I_2^-, t_{\mathcal{A}}^{56-}]$, $\{\mathcal{K},\mathcal{Q}\} \subseteq R(t)$ since $I_1^- < I_2^-$ and $t_{\mathcal{A}}^{56-} = \texttt{min}(t_{\mathcal{K}}^{56-}, t_{\mathcal{Q}}^{56-})$. From Claim D.24(c), it follows that a process can become a releaser during I_2 only if it is promoted by a releaser of L. Then to show that for all $t \in [I_2^-, t_{\mathcal{A}}^{56-}]$, $R(t) = \{\mathcal{K},\mathcal{Q}\}$, we need to show that no process is promoted by $\mathcal{K}$ or $\mathcal{Q}$ during $[I_2^-, t_{\mathcal{A}}^{56-}]$. If a process was promoted by $\mathcal{K}$ or $\mathcal{Q}$ during $[I_2^-, t_{\mathcal{A}}^{56-}]$ then by definition cease-release events $\pi_{\mathcal{K}}$ or $\pi_{\mathcal{Q}}$ would have occurred during $[I_2^-, t_{\mathcal{A}}^{56-}]$, but as shown above this does not happen.

From Claim D.24(a), $\mathsf{Sync2} = \bot$ at time I_2^-. From a code inspection, $\mathsf{Sync2}$ is changed to a non-$\bot$ value only in line 56 (during `helpRelease()`), moreover only by a releaser of L (from Claim D.15). Since for all $t \in [I_2^-, t_{\mathcal{A}}^{56-}]$, $R(t) = \{\mathcal{K},\mathcal{Q}\}$ and $t_{\mathcal{A}}^{56-} = \texttt{min}(t_{\mathcal{K}}^{56-}, t_{\mathcal{Q}}^{56-})$, it follows then that $\mathsf{Sync2} = \bot$ throughout $[I_2^-, t_{\mathcal{A}}^{56-}]$ and $\mathcal{A}$ executes a successful $\mathsf{Sync2}.\texttt{CAS}(\bot, \mathcal{A})$ operation in line 56. Thus $\mathcal{A}$'s cease-release event $\tau_{\mathcal{A}}$ occurs at $t_{\mathcal{A}}^{56}$.

Since $\mathsf{Sync2} = \bot$ throughout $[I_0^-, I_1^+]$ (Claims D.23(a) and D.23(g)) and throughout $[I_2^-, t_{\mathcal{A}}^{56-}]$, it follows that $\mathsf{Sync2}$ was changed to a non-$\bot$ value for the first time at $t_{\mathcal{A}}^{56}$, thus $\lambda = t_{\mathcal{A}}^{56}$. Then it follows for all $t \in [I_2^-, \lambda)$, $R(t) = \{\mathcal{K},\mathcal{Q}\}$, and $\mathsf{Sync2} = \bot$ throughout $[I_2^-, \lambda)$

Proof of (b): From Part (a), $\mathcal{A}$'s cease-release event τ_A occurs at $\lambda = t_{\mathcal{A}}^{56}$, and thus $\mathcal{A}$'s $\mathsf{Sync2}.\texttt{CAS}(\bot, \mathcal{A})$ operation in line 56 succeeds. Then from the code structure $\mathcal{A}$ does not satisfy the if-condition on line 56 and returns from its call to $\texttt{helpRelease}_{\mathcal{A}}\texttt{()}$. Thus, Figure 12 follows.

Proof of (c), (d), (e), (f), (g), (h), (i) and (j): From Part (a), $\lambda = t_{\mathcal{A}}^{56}$ and for all $t \in [I_2^-, \lambda)$, $R(t) = \{\mathcal{K}, Q\}$ and $\mathsf{Sync2} = \bot$ throughout $[I_2^-, \lambda)$ and cease-release event $\tau_{\mathcal{A}}$ occurs at λ. Then $\mathcal{A}$ ceases to be a releaser of L at λ, and thus $R(\lambda) = \{\mathcal{B}\}$ and $\mathsf{Sync2} = \mathcal{A} \neq \bot$ at λ. From

Claim D.24(c) it follows that $\mathcal{B}$ will continue to be the only releaser of L until the point when $\mathcal{B}$ ceases to be a releaser of L or promotes another process. Let $t > \lambda$ be the point in time when $\mathcal{B}$ ceases to be a releaser of L. Since $\mathcal{B}$ ceases to be a releaser of L if it promotes another process (by definition of cease-release event $\pi_{\mathcal{B}}$), it follows that $\mathcal{B}$ is the only releaser of L throughout $[\lambda, t)$. Then from Claim D.15 it follows that $\mathcal{B}$ has exclusive write-access to $\mathsf{Sync1}, \mathsf{Sync2}$ and exclusive registration-access to $\mathsf{PawnSet}$ throughout $[\lambda, t)$.

Now consider $\mathcal{B}$'s $\mathtt{helpRelease}_{\mathcal{B}}\mathtt{()}$ call. Since $\lambda = t^{56}_{\mathcal{A}} < t^{56}_{\mathcal{B}}$ and $\mathsf{Sync2} \neq \perp$ at λ and $\mathcal{B}$ has exclusive write-access to $\mathsf{Sync2}$ throughout $[\lambda, t)$, $\mathcal{B}$ fails the $\mathsf{Sync2}.\mathtt{CAS}(\perp, \mathcal{B})$ operation at $t^{56}_{\mathcal{B}}$, and thus satisfies the if-condition of line 56. It then executes lines 57 - 62, and calls $\mathtt{doPromote}_{\mathcal{B}}\mathtt{()}$ in line 62. Then Figures 13 and 14 and Part (c) follows immediately.

We now show that $\gamma = t^{65}_{\mathcal{B}} \leq t$. Since $\lambda = t^{56}_{\mathcal{A}}$ and $t^{56}_{\mathcal{A}} < t^{56}_{\mathcal{B}} < t^{65}_{\mathcal{B}}$, it would follow that $\lambda < \gamma$, and hence we would have proved Part (d). And since $\mathcal{B}$ is the only releaser of L throughout $[\lambda, t)$, we would have proved Part (e) as well, i.e., $\mathcal{B}$ is the only releaser throughout $[\lambda, \gamma)$.

During $\mathtt{doPromote}_{\mathcal{B}}\mathtt{()}$, $\mathcal{B}$ executes a $\mathsf{PawnSet}.\mathtt{promote()}$ operation in line 65. Since $\mathcal{K}$ and $\mathcal{Q}$ are the first two releasers of L during T (Claim D.24(b)), and only a releaser executes a $\mathsf{PawnSet}.\mathtt{promote()}$ operation (Claim D.18), and $\mathcal{A}$ ceased to be a releaser at $t^{56}_{\mathcal{A}} < t^{65}_{\mathcal{B}}$, it follows that $\mathcal{B}$'s $\mathsf{PawnSet}.\mathtt{promote()}$ operation in line 65 is the first $\mathsf{PawnSet}.\mathtt{promote()}$ operation, and thus $\gamma = t^{65}_{\mathcal{B}}$. Since none of $\mathcal{B}$'s cease-release events occur during $[t^{56}_{\mathcal{B}}, t^{65-}_{\mathcal{B}}]$, $t \geq t^{65}_{\mathcal{B}}$.

During $[t^{56}_{\mathcal{B}}, t^{65}_{\mathcal{B}}]$, $\mathcal{B}$ resets $\mathsf{Sync1}$ and $\mathsf{Sync2}$ in lines 58 and 60, respectively, and since $\mathcal{B}$ has exclusive write-access to $\mathsf{Sync1}$ and $\mathsf{Sync2}$ throughout $[t^{56}_{\mathcal{B}}, t^{65}_{\mathcal{B}}]$, at time $\gamma = t^{65}_{\mathcal{B}}$, $\mathsf{Sync1} = \mathsf{Sync2} = \perp$. Thus, Part (f) follows.

By definition γ is the point in time when the first $\mathsf{PawnSet}.\mathtt{promote()}$ operation occurs. Since a promotion event occurs only when a $\mathsf{PawnSet}.\mathtt{promote()}$ operation returns a non-$\langle\perp, \perp\rangle$ value, it follows that no promotion event occurs during $[I_0^-, \gamma)$. Hence, Part (g) follows.

Since $\mathcal{B}$ has exclusive write-access to $\mathsf{Sync2}$ throughout $[\lambda, t^{65}_{\mathcal{B}}]$, and $\mathsf{Sync2} = \mathcal{A}$ at $\lambda > t^{56}_{\mathcal{B}}$, $\mathcal{B}$ reads the value $\mathcal{A}$ from $\mathsf{Sync2}$ in line 59 and executes a $\mathsf{PawnSet}.\mathtt{remove}(\mathcal{A})$ operation in line 61. Since $\mathcal{B}$ executes $\mathsf{PawnSet}.\mathtt{remove}(\mathcal{A})$ and $\mathsf{PawnSet}.\mathtt{remove}(\mathcal{B})$ in lines 61 and 64 during $[\lambda, \gamma)$ and $\mathcal{B}$ has exclusive registration-access to $\mathsf{PawnSet}$ during $[\lambda, \gamma)$, it follows from the semantics of the $\mathsf{AbortableProArray}_n$ object that $\mathcal{B}$'s $\mathsf{PawnSet}.\mathtt{promote()}$ operation at time γ does not return values in $\{\langle a, b\rangle | a \in \{\mathcal{A}, \mathcal{B}\} = \{\mathcal{K}, \mathcal{Q}\}, b \in \mathbb{N}\}$. Hence, Part (h) follows.

Case a - $\mathcal{B}$'s $\mathsf{PawnSet}.\mathtt{promote()}$ operation returns a non-$\langle\perp, \perp\rangle$ value: Then $\mathcal{B}$'s cease-release event $\pi_{\mathcal{B}}$ occurs at $t^{65}_{\mathcal{B}} = \gamma$ (Claim D.18), and thus Part (i) holds.

Case b - $\mathcal{B}$'s $\mathsf{PawnSet}.\mathtt{promote()}$ operation in line 65 returns $\langle\perp, \perp\rangle$. Then $\mathcal{B}$ did not find any process to promote, and thus cease-release event $\pi_{\mathcal{B}}$ did not occur. From the code structure $\mathcal{B}$ goes on to execute a $\mathsf{Ctr}.\mathtt{CAS}(2, 0)$ operation in line 68. Since $\mathsf{Ctr} = 2$ throughout I_2, it follows that $\mathcal{B}$'s $\mathsf{Ctr}.\mathtt{CAS}(2, 0)$ operation succeeds, and thus $\mathcal{B}$'s cease-release event $\theta_{\mathcal{B}}$ occurs at $t^{68}_{\mathcal{B}}$ and the intervals I_2 and T end. Therefore $t' = t^{68}_{\mathcal{B}} > t^{65}_{\mathcal{B}} = \gamma$. Clearly, $\mathcal{B}$ does not promote any process in $[t^{65}_{\mathcal{B}}, t^{68}_{\mathcal{B}}] = [\gamma, t']$, and thus Part (j) holds.

Proof of (k): From an inspection of Figure 9, $\mathcal{K}$ executes a $\mathsf{Sync1}.\mathtt{CAS}(\perp, \mathcal{K})$ operation in line 37. Since $I_2^- < t^{36}_{\mathcal{K}} < t^{37}_{\mathcal{K}} < t^{56}_{\mathcal{K}} < \gamma$ (from Parts (a) and (d)), it follows that $t^{37}_{\mathcal{K}} \in [I_2^-, \gamma]$. From an inspection of Figures 10 and 10, $\mathcal{Q}$ may or may not execute a $\mathsf{Sync1}.\mathtt{CAS}(\perp, \infty)$ operation in line 26. If $\mathcal{Q}$ executes a $\mathsf{Sync1}.\mathtt{CAS}(\perp, \infty)$ operation in line 26, since $I_2^- < t^{26}_{\mathcal{Q}} < t^{56}_{\mathcal{Q}} < \gamma$, it follows that $t^{26}_{\mathcal{Q}} \in [I_2^-, \gamma]$.

Since for all $t \in [I_2^-, \gamma]$, $R(t) \subseteq \{\mathcal{K}, \mathcal{Q}\}$ (from Parts (a) and (e)), and only releasers of L have write-access to $\mathsf{Sync1}$ (Claim D.15), and $\mathsf{Sync1} = \perp$ at I_2^- (Claim D.24(a)), it follows that either $\mathcal{K}$ or $\mathcal{Q}$ executes a successful $\mathtt{CAS()}$ operation on $\mathsf{Sync1}$. Then from the code structure it follows that either $\mathcal{K}$ or $\mathcal{Q}$ executed a call to doCollect() in lines 38 or 29, respectively. Since $t_{\mathcal{K}}^{38} < t_{\mathcal{K}}^{39-} = t_{\mathcal{K}}^{56-} < \gamma$ and $t_{\mathcal{Q}}^{29} < t_{\mathcal{Q}}^{56-} < \gamma$, $\mathcal{K}$ or $\mathcal{Q}$ executed a call to doCollect() during $[I_2^-, \gamma]$. ☐

Claim D.26. *If a process p is promoted at time $t' \in T$ and a* PawnSet.reset() *has not been executed during $[I_0^-, t']$, then p did not execute a* PawnSet.abort(p, s) *operation during $[I_0^-, t']$, where $s \in \mathbb{N}$.*

Proof. Suppose not, i.e., p executed a PawnSet.abort(p, s) operation at time $t < t'$. Since p has not been promoted before $t' > t$ it follows that a PawnSet.promote() operation that returns $\langle p, \cdot \rangle$ has not been executed before t. Then from Claim D.5(a) and the semantics of PawnSet, it follows that the p-th entry of PawnSet is not at value $\langle \mathsf{PRO}, s \rangle = \langle 2, s \rangle$ throughout $[I_0^-, t]$. Then p's PawnSet.abort(p, s) operation at t succeeds, and thus p writes value $\langle \mathsf{ABORT}, s \rangle = \langle 3, s \rangle$ to the p-entry of PawnSet. Then for p to be promoted at $t' > t$, it follows from the semantics of PawnSet and Claim D.5(a), that during $[t, t')$ a PawnSet.reset() operation and then a PawnSet.collect(A) operation where $A[p] = s$, must be executed, followed by a PawnSet.promote() at t' that returns $\langle p, s \rangle$. This is a contradiction to the assumption that a PawnSet.reset() is not executed during $[I_0, t']$. ☐

Let ℓ be the number of times a promotion occurs during T. For all $i \in \{1, \ldots, \ell\}$, define Ω_i to be the i-th interval $[\Omega_i^-, \Omega_i^+]$ that begins when the i-th promotion occurs during T and ends when the promoted process ceases to be a releaser of L. Let $\mathcal{P}_i$ be the process promoted at Ω_i^-.

Claim D.27. *If $I_2 \neq \varnothing$ and $R(I_0^-) = \varnothing$ and at time I_0^-,* Sync1 = Sync2 = $\perp$ *and* PawnSet *is candidate-empty, then the following claims hold for all $i \in \{1, \ldots, \ell\}$:*

(a) If $\ell \geq 1$, then $\gamma = \Omega_1^-$ and $R(\Omega_1^-) = \{\mathcal{P}_1\}$, and Sync1 = Sync2 = $\perp$ *at Ω_1^-, and no* PawnSet.reset() *operation has been executed during $[I_0^-, \Omega_1^-]$.*

(b) If $R(\Omega_i^-) = \{\mathcal{P}_i\}$, then for all $t \in [\Omega_i^-, \Omega_i^+)$, $R(t) = \{\mathcal{P}_i\}$. (i.e., $\mathcal{P}_i$ is the only releaser throughout Ω_i)

(c) If $i \neq \ell$ and $R(\Omega_i^-) = \{\mathcal{P}_i\}$, then $\Omega_i^+ = \Omega_{i+1}^-$ and $R(\Omega_{i+1}^-) = \{\mathcal{P}_{i+1}\}$. (i.e., $\mathcal{P}_{i+1}$ is the only releaser at Ω_{i+1}^-)

(d) If $i \neq \ell$, then $\Omega_i^+ = \Omega_{i+1}^-$ and $R(\Omega_{i+1}^-) = \{\mathcal{P}_{i+1}\}$.

(e) For all $t \in [\Omega_i^-, \Omega_i^+)$, $R(t) = \{\mathcal{P}_i\}$. (i.e., $\mathcal{P}_i$ is the only releaser throughout Ω_i)

Proof. **Proof of (a):** If the PawnSet.promote() operation at time γ returns value $\langle \perp, \perp \rangle$, then from Claims D.25(g) and D.25(j) it follows that no promotion occurs during T, which is a contradiction to $\ell \geq 1$. Thus, the PawnSet.promote() operation at time γ returns a non-$\langle \perp, \perp \rangle$ value. By definition γ is the point when the first PawnSet.promote() operation occurs, and Ω_1^- is the point when the first promotion occurs and $\mathcal{P}_1$ is the process promoted at Ω_1^-. Then $\gamma = \Omega_1^-$, and $\mathcal{P}_1$ is the first promoted process. From Claim D.25(e), $\mathcal{B}$ is the only releaser of L at the point in time immediately before time γ. Then from Claim D.25(i) it follows that $\mathcal{B}$ promotes $\mathcal{P}_1$ at time $\gamma = \Omega_1^-$, and $\mathcal{B}$ ceases to be a releaser of L at γ, therefore $R(\gamma) = \{\mathcal{P}_1\}$. From Claim D.25(f) it follows that Sync1 = Sync2 = $\perp$ at Ω_1^-.

From an inspection of the code, a PawnSet.reset() is executed only in line 67, and it can be executed only after a PawnSet.promote() is executed in line 65. Since γ is the first point when a PawnSet.promote() is executed, it follows that no PawnSet.reset() operation was executed during $[I_0^-, \gamma]$.

Proof of (b): Since $R(\Omega_i^-) = \{\mathcal{P}_i\}$, and Ω_i^+ is the point when $\mathcal{P}_i$ ceases to be a releaser of L, for all $t \in [\Omega_i^-, \Omega_i^+)$, $\{\mathcal{P}_i\} \subseteq R(t)$. To show that for all $t \in [\Omega_i^-, \Omega_i^+)$, $R(t) = \{\mathcal{P}_i\}$, we need to show that no other process becomes a releaser of L, during $[\Omega_i^-, \Omega_i^+)$. Suppose some process $q \neq \mathcal{P}_i$ becomes a releaser of L some time during that interval. Since $\Omega_i^- > \Omega_1^- = \gamma > I_2^-$, from Claim D.24(c) it follows that $\mathcal{P}_i$ promotes q during $[\Omega_i^-, \Omega_i^+)$. Then from Claim D.18, $\mathcal{P}_i$'s cease-release event $\pi_{\mathcal{P}_i}$ occurs during $[\Omega_i^-, \Omega_i^+)$, and thus $\mathcal{P}_i$ ceases to be a releaser of L during $[\Omega_i^-, \Omega_i^+)$. Hence a contradiction.

Proof of (c): Since $i < \ell$, it follows that there exists a process $\mathcal{P}_{i+1}$ that becomes a releaser of L during T. By definition, $\mathcal{P}_i$ and $\mathcal{P}_{i+1}$ are the i-th and $(i+1)$-th promoted processes during T, respectively. Since $\Omega_{i+1}^- > \Omega_i^- > \Omega_1^- = \gamma > I_2^-$, from Claim D.24(c) it follows that no other process becomes a releaser after $\mathcal{P}_i$ became a releaser and before $\mathcal{P}_{i+1}$ becomes a releaser, i.e., during $[\Omega_i^-, \Omega_{i+1}^-]$. Moreover, since $R(\Omega_i^-) = \{\mathcal{P}_i\}$, it follows that the next process to be promoted, i.e., $\mathcal{P}_{i+1}$, is promoted by the only releaser of L, $\mathcal{P}_i$. Then from Claim D.18, it follows that $\mathcal{P}_i$ promotes $\mathcal{P}_{i+1}$ by executing a $\mathsf{PawnSet}.\mathtt{promote()}$ in line 65 that returns $\langle \mathcal{P}_{i+1}, s\rangle$, where $s \in \mathbb{N}$, and event $\pi_{\mathcal{P}_i}$ occurs at $t_{\mathcal{P}_i}^{65}$. Then $\mathcal{P}_i$ ceases to be a releaser of L at $t_{\mathcal{P}_i}^{65}$ and thus $\Omega_i^+ = t_{\mathcal{P}_i}^{65}$. Since Ω_{i+1}^- is the point when $\mathcal{P}_{i+1}$ becomes a releaser of L, it follows that $\Omega_i^+ = \Omega_{i+1}^-$, and thus $R(\Omega_{i+1}^-) = \{\mathcal{P}_{i+1}\}$.

Proof of (d): We prove by induction that for all $k < \ell$, $R(\Omega_{k+1}^-) = \{\mathcal{P}_{k+1}\}$ and $\Omega_k^+ = \Omega_{k+1}^-$.

Basis ($k = 1$) From Part (a), $\mathcal{P}_1$ is the only releaser of L at Ω_1^-, and clearly $\ell > k = 1$. Then from Part (c), $\Omega_1^+ = \Omega_2^-$ and $R(\Omega_2^-) = \{\mathcal{P}_2\}$.

Induction step ($k > 1$) By definition $\mathcal{P}_k$ is the promoted process at Ω_k^-, and since $|R(\Omega_{k-1}^+)| = 1$ and $\Omega_{k-1}^+ = \Omega_k^-$ (by the induction hypothesis), it follows that $\mathcal{P}_k$ is the only releaser of L at Ω_k^-. Then from Part (c), $\Omega_k^+ = \Omega_{k+1}^-$ and $R(\Omega_{k+1}^-) = \{\mathcal{P}_{k+1}\}$.

Proof of (e): From Part (a), $R(\Omega_1^-) = \{\mathcal{P}_1\}$, and thus from Part (b), for all $t \in [\Omega_1^-, \Omega_1^+)$, $R(t) = \{\mathcal{P}_1\}$. From Part (d), for all $i > 1$, $R(\Omega_i^-) = \{\mathcal{P}_i\}$, and thus from Part (b), for all $t \in [\Omega_i^-, \Omega_i^+)$, $R(t) = \{\mathcal{P}_i\}$. Hence, our claim follows. ☐

Claim D.28. *If $I_2 \neq \varnothing$ and $R(I_0^-) = \varnothing$ and at time I_0^-, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$ and $\mathsf{PawnSet}$ is candidate-empty, then the following claims hold for all $i \in \{1, \ldots, \ell\}$:*

(a) A $\mathsf{PawnSet}.\mathtt{reset()}$ operation is not executed during $[I_0^-, \Omega_i^-]$.

(b) $\mathcal{P}_i$ executes lines of code of $\mathtt{lock}_{\mathcal{P}_i}\mathtt{()}$ starting with line 2 as depicted in Figure 15.

(c) $\mathcal{P}_i$'s call to $\mathtt{lock}_{\mathcal{P}_i}\mathtt{()}$ returns ∞, and $\mathcal{P}_i$ finishes $\mathtt{lock}_{\mathcal{P}_i}\mathtt{()}$ during T, and $\mathsf{Role}[\mathcal{P}_i] = \mathsf{PAWN\_P}$ when $\mathcal{P}_i$'s call to $\mathtt{lock}_{\mathcal{P}_i}\mathtt{()}$ returns.

(d) Exactly one cease-release event among $\pi_{\mathcal{P}_i}$ and $\theta_{\mathcal{P}_i}$ occurs during $\mathcal{P}_i$'s call to $\mathtt{doPromote}_{\mathcal{P}_i}\mathtt{()}$.

(e) $\mathcal{P}_i$ executes lines of code of $\mathtt{release}_{\mathcal{P}_i}\mathtt{()}$ starting with line 34 as depicted in Figure 16.

(f) $\mathcal{P}_i$ does not write to $\mathsf{Sync1}$ or $\mathsf{Sync2}$ during $[\Omega_i^-, \Omega_i^+]$.

(g) $t_{\mathcal{P}_i}^{2} < \Omega_i^- < t_{\mathcal{P}_i}^{34-} < \Omega_i^+ < t_{\mathcal{P}_i}^{49}$ and $\Omega_i^+ \leq I_2^+$.

(h) If $i \neq \ell$, then a $\mathsf{PawnSet}.\mathtt{reset()}$ operation is not executed during $[I_0^-, \Omega_i^+]$.

(i) Throughout $[\gamma, \Omega_\ell^+]$, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$.

(j) If $\ell > 1$, $I_2^+ = \Omega_\ell^+ = t_{\mathcal{P}_\ell}^{68}$.

(k) For all $t \in [\gamma, I_2^+)$, $|R(t)| = 1$.

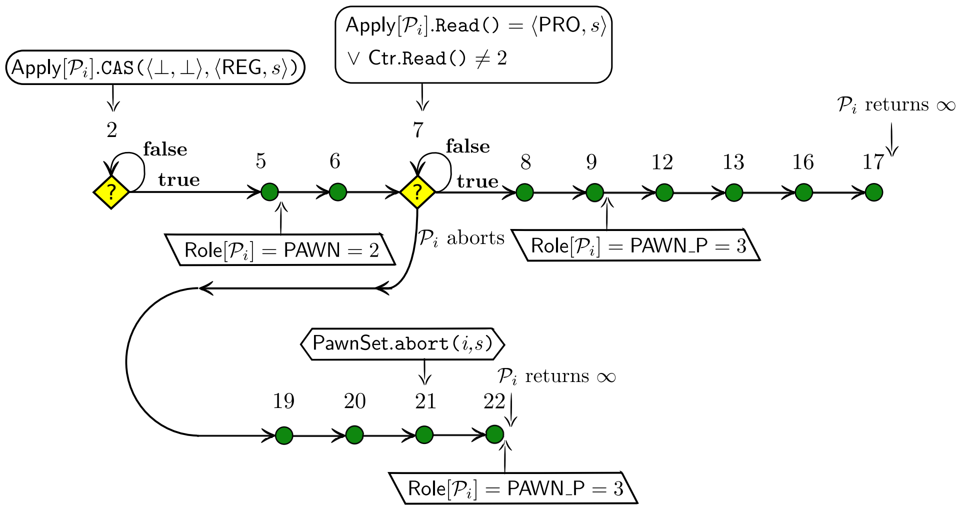


Figure 15: $\mathcal{P}_i$'s call to $\texttt{lock}_{\mathcal{P}_i}\texttt{()}$

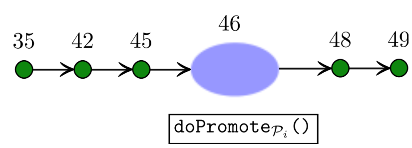


Figure 16: $\mathcal{P}_i$'s call to $\texttt{release}_{\mathcal{P}_i}\texttt{()}$

(l) $R(I_2^+) = \varnothing$ *and at* I_2^+, $\mathsf{Sync1} = \mathsf{Sync2} = \perp$ *and* $\mathsf{PawnSet}$ *is candidate-empty.*

Proof. **Proof of (a)-(h):** We prove Parts (a)-(h) by induction on i. First, we prove Part (a) for $i = 1$. Second, we show that if Part (a) is true for a fixed i, then Parts (b)-(h) are true for i. Finally, we show that if Parts (a)-(h) are true for i, then Part (a) is true for $i + 1$, thus completing the proof.

From Claim D.27(a), no $\mathsf{PawnSet}.\texttt{reset()}$ operation has been executed during $[I_0^-, \Omega_1^-]$. Hence, Part (a) for $i = 1$ holds.

Now we show that if Part (a) is true for a fixed i, then Parts (b)-(h) follow for i.

Proof of Parts (b) and (c) if Part (a) for i is true: Let q be the process that promotes $\mathcal{P}_i$ at Ω_i^-. Then q's $\mathsf{PawnSet}.\texttt{promote()}$ operation in line 65 returned value $\langle \mathcal{P}_i, s\rangle$, where $s \in \mathbb{N}$, and $\Omega_i^- = t_q^{65}$. Then from the semantics of the $\mathsf{PawnSet}$ object it follows that the $\mathcal{P}_i$-th entry of $\mathsf{PawnSet}$ was $\langle \mathsf{REG}, s\rangle = \langle 1, s\rangle$ immediately before Ω_i^-. Then from Claim D.5(b) it follows that some process (say r) executed a $\mathsf{PawnSet}.\texttt{collect(}A\texttt{)}$ operation in line 55 where $A[\mathcal{P}_i] = s$. Then from the code structure, r read $\mathsf{apply}[\mathcal{P}_i] = \langle \mathsf{REG}, s\rangle$ in line 52. By Claim D.6(a) $\mathsf{apply}[\mathcal{P}_i]$ is set to value $\langle \mathsf{REG}, s\rangle$ only by process $\mathcal{P}_i$ when it executes a successful $\mathsf{apply}[\mathcal{P}_i].\texttt{CAS(}\langle\perp,\perp\rangle, \langle \mathsf{REG}, s\rangle\texttt{)}$, therefore $\mathcal{P}_i$ executed the same and broke out of the spin loop of line 2. Note that $t_{\mathcal{P}_i}^2 < t_r^{52} < \Omega_i^- = t_q^{65}$.

Since $\mathsf{Ctr} = 0$ throughout I_0, $\mathsf{Ctr} = 1$ throughout I_1 and $\mathsf{Ctr} = 2$ throughout I_2, it follows that Ctr is increased only at points I_1^- and I_2^- during T. Since $\mathcal{K}$ and $\mathcal{Q}$ are the first two releasers of L and they increased Ctr to 1 and 2, respectively, at I_1^- and I_2^-, respectively, it follows that no other process apart from $\mathcal{K}$ and $\mathcal{Q}$ increases the value of Ctr during T. Since $\Omega_i^- \geq \Omega_1^- = \gamma > I_2^-$ (by Claims D.25(a) and D.25(d) and D.27(a)), $\mathcal{P}_i$ becomes a releaser of L only after I_2^- (the point at which $\mathcal{Q}$ became a releaser of L). Thus, $\mathcal{P}_i$ is not among the first two releasers of L, thus $\mathcal{P}_i \notin \{\mathcal{K}, \mathcal{Q}\}$. Then it follows that $\mathcal{P}_i$ does not increase Ctr. Therefore $\mathcal{P}_i$'s $\mathsf{Ctr}.\texttt{inc()}$ operation in line 5 returns value $2 = \mathsf{PAWN}$, and thus $\mathcal{P}_i$ sets $\mathsf{Role}[\mathcal{P}_i]$ to $2 = \mathsf{PAWN}$ in line 5. Then from the code structure $\mathcal{P}_i$ satisfies the if-condition of line 6 and proceeds to spin in line 7.

Case a - $\mathcal{P}_i$ receives a signal to abort while busy-waiting in line 7: Then $\mathcal{P}_i$ stops spinning in line 7 and executes $\texttt{abort}_{\mathcal{P}_i}\texttt{()}$. Since $\mathcal{P}_i$ last set $\mathsf{Role}[\mathcal{P}_i]$ to PAWN in line 5, it then follows from the code structure that $\mathcal{P}_i$ proceeds to execute lines 18-20, and satisfies the if-condition of line 20, and then executes a $\mathsf{PawnSet}.\texttt{abort(}\mathcal{P}_i, s\texttt{)}$ operation in line 21.

Since a $\mathsf{PawnSet}.\texttt{reset()}$ operation has not been executed during $[I_0^-, \Omega_i^-]$, from Claim D.26, it follows that $\mathcal{P}_i$ did not execute a $\mathsf{PawnSet}.\texttt{abort(}\mathcal{P}_i, s\texttt{)}$ operation in line 21 during $[I_0^-, \Omega_i^-]$, thus $t_{\mathcal{P}_i}^{21} > \Omega_i^-$. Since $\mathcal{P}_i$ has exclusive-registration access to $\mathsf{PawnSet}$ during $[\Omega_i^-, \Omega_i^+]$, and p has not executed any of its cease-release events or reset $\mathsf{PawnSet}$ during $[t_{\mathcal{P}_i}^{2}, t_{\mathcal{P}_i}^{21}]$, and $t_{\mathcal{P}_i}^{2} < \Omega_i^-$, it then follows that $\mathsf{PawnSet}$ was not reset during $[\Omega_i^-, t_{\mathcal{P}_i}^{21}]$. Then since the $\mathcal{P}_i$-th entry of $\mathsf{PawnSet}$ was last changed to $\langle \mathsf{PRO}, s\rangle = \langle 2, s\rangle$ at Ω_i^-, it remains $\langle \mathsf{PRO}, s\rangle$ throughout $[\Omega_i^-, t_{\mathcal{P}_i}^{21}]$. Then $\mathcal{P}_i$'s $\mathsf{PawnSet}.\texttt{abort(}\mathcal{P}_i, s\texttt{)}$ operation in line 21 returns $\texttt{false}$ by the semantics of the $\mathsf{PawnSet}$ object. Then p satisfies the if-condition of line 21, proceeds to set $\mathsf{Role}[\mathcal{P}_i]$ to $\mathsf{PAWN\_P}$ in line 22, and then returns ∞ from its call to $\texttt{abort}_{\mathcal{P}_i}\texttt{()}$ and $\texttt{lock}_{\mathcal{P}_i}\texttt{()}$.

Case b - $\mathcal{P}_i$ does not receive a signal to abort while busy-waiting in line 7:

Recall that process q promotes $\mathcal{P}_i$ at Ω_i^- by executing a $\mathsf{PawnSet}.\texttt{promote()}$ operation in line 65 that returns value $\langle \mathcal{P}_i, s\rangle$, where $s \in \mathbb{N}$. Since processes in the system continue to take steps, process q sets its local variable j to value $\mathcal{P}_i$ in line 65, and proceeds to fail the if-condition of line 66, and then executes line 70 where $\langle j, seq\rangle = \langle \mathcal{P}_i, s\rangle$. Then q executes a $\mathsf{apply}[\mathcal{P}_i].\texttt{CAS(}\langle \mathsf{REG}, s\rangle, \langle \mathsf{PRO}, s\rangle\texttt{)}$ operation in line 70.

Recall that process r read value $\mathsf{apply}[\mathcal{P}_i] = \langle \mathsf{REG}, s\rangle$ in line 52 and $t_{\mathcal{P}_i}^{2} < t_r^{52} < \Omega_i^- = t_q^{65}$. From an inspection of the code, $\mathsf{apply}[\mathcal{P}_i]$ can change from value $\langle \mathsf{REG}, s\rangle$ only to value $\langle \mathsf{PRO}, s\rangle$ and from value $\langle \mathsf{PRO}, s\rangle$ only to value $\langle \bot, \bot\rangle$. Also, $\mathsf{apply}[\mathcal{P}_i]$ can be changed from $\langle \mathsf{PRO}, s\rangle$ to $\langle \bot, \bot\rangle$, only if p executes line 32 or 49. Since p is spinning in line 7 it follows that a $\mathsf{apply}[\mathcal{P}_i].\texttt{CAS(}\langle \mathsf{PRO}, s\rangle, \langle \bot, \bot\rangle\texttt{)}$ operation is not executed during (Ω_i^-, t_q^{70}), and thus $\mathsf{apply}[\mathcal{P}_i] = \langle \mathsf{REG}, s\rangle$ throughout (Ω_i^-, t_q^{70}). Therefore, q executes a successful $\mathsf{apply}[\mathcal{P}_i].\texttt{CAS(}\langle \mathsf{REG}, s\rangle, \langle \mathsf{PRO}, s\rangle\texttt{)}$ operation in line 70, and thus $\mathsf{apply}[\mathcal{P}_i] = \langle \mathsf{PRO}, s\rangle$ at t_q^{70}.

Since $\mathcal{P}_i$ is busy-waiting in line 7 for $\mathsf{apply}[\mathcal{P}_i]$ to change to $\langle \mathsf{PRO}, s\rangle$, it then follows that $\mathcal{P}_i$ busy-waits throughout (Ω_i^-, t_q^{70}), and reads $\mathsf{apply}[\mathcal{P}_i] = \langle \mathsf{PRO}, s\rangle$ when it executes line 7 for the first time after t_q^{70}. Then $\mathcal{P}_i$ breaks out of the spin loop, and then from the code structure, $\mathcal{P}_i$ proceeds to set $\mathsf{Role}[\mathcal{P}_i]$ to $\mathsf{PAWN\_P}$ in line 9, breaks out of the role-loop in line 12, executes line 13 and fails the if-condition of line 13, and executes lines 16-17, and returns from $\texttt{lock}_{\mathcal{P}_i}\texttt{()}$ in line 17 with value ∞. Note that $\Omega_i^- < t_{\mathcal{P}_i}^{9}$.

Proof of Parts (d), (e) and (f) if Part (a) for i is true: Since $\mathcal{P}_i$ is the only releaser of L throughout $[\Omega_i^-,\Omega_i^+)$ (Claim D.27(e)), it follows from Claim D.15 that $\mathcal{P}_i$ has exclusive write-access to objects $\mathsf{Sync1}$ and $\mathsf{Sync2}$ and exclusive registration-access to $\mathsf{PawnSet}$ throughout $[\Omega_i^-,\Omega_i^+)$.

Since $\mathcal{P}_i$ returns from its call to $\texttt{lock}_{\mathcal{P}_i}\texttt{()}$ with value ∞ (by Part (c)), $\mathcal{P}_i$ executes a call to $\texttt{release}_{\mathcal{P}_i}\texttt{()}$ (follows from conditions b and d).

Since $\mathsf{Role}[\mathcal{P}_i] = \mathsf{PAWN\_P}$ when $\mathcal{P}_i$'s call to $\texttt{lock}_{\mathcal{P}_i}\texttt{()}$ returns (by Part (c)), $\mathsf{Role}[\mathcal{P}_i] = \mathsf{PAWN\_P}$ at $t_{\mathcal{P}_i}^{34-}$. Since $\mathsf{Role}[\mathcal{P}_i]$ is unchanged during $[t_{\mathcal{P}_i}^{34}, t_{\mathcal{P}_i}^{49-}]$ (follows from Claim D.4(b)), it follows from the code structure that during $\mathcal{P}_i$'s call to $\texttt{release}_{\mathcal{P}_i}\texttt{(}j\texttt{)}$, $\mathcal{P}_i$ only executes lines 34-35, 42 and 45-50. Then Figure 16 follows.

From an inspection of Figures 15 and 16, $\mathcal{P}_i$ does not execute a call to $\texttt{helpRelease}_{\mathcal{P}_i}\texttt{()}$ or execute a $\mathsf{Ctr}.\texttt{CAS(}1,0\texttt{)}$ operation in line 36 during $\texttt{release}_{\mathcal{P}_i}\texttt{()}$. Then from Claims D.7(a) and D.7(b) $\mathcal{P}_i$'s cease-release events $\phi_{\mathcal{P}_i}$ and $\tau_{\mathcal{P}_i}$ do not occur. Since $\mathcal{P}_i$ executes a call to $\texttt{doPromote}_{\mathcal{P}_i}\texttt{()}$ only in line 46, it follows from Claim D.19 that exactly one cease-release event among $\pi_{\mathcal{P}_i}$ and $\theta_{\mathcal{P}_i}$ occurs during $\mathcal{P}_i$'s call to $\texttt{doPromote}_{\mathcal{P}_i}\texttt{()}$. Hence, Part (d) follows. Then Ω_i^+ is the point when cease-release event $\pi_{\mathcal{P}_i}$ or $\theta_{\mathcal{P}_i}$ occurs. From an inspection of Figures 15 and 16 and the code, it is clear that $\mathcal{P}_i$ does not change $\mathsf{Sync1}$ or $\mathsf{Sync2}$ during $\texttt{lock}_{\mathcal{P}_i}\texttt{()}$ and $\texttt{release}_{\mathcal{P}_i}\texttt{(.)}$ Therefore, $\mathcal{P}_i$ does not change $\mathsf{Sync1}$ or $\mathsf{Sync2}$ during $[\Omega_i^-, \Omega_i^+]$.

Proof of Part (g) if Part (a) for i is true: As argued in Part (b) and (c), $t^5_{\mathcal{P}_i} < \Omega_i^-$, and $\Omega_i^- < t^9_{\mathcal{P}_i}$ or $\Omega_i^- < t^{21}_{\mathcal{P}_i}$. Since $t^9_{\mathcal{P}_i} < t^{34}_{\mathcal{P}_i}$ and $t^{21}_{\mathcal{P}_i} < t^{34}_{\mathcal{P}_i}$, it then follows that $t^5_{\mathcal{P}_i} < \Omega_i^- < t^{34}_{\mathcal{P}_i}$.

From Part (d), exactly one cease-release event among $\pi_{\mathcal{P}_i}$ and $\theta_{\mathcal{P}_i}$ occurs during $\mathcal{P}_i$'s call to $\texttt{doPromote}_{\mathcal{P}_i}\texttt{()}$. If cease-release event $\theta_{\mathcal{P}_i}$ occurs then Ω_i^+ is the point when $\mathcal{P}_i$'s cease-release event $\theta_{\mathcal{P}_i}$ occurs,i.e, $\Omega_i^+ = t^{68}_{\mathcal{P}_i}$. Then $\mathcal{P}_i$ changes Ctr to 0 and the Ctr-cycle interval T ends at $\Omega_i^+ = t^{68}_{\mathcal{P}_i} = I_2^+$.

If cease-release event $\pi_{\mathcal{P}_i}$ occurs then Ω_i^+ is the point when $\mathcal{P}_i$'s cease-release event $\pi_{\mathcal{P}_i}$ occurs,i.e, $\Omega_i^+ = t^{65}_{\mathcal{P}_i} < I_2^+$.

Since $\mathcal{P}_i$ calls $\texttt{doPromote}_{\mathcal{P}_i}\texttt{()}$ only in line 46 (by inspection of Figure 16), it then follows that $\Omega_i^+ \in \left\{t^{65}_{\mathcal{P}_i}, t^{68}_{\mathcal{P}_i}\right\} < t^{49}_{\mathcal{P}_i}$. Thus, Part (g) holds.

Proof of Part (h) if Part (a) for i is true: As argued in Part (f), exactly one cease-release event among $\pi_{\mathcal{P}_i}$ and $\theta_{\mathcal{P}_i}$ occurs during $\mathcal{P}_i$'s call to $\texttt{doPromote}_{\mathcal{P}_i}\texttt{()}$. If cease-release event $\theta_{\mathcal{P}_i}$ occurs then Ω_i^+ is the point when $\mathcal{P}_i$'s cease-release event $\theta_{\mathcal{P}_i}$ occurs,i.e, $\Omega_i^+ = t^{68}_{\mathcal{P}_i}$. Then $\mathcal{P}_i$ changes Ctr to 0 and the Ctr-cycle interval T ends at $\Omega_i^+ = t^{68}_{\mathcal{P}_i}$, and thus $\ell = i$. This is a contradiction to the assumption $i \neq \ell$, hence $\mathcal{P}_i$'s cease-release event $\pi_{\mathcal{P}_i}$ occurs during $\mathcal{P}_i$'s call to $\texttt{doPromote}_{\mathcal{P}_i}\texttt{()}$. Then Ω_i^+ is the point when $\mathcal{P}_i$'s cease-release event $\pi_{\mathcal{P}_i}$ occurs,i.e, $\Omega_i^+ = t^{65}_{\mathcal{P}_i}$. From an inspection of Figures 15 and 16 and the code, it follows that $\mathcal{P}_i$ does not execute a $\mathsf{PawnSet}.\texttt{reset()}$ operation during $[t^2_{\mathcal{P}_i}, t^{46-}_{\mathcal{P}_i}]$, and $\mathcal{P}_i$ calls $\texttt{doPromote}_{\mathcal{P}_i}\texttt{()}$ only in line 46. Since $\Omega_i^+ = t^{65}_{\mathcal{P}_i}$, from an inspection of the code of $\texttt{doPromote}_{\mathcal{P}_i}\texttt{()}$, $\mathcal{P}_i$ does not execute a $\mathsf{PawnSet}.\texttt{reset()}$ operation during $[t^{65-}_{\mathcal{P}_i}, t^{68-}_{\mathcal{P}_i}]$. Then $\mathcal{P}_i$ does not execute a $\mathsf{PawnSet}.\texttt{reset()}$ operation during $[\Omega_i^-, \Omega_i^+]$.

Since $\mathcal{P}_i$ is the only releaser of L throughout $[\Omega_i^-,\Omega_i^+)$ (Claim D.27(e)), it follows from Claim D.15 that $\mathcal{P}_i$ has exclusive registration-access to $\mathsf{PawnSet}$ throughout $[\Omega_i^-,\Omega_i^+)$. Then since no $\mathsf{PawnSet}.\texttt{reset()}$ operation was executed during $[I_0^-, \Omega_i^-]$, and $\mathcal{P}_i$ does not execute a $\mathsf{PawnSet}.\texttt{reset()}$ operation during $[\Omega_i^-, \Omega_i^+]$, it follows that no $\mathsf{PawnSet}.\texttt{reset()}$ operation is executed during $[I_0^-, \Omega_i^+]$. Hence, Part (h) holds.

Finally, we show that if Parts (a)-(h) are true for i, then Part (a) is true for $i+1$, thus completing the proof. From Part (h) for i, no $\mathsf{PawnSet}.\texttt{reset()}$ operation has been executed during $[I_0^-, \Omega_i^+]$. From Claim D.27(d), $\Omega_i^+ = \Omega_{i+1}^-$. Then Part (a) for $i+1$ holds.

Proof of (i): From Claim D.27(a), $\mathsf{Sync1} = \mathsf{Sync2} = \perp$ at $\Omega_1^- = \gamma$. From Claims D.27(a) and D.27(d), it follows that $\gamma = \Omega_1^- < \Omega_1^+ = \Omega_2^- < \Omega_2^+ = \Omega_3^- \ldots < \Omega_{\ell-1}^+ = \Omega_\ell^- < \Omega_\ell^+$.

From Claim D.27(e), for all $t \in [\Omega_i^-, \Omega_i^+)$, $R(t) \in \{\mathcal{P}_i\}$. Then $\mathcal{P}_i$ has exclusive write-access to $\mathsf{Sync1}$ and $\mathsf{Sync2}$ throughout $[\Omega_i^-, \Omega_i^+)$. Since $\mathcal{P}_i$ does not change $\mathsf{Sync1}$ or $\mathsf{Sync2}$ during $[\Omega_i^-, \Omega_i^+]$ (Part (f)), it then follows that $\mathsf{Sync1} = \mathsf{Sync2} = \perp$ throughout $[\Omega_1^-, \Omega_\ell^+] = [\gamma, \Omega_\ell^+]$.

Proof of (j): As argued in Part (f), exactly one cease-release event among $\pi_{\mathcal{P}_\ell}$ and $\theta_{\mathcal{P}_\ell}$ occurs during $\mathcal{P}_\ell$'s call to $\texttt{doPromote}_{\mathcal{P}_\ell}\texttt{()}$. If cease-release event $\pi_{\mathcal{P}_\ell}$ occurs then $\mathcal{P}_\ell$ promotes some process, and thus the number of processes that get promoted during T is larger than ℓ, which contradicts the definition of ℓ. Hence, cease-release event $\theta_{\mathcal{P}_\ell}$ occurs during $\texttt{doPromote}_{\mathcal{P}_\ell}\texttt{()}$ and Ω_ℓ^+ is the point when cease-release event $\theta_{\mathcal{P}_\ell}$ occurs,i.e, $\Omega_\ell^+ = t^{65}_{\mathcal{P}_\ell}$. Since Ctr is changed from 2 to 0 when $\theta_{\mathcal{P}_\ell}$ occurs, the Ctr-cycle interval T ends at $\Omega_\ell^+ = t^{68}_{\mathcal{P}_\ell}$, and thus $I_2^+ = \Omega_\ell^+ = t^{68}_{\mathcal{P}_\ell}$.

Proof of (k) and (l):

Case a - $\ell = 0$: Consider the first PawnSet.`promote()` operation at γ. Since $\ell = 0$, the PawnSet.`promote()` operation at γ returns value $\langle\perp,\perp\rangle$. Then from Claim D.25(j), it follows that $\mathcal{B}$'s cease-release event $\theta_{\mathcal{B}}$ occurs at $t' = t_{\mathcal{B}}^{68} \geq \gamma$, and throughout $[\gamma, t']$ no process is promoted, and for all $t \in [\gamma, t')$, $R(t) = \{\mathcal{B}\}$. Since Ctr is changed from 2 to 0 when $\theta_{\mathcal{B}}$ occurs, the Ctr-cycle interval T ends at $t' = t_{\mathcal{B}}^{68}$, and thus $I_2^+ = t_{\mathcal{B}}^{68} = t'$. Then for all $t \in [\gamma, t') = [\gamma, I_2^+)$, $|R(t)| = 1$.

From an inspection of Figure 14 and the code, it follows that $\mathcal{B}$ executed a PawnSet.`reset()` operation in line 67 during $[\gamma, t']$, and thus PawnSet is candidate-empty immediately after. Since for all $t \in [\gamma, t')$, $R(t) = \{\mathcal{B}\}$, $\mathcal{B}$ has exclusive registration-access to PawnSet throughout $[\gamma, t')$ (follows from Claim D.15). Then it follows that PawnSet is candidate-empty at $t' = I_2^+$.

Since for all $t \in [\gamma, t')$, $R(t) = \{\mathcal{B}\}$, $\mathcal{B}$ has exclusive write-access to Sync1 and Sync2 throughout $[\gamma, t')$ (follows from Claim D.15). Since Sync1 $=$ Sync2 $= \perp$ at γ (Claim D.25(f)), and $\mathcal{B}$ does not write to Sync1 and Sync2 during $[\gamma, t']$, it follows that Sync1 $=$ Sync2 $= \perp$ throughout $[\gamma, t'] = [\gamma, I_2^+]$.

Case b - $\ell \geq 1$: From Part (j), $I_2^+ = \Omega_\ell^+ = t_{\mathcal{P}_\ell}^{68}$. Then from Part (i), it follows that Sync1 $=$ Sync2 $= \perp$ throughout $[\gamma, I_2^+]$, and from Claim D.27(e), it follows that for all $t \in [\Omega_1^-, \Omega_\ell^+] = [\gamma, I_2^+)$, $|R(t)| = 1$. Since $\mathcal{P}_\ell$ ceases to be a releaser of L at Ω_ℓ^+, $R(I_2^+) = \varnothing$.

Since $\Omega_\ell^+ = t_{\mathcal{P}_\ell}^{68}$, $\mathcal{P}_i$ executed line 68 and before that line 67. Hence, $\mathcal{P}_\ell$ executed a PawnSet.`reset()` operation at $t_{\mathcal{P}_\ell}^{67} < \Omega_\ell^+$. Since $t_{\mathcal{P}_\ell}^{67} > t_{\mathcal{P}_\ell}^{34-}$ and $t_{\mathcal{P}_\ell}^{34-} > \Omega_\ell^-$ (by Part (g)), it follows that $t_{\mathcal{P}_\ell}^{67} > \Omega_\ell^-$. Hence, $\mathcal{P}_\ell$ executed a PawnSet.`reset()` operation at $t_{\mathcal{P}_\ell}^{67} \in [\Omega_\ell^-, \Omega_\ell^+]$. Since $\mathcal{P}_\ell$ is the only releaser of L throughout $[\Omega_\ell^-,\Omega_\ell^+)$ (Claim D.27(e)), it follows from Claim D.15 that $\mathcal{P}_\ell$ has exclusive registration-access to PawnSet throughout $[\Omega_\ell^-,\Omega_\ell^+)$. Then it follows that PawnSet is candidate-empty at $\Omega_\ell^- = I_2^+$. ☐

Claim D.29. *$R(I_0^-) = \varnothing$ and at I_0^-, Sync1 $=$ Sync2 $= \perp$ and PawnSet is candidate-empty for any Ctr-cycle interval T during history H.*

Proof. Let T^k denote the k-th Ctr-cycle interval T during history H. We give a proof by induction over the integer k. **Basis -** At I_0^- for T^1, the claim holds trivially since all variables are at their initial values (Sync1 $=$ Sync2 $= \perp$ and PawnSet is candidate-empty).

Induction Step - By the induction hypothesis, at I_0^- for T^{k-1}, $R(I_0^-) = \varnothing$, and Sync1 $=$ Sync2 $= \perp$ and PawnSet is candidate-empty. Since T^k begins immediately after T^{k-1} ends, to prove our claim we need to show that, when T^{k-1} ends, there are no releasers of L and Sync1 $=$ Sync2 $= \perp$ and PawnSet is candidate-empty. The time interval T^{k-1} ends either at time I_1^+ or time I_2^+.

Case a - T^{k-1} ends at time I_1^+: Then $I_2 = \varnothing$. From Claim D.23(f) it follows that $\mathcal{K}$ is the only releaser of L during I_1. Since $I_2 = \varnothing$, it then follows from Claim D.23(e), that $\mathcal{K}$'s Ctr.`CAS(1,0)` operation in line 36 is successful, and the interval I_1 as well as T^{k-1} ends at time $t_{\mathcal{K}}^{36}$. Then $\mathcal{K}$'s cease-release event $\phi_{\mathcal{K}}$ occurs at $t_{\mathcal{K}}^{36} = I_1^+$, and thus there are no releasers of L immediately after T^{k-1} ends. And from Claim D.23(g), it follows that Sync1 $=$ Sync2 $= \perp$ and PawnSet is candidate-empty when T^{k-1} ends.

Case b - T^{k-1} ends at time I_2^+: Then $I_2 \neq \varnothing$. Then our proof obligation follows immediately from Claim D.28(l). ☐

Note that in the following claims, notations I_0, I_1, I_2, λ, γ, Ω_i, $\mathcal{K}$, $\mathcal{Q}$ and $\mathcal{P}_i$ are defined relative to a Ctr-cycle interval, as was defined previously in pages 38, 43 and 47. The exact Ctr-cycle interval is clear from the context of the discussion.

Lemma D.30. *The mutual exclusion property holds during history H.*

Proof. For the purpose of a contradiction assume that at time t, two processes (say p and q) are poised to execute a call to $\mathsf{L}.\texttt{release()}$. From Claim D.13(b), it follows that both p and q are releasers of L at t. Consider the Ctr-cycle interval T such that $t \in T$.

From Claim D.29 it follows that at I_0^-, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$ and $\mathsf{PawnSet}$ is candidate-empty, and $R(I_0^-) = \varnothing$. Then from Claims D.23(a), D.23(f), D.25(a), D.25(e) and D.25(k), it follows that during T, lock L has two releasers only during $[I_2^-, \lambda)$. Then $t \in [I_2^-, \lambda)$. Also from Claim D.25(a), for all $t \in [I_2^-, \lambda)$, $R(t) = \{\mathcal{K}, \mathcal{Q}\}$. Then $\{p, q\} = \{\mathcal{K}, \mathcal{Q}\}$. Let $p = \mathcal{K}$ and $q = \mathcal{Q}$ without loss of generality.

Recall that I_2^- is the point in time when $\mathcal{Q}$ increases Ctr from 1 to 2 and sets $\mathsf{Role}[\mathcal{Q}]$ to QUEEN in line 5. Since q's call to $\texttt{lock()}$ returned a non-$\bot$ value, it follows from an inspection of Figure 10, that $\mathcal{Q}$ returned either in line 17 or line 27. Then $\mathcal{Q}$ either read a non-$\bot$ value from $\mathsf{Sync1}$ in line 14 or $\mathcal{Q}$ failed the $\mathsf{Sync1}.\texttt{CAS}(\bot, \infty)$ operation in line 26. Since $\mathsf{Sync1} = \bot$ at I_2^- (by Claim D.24(a)), and $I_2^- = t_{\mathcal{Q}}^5$, it then follows that $\mathsf{Sync1}$ is changed to a non-$\bot$ value during $[I_2^-, t]$. Clearly, $\mathcal{Q}$ does not change $\mathsf{Sync1}$ during $[I_2^-, t]$.

Recall that I_1^- is the point in time when $\mathcal{K}$ increases Ctr from 0 to 1 and sets $\mathsf{Role}[\mathcal{K}]$ to KING in line 5. It follows from an inspection of Figure 8, that $\mathcal{K}$ does not change $\mathsf{Sync1}$ during $\texttt{lock}_{\mathcal{K}}\texttt{()}$, and thus during $[I_1^-, t]$. Since $\mathsf{Sync1}$ is changed to a non-$\bot$ only by a releaser of L (by Claim D.15) and $\mathsf{Sync1} = \bot$ at I_2^-, and the only releasers of L during $[I_2^-, t]$ do not change $\mathsf{Sync1}$, it then follows that $\mathsf{Sync1} = \bot$ throughout $[I_2^-, t]$. Hence, a contradiction. ☐

Claim D.31. *Consider an arbitrary* Ctr*-cycle interval* T*.*

(a) If p is collected during T and p does not abort, then p is promoted and notified during T.

(b) If $\mathsf{apply}[p] = \langle \mathsf{REG}, s \rangle$ at I_0^-, where $s \in N$, and p does not abort and p does not increase Ctr*, then p is notified during T.*

Proof. **Proof of (a):** From Claim D.29 it follows that at I_0^-, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$ and $\mathsf{PawnSet}$ is candidate-empty, and $R(I_0^-) = \varnothing$. Then from Claim D.25(k), it follows that exactly one call to $\texttt{doCollect()}$ is executed during T by a process $q \in \{\mathcal{K}, \mathcal{Q}\}$. Since processes are collected only during a call to $\texttt{doCollect()}$, $q \in \{\mathcal{K}, \mathcal{Q}\}$ collects p during $\texttt{doCollect}_q\texttt{()}$ during T. And q does so by executing a $\mathsf{PawnSet}.\texttt{collect}(A)$ operation in line 55, where $A[p] = s \in \mathbb{N}$, and sets the p-th entry of $\mathsf{PawnSet}$ to $\langle \mathsf{REG}, s \rangle$. Since a $\mathsf{PawnSet}.\texttt{promote()}$ that returns $\langle \bot, \bot \rangle$ is executed at $t_{\mathcal{P}_\ell}^{65}$ during T, it then follows from the semantics of the $\mathsf{PawnSet}$ object that p was promoted during T. Then $p = \mathcal{P}_i$, for some $i \leq \ell$. Note that T does not end during $[\Omega_i^-, \Omega_i^+)$.

We now show that p is also notified of its promotion during T. The process (say r) that promoted p by executing a $\mathsf{PawnSet}.\texttt{promote()}$ operation in line 65, also goes on to notify p of its promotion by executing a $\mathsf{apply}[p].\texttt{CAS}(\langle \mathsf{REG}, s \rangle, \langle \mathsf{PRO}, s \rangle)$ operation in line 70. Since p does not abort, it follows from an inspection of Figure 15 and the code, that p spins on $\mathsf{apply}[p]$ in line 7 until its notification. Then p executes line 9 at $t_p^9 > t_r^{70} > t_r^{65} = \Omega_i^-$. Since $t_p^9 < \Omega_i^+$ and T does not end before Ω_i^+, it follows that p is notified during T.

Proof of (b): Since p does not increase Ctr it follows that p reads $\mathsf{Ctr} = 2$ every time it executes a $\mathsf{Ctr}.\texttt{inc()}$ operation in line 5, and sets $\mathsf{Role}[p] = \mathsf{PAWN}$ in line 5. Then p satisfies the if-condition of line 6 and spins on variables $\mathsf{apply}[p]$ and Ctr in line 7. Since Ctr is only changed to 0 at the end of T, it follows that $\mathsf{Ctr} = 2$ throughout $[t_p^5, I_2^+)$. Then p busy-waits in the spin loop of line 7 until the end of T, or if it reads value $\langle \mathsf{PRO}, s \rangle$, for some $s \in \mathbb{N}$, from $\mathsf{apply}[p]$ in line 7 during T. Now, $\mathsf{apply}[p]$ is changed to value $\langle \mathsf{PRO}, s \rangle$ by some process other than p, only if that process notifies p, i.e., executes a successful $\mathsf{apply}[p].\texttt{CAS}(\langle \mathsf{REG}, s \rangle, \langle \mathsf{PRO}, s \rangle)$ operation in line 70. We now show that p is notified during T.

From Claim D.29 it follows that at I_0^-, $\mathsf{Sync1} = \mathsf{Sync2} = \perp$ and $\mathsf{PawnSet}$ is candidate-empty, and $R(I_0^-) = \varnothing$. Then from Claim D.25(k), it follows that exactly one call to `doCollect()` is executed during T by a process $q \in \{\mathcal{K}, \mathcal{Q}\}$. Consider the point when q reads $\mathsf{apply}[p]$ in line 52. If q reads a value different from $\langle \mathsf{REG}, s\rangle$, then some process must have notified p during $[t_p^2, t_q^{52}]$, and since $I_0^- < t_p^2$ and $t_q^{52} \in T$, our claim holds. If q reads the value $\langle \mathsf{REG}, s\rangle$ from apply[p], then q collects p during T by executing a $\mathsf{PawnSet}.\mathtt{collect}(A)$ operation, where $A[p] = s$, in line 55 during T. Thus, our claim follows from Part (a). ☐

Claim D.32. *If p registered itself in line 2, and incurred $\mathcal{O}(1)$ RMRs in the process, and p does not abort, and all processes in the system continue to take steps, then*

(a) p finishes its call to $\mathtt{lock}_p\mathtt{()}$ *and returns a non-$\perp$ value.*

(b) p incurs $\mathcal{O}(1)$ RMRs in expectation during its call to $\mathtt{lock}_p\mathtt{()}$*.*

Proof. **Proof of (a) and (b):** From an inspection of the code of $\mathtt{lock}_p\mathtt{()}$, p incurs a constant number of RMRs while executing all other lines of $\mathtt{lock}_p\mathtt{()}$ except while busy-waiting in lines 2, 7 and 14.

Consider p's call to $\mathtt{lock}_p\mathtt{()}$. By assumption of the claim, p registered itself in line 2 by executing a successful $\mathsf{apply}[p].\mathtt{CAS}(\langle\perp,\perp\rangle, \langle \mathsf{REG}, s\rangle)$ operation in line 2, and incurred $\mathcal{O}(1)$ RMRs in the process. Then p proceeds to execute a $\mathsf{Ctr}.\mathtt{inc()}$ operation in line 5, and stores the returned value in $\mathsf{Role}[p]$. A $\mathsf{Ctr}.\mathtt{inc()}$ operation returns values in $\{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN}, \perp\}$. If it returns $\perp$, p repeats the role-loop, and executes another $\mathsf{Ctr}.\mathtt{inc()}$ operation in line 5. From Claim A.2, it follows that p repeats the role-loop only a constant number of times before its $\mathsf{Ctr}.\mathtt{inc()}$ operation returns a non-$\perp$ value.

Case a - p executes a $\mathsf{Ctr}.\mathtt{inc()}$ operation in line 5 that returns KING. Then p sets $\mathsf{Role}[p] = \mathsf{KING}$ in line 5. Then from the code structure p does not busy-wait on any variables, and proceeds to return ∞ in line 17, and thus incurs only $\mathcal{O}(1)$ RMRs. Hence, (a) and (b) hold.

Case b - p executes a $\mathsf{Ctr}.\mathtt{inc()}$ operation in line 5 that returns QUEEN. Then p increments Ctr from 1 to 2 in line 5 and sets $\mathsf{Role}[p] = \mathsf{QUEEN}$ in line 5. Then from the code structure p proceeds to busy-wait on $\mathsf{Sync1}$ in line 14. Since p increased Ctr from 1 to 2, $t_p^{14} = I_2^-$ for some Ctr-cycle interval T. From Claim D.29 it follows that at I_0^-, $\mathsf{Sync1} = \mathsf{Sync2} = \perp$ and $\mathsf{PawnSet}$ is candidate-empty, and $R(I_0^-) = \varnothing$. Then from Claim D.24(g) it follows that p does not starve in line 14. Since p does not abort, it follows from an inspection of Figure 10 and the code, that p returns a non-$\perp$ value in line 17, and p does not change $\mathsf{Sync1}$. Hence, we have shown that Part (a) holds. Apart from p, the only releasers of L during T are $\{\mathcal{K}, \mathcal{P}_1, \ldots, \mathcal{P}_\ell\}$, where ℓ is the number of promotions during T. From an inspection of Figures 8, 9, 15, 16 and the code, it follows that only $\mathcal{K}$ possibly writes a non-$\perp$ value to $\mathsf{Sync1}$ during T in line 37. Since $\mathsf{Sync1}$ is written to only be a releaser of L, and $t_p^{14} \in T$, it then follows that $\mathsf{Sync1}$ is changed to a non-$\perp$ value at most once during T. Then p incurs at most one RMR while busy-waiting on $\mathsf{Sync1}$. Hence, we have shown that Part (b) holds.

Case c - p executes a $\mathsf{Ctr}.\mathtt{inc()}$ operation in line 5 that returns PAWN. Then p found Ctr to be 2 in line 5 and set $\mathsf{Role}[p] = \mathsf{PAWN}$ in line 5. Then from the code structure p proceeds to busy-wait on $\mathsf{apply}[p]$ and Ctr in line 7.

We now show that p does not starve while busy-waiting in line 7. Since $\mathsf{Ctr} = 2$ at t_p^5, it follows that $t_p^5 \in T$ for some Ctr-cycle interval T.

Subcase (i) - $\mathsf{apply}[p] = \langle \mathsf{REG}, s\rangle$ at I_0^- during T, for some $s \in \mathbb{N}$. Then from Claim D.31(b), p is notified during T. Since p is notified during T and p does not abort, it follows that p does not change $\mathsf{apply}[p]$, and thus $\mathsf{apply}[p]$ is changed from $\langle \mathsf{REG}, s\rangle$ to $\langle \mathsf{PRO}, s\rangle$ when p is notified. Since $\mathsf{apply}[p]$ is changed from $\langle \mathsf{PRO}, s\rangle$ to some other value only by p, it then follows that $\mathsf{apply}[p]$ remains $\langle \mathsf{PRO}, s\rangle$ when p reads $\mathsf{apply}[p]$ for the first time after p was notified. Then p incurs one RMR when it reads $\mathsf{apply}[p]$ in line 7 after its notification, breaks out of the spin loop of line 7, proceeds to satisfy the if-condition of line 8, and sets $\mathsf{Role}[p] = \mathsf{PAWN\_P}$ in line 9, and proceeds to return ∞ in line 17. Then we have shown Parts (a) and (b) hold.

Subcase (ii) - $\mathsf{apply}[p] \neq \langle \mathsf{REG}, s\rangle$ at I_0^- during T, for some $s \in \mathbb{N}$. Consider the only call to `doCollect()` during T by $q \in \{\mathcal{K}, \mathcal{Q}\}$. If p registered itself (i.e., executed its $\mathsf{apply}[p]$.`CAS(`$\langle\perp,\perp\rangle$,$\langle \mathsf{REG}, s\rangle$`)` operation in line 2) before q reads $\mathsf{apply}[p]$ in line 52 during $\texttt{doCollect}_q\texttt{()}$), then q collects p during T. Then from Claim D.31(a), p is collected and promoted during T, and eventually notified. Then Parts (a) and (b) hold as argued in **Subcase (i)**.

If p registers itself after q attempts to acknowledge p during T, then no process changes $\mathsf{apply}[p]$ during T. Then p continues to busy-wait in line 7, until the Ctr-cycle interval T ends and Ctr is reset to 0.

If Ctr is increased to 2 before p reads Ctr again in line 7, then let T' be the Ctr-cycle interval that starts when Ctr was reset to 0 at the end of T. Since $\mathsf{apply}[p]$ was changed to a non-$\langle \mathsf{REG}, s\rangle$ value before the start of T', it follows that $\mathsf{apply}[p] = \langle \mathsf{REG}, s\rangle$ at the start of T'. Then from Claim D.31(b), p is acknowledged, collected, promoted during T', and eventually notified. Then Parts (a) and (b) hold as argued in **Subcase (i)**.

If $\mathsf{Ctr} \neq 2$ when p reads Ctr again in line 7, then p incurs one RMR in line 7, breaks out of the spin loop, and proceeds to execute line 8. If p satisfies the if-condition of line 8, then p has been acknowledged during some Ctr-cycle interval T''. Then from Claim D.31(a), p is collected, promoted during T'', and eventually notified. Then Parts (a) and (b) hold as argued in **Subcase (i)**. If p fails the if-condition of line 8, then p proceeds to repeat the role-loop. Consider p's second iteration of the role-loop. If p sets $\mathsf{Role}[p] = \{\mathsf{KING}, \mathsf{QUEEN}\}$ in line 5, then Parts (a) and (b) hold as argued in **Case a** and **Case b**. If p sets $\mathsf{Role}[p] = \mathsf{PAWN}$ in line 5, then it follows that $t_p^5 \in T'''$, for some Ctr-cycle interval T''', such that $\mathsf{apply}[p] = \langle \mathsf{REG}, s\rangle$ at I_0^- for T'''. Parts (a) and (b) hold as argued in **Case c(i)**. ☐

Lemma D.33. *If all processes in the system continue to take steps and p does not abort, then*

(a) p finishes its call to $\texttt{lock}_p\texttt{()}$ *and returns a non-$\perp$ value.*

(b) p incurs $\mathcal{O}(1)$ RMRs in expectation during its call to $\texttt{lock}_p\texttt{()}$*.*

Proof. From an inspection of $\texttt{lock}_p\texttt{()}$, p incurs a constant number of RMRs while executing all other lines of $\texttt{lock}_p\texttt{()}$ except while busy-waiting in lines 2, 7 and 14.

Consider p's call to $\texttt{lock}_p\texttt{()}$. Process p first attempts to register itself in line 2, by attempting to execute an $\mathsf{apply}[p]$.`CAS(`$\langle\perp,\perp\rangle$,$\langle \mathsf{REG}, s\rangle$`)` operation. Now, $\mathsf{apply}[p]$ is changed from $\langle\perp,\perp\rangle$ to a non-$\langle\perp,\perp\rangle$ value only by p (Claim D.6(a)). If $\mathsf{apply}[p] = \langle\perp,\perp\rangle$ at t_p^{2-}, then p executes a successful $\mathsf{apply}[p]$.`CAS(`$\langle\perp,\perp\rangle$, $\langle \mathsf{REG}, s\rangle$`)` operation in line 2 and incurs only one RMR. Then our claims follow immediately from Claims D.32(a) and D.32(b).

If $\mathsf{apply}[p] \neq \langle\perp,\perp\rangle$ at t_p^{2-}, it follows that some process p' executed a successful $\mathsf{apply}[p]$.`CAS(`$\langle\perp,\perp\rangle$, $\langle \mathsf{REG}, s'\rangle$`)` in line 2 during $\texttt{lock}_p\texttt{()}$, and $\mathsf{apply}[p] \neq \langle\perp,\perp\rangle$ throughout $[t_{p'}^2, t_p^{2-}]$. Since calls to $\texttt{lock}_p\texttt{()}$ are not executed concurrently, it follows that p' has completed its call to $\texttt{lock}_p\texttt{()}$ during $[t_{p'}^2, t_p^{2-}]$.

Case 1 - p''s call to $\texttt{lock}_p\texttt{()}$ returned $\perp$. Then it follows from the code structure that p' executed a call to $\texttt{abort}_p\texttt{()}$ and returned from line 18 or 33. Since p executed a successful

$\mathsf{apply}[p]$.CAS($\langle\perp,\perp\rangle$, $\langle\mathsf{REG}, s'\rangle$) in line 2, p' could not have aborted while busy-waiting on line 2, and thus p' aborted while busy-waiting in line 7 or 14. Then p' executed line 3, and set its local variable $p'.flag$ to **true**, and thus p could not have returned $\perp$ from line 18 during $\mathtt{abort}_p$(). Then p' returned $\perp$ in line 33, and thus p' executed operations $\mathsf{apply}[p]$.CAS($\langle\mathsf{REG}, s'\rangle$,$\langle\mathsf{PRO}, s'\rangle$) (in line 19), and $\mathsf{apply}[p]$.CAS($\langle\mathsf{PRO}, s'\rangle$,$\langle\perp,\perp\rangle$) (in line 32). Since, $\mathsf{apply}[p]$ can be changed from $\langle\mathsf{REG}, s'\rangle$ only to $\langle\mathsf{PRO}, s'\rangle$, and from $\langle\mathsf{PRO}, s'\rangle$ only to $\langle\perp,\perp\rangle$, it then follows that p' executes a successful $\mathsf{apply}[p]$.CAS($\langle\mathsf{PRO}, s'\rangle$,$\langle\perp,\perp\rangle$) (in line 32). Then p' eventually resets $\mathsf{apply}[p]$ during its $\mathtt{lock}_p$() call. Since $\mathsf{apply}[p] \neq \langle\perp,\perp\rangle$ throughout $[t^2_{p'}, t^{2-}_p]$ and p' completed its call to $\mathtt{lock}_p$() during $[t^2_{p'}, t^{2-}_p]$, we have a contradiction.

Case 2 - p''s call to $\mathtt{lock}_p$() returned a non-$\perp$ value. Then from the code structure p' executed operations $\mathsf{apply}[p]$.CAS($\langle\mathsf{REG}, s'\rangle$,$\langle\mathsf{PRO}, s'\rangle$) (in line 16 or line 19) before returning from its call to $\mathtt{lock}_p$(). Since $\mathsf{apply}[p]$ can be changed from $\langle\mathsf{REG}, s'\rangle$ only to $\langle\mathsf{PRO}, s'\rangle$, and from $\langle\mathsf{PRO}, s'\rangle$ only to $\langle\perp,\perp\rangle$ and only by a process with pseudo-ID p, it then follows that $\mathsf{apply}[p] = \langle\mathsf{PRO}, s'\rangle$ when p''s $\mathtt{lock}_p$() returns. Then it also follows that $\mathsf{apply}[p] = \langle\mathsf{PRO}, s'\rangle$ until a process with pseudo-ID p executes an $\mathsf{apply}[p]$.CAS($\langle\mathsf{PRO}, s'\rangle$,$\langle\perp,\perp\rangle$) operation.

Since p' won the lock L, it follows that some process, say r, eventually executes a call to $\mathtt{release}_p(j)$, for some integer j. Since a call to $\mathtt{release}_p(j)$ is wait-free and all processes continue to take steps, it follows that eventually r executes lines 48 and 49 where it reads value $\langle\mathsf{PRO}, s'\rangle$ from $\mathsf{apply}[p]$ in line 48 and resets $\mathsf{apply}[p]$ with a $\mathsf{apply}[p]$.CAS($\langle\mathsf{PRO}, s'\rangle$,$\langle\perp,\perp\rangle$) operation in line 49. Since p does not abort, and no other process calls $\mathtt{lock}_p$() concurrently, it then follows that eventually p executes a successful $\mathsf{apply}[p]$.CAS($\langle\perp,\perp\rangle$,$\langle\mathsf{REG}, s\rangle$) operation in line 2. Since $\mathsf{apply}[p]$ changed only once from $\langle\mathsf{PRO}, s'\rangle$ to $\langle\perp,\perp\rangle$ while p busy-waited in line 2, it follows that p incurs $\mathcal{O}(1)$ RMRs during the entire process. Then our claims follow immediately from Claims D.32(a) and D.32(b). ☐

Lemma D.34. *The abort-way is wait- free.*

Proof. The abort-way is defined to be all steps taken by a process (say p) after it receives a signal to abort and breaks out of one of the busy-wait cycles of lines 2, 7 or 14. After p breaks out of one of the busy-wait cycles of lines 2, 7 or 14 p executes a call to $\mathtt{abort}_p$(). If p's call to $\mathtt{abort}_p$() returns $\perp$, then p's passage ends, or else p's $\mathtt{lock}_p$() returns non-$\perp$ value and p calls $\mathtt{release}_p$() and p's passage ends when the $\mathtt{release}_p$() method returns. Since $\mathtt{abort}_p$() and $\mathtt{release}_p$() are both wait-free (by Lemma D.2), our claim follows. ☐

Lemma D.35. *The starvation freedom property holds during history H.*

Proof. Consider a process p that begins to execute its passage. From Lemma D.33(a), it follows that if p does not abort during $\mathtt{lock}_p$() and all processes continue to take steps then p eventually returns from $\mathtt{lock}_p$() with a non-$\perp$ value. Then p eventually calls $\mathtt{release}_p$(), and since $\mathtt{release}_p$() is wait-free, p eventually completes its passage. If p receives a signal to abort during $\mathtt{lock}_p$(), then p executes its abort-way. Since the abort-way is wait-free (by Lemma D.34), p eventually completes its passage. ☐

Lemma D.36. *If a call to* $\mathtt{release}_p(j)$ *returns* **true***, then there exists a concurrent call to* lock() *that eventually returns j.*

Proof. The only operations that write a value to $\mathsf{Sync1}$ are $\mathsf{Sync1}$.CAS($\perp,\infty$) in line 26, and $\mathsf{Sync1}$.CAS($\perp, j$) in line 37. From Claim D.15, $\mathsf{Sync1}$ is written to only by a releaser of L. From Claim D.29 it follows that at I^-_0, $\mathsf{Sync1} = \mathsf{Sync2} = \perp$ and $\mathsf{PawnSet}$ is candidate-empty, and $R(I^-_0) = \varnothing$. Then from Claims D.23(a), D.23(f), D.25(a), D.25(e), D.28(k), and D.28(l), the only releasers of L during a Ctr-cycle interval T, are $\{\mathcal{K}, \mathcal{Q}, \mathcal{P}_1, \ldots, \mathcal{P}_\ell\}$. Then from an inspection of Figures 8, 8, 10, 11, 15 and 16, it follows that only $\mathcal{K}$ and $\mathcal{Q}$ can write to $\mathsf{Sync1}$ during Ctr-cycle interval T.

Since p returns **true**, it then follows from an inspection of the code that p executed a successful $\mathsf{Sync1}.\mathtt{CAS}(\bot, j)$ operation in line 37, and thus failed the $\mathsf{Ctr}.\mathtt{CAS}(1,0)$ operation in line 36 and $\mathsf{Role}[p] = \mathsf{KING}$ at t_p^{36}. Then $p = \mathcal{K}$ for some Ctr-cycle interval T. Since $\mathcal{K}$ failed the $\mathsf{Ctr}.\mathtt{CAS}(1,0)$ operation in line 36, it then follows that Ctr was increased to 1 by process $\mathcal{Q}$ during T, and $I_2^- = t_{\mathcal{Q}}^5 < t_{\mathcal{K}}^{36}$. Since $I_1^- = t_{\mathcal{K}}^5$ and $I_1^- < I_2^-$, it then follows that $\mathcal{Q}$'s $\mathtt{lock}_{\mathcal{Q}}\mathtt{()}$ call is concurrent to $\mathcal{K}$'s $\mathtt{release}_{\mathcal{K}}\mathtt{(}j\mathtt{)}$ call.

From Claim D.29 it follows that at I_0^-, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$ and $\mathsf{PawnSet}$ is candidate-empty, and $R(I_0^-) = \varnothing$. Then from Claim D.25(a), $\mathsf{Sync1} = \bot$ at I_2^-, and $\mathcal{K}$ and $\mathcal{Q}$ are the only two releasers of L during $[I_2^-, \lambda)$, where λ is the first point in time when T is changed to a non-$\bot$ value, and $\lambda = \mathtt{min}(t_{\mathcal{K}}^{56}, t_{\mathcal{Q}}^{56})$.

Now, $\mathsf{Sync1}$ is reset only in line 58, and since $t_{\mathcal{K}}^{58} > t_{\mathcal{K}}^{56} \geq \lambda$ and $t_{\mathcal{Q}}^{58} > t_{\mathcal{Q}}^{56} \geq \lambda$, it then follows that $\mathcal{K}$ and $\mathcal{Q}$ do not reset $\mathsf{Sync1}$ during $[I_2^-, \lambda]$. Since $\mathcal{K}$ and $\mathcal{Q}$ are the only processes with write-access to $\mathsf{Sync1}$, $\mathsf{Sync1}$ is not reset during $[I_2^-, \lambda]$.

Consider $\mathcal{Q}$'s $\mathtt{lock()}$ call (see Figure 10). Since $\mathcal{K}$ executed a successful $\mathsf{Sync1}.\mathtt{CAS}(\bot, j)$ operation and $\mathsf{Sync1}$ is not reset during $[I_2^-, \lambda]$, it then follows that if $\mathcal{Q}$ executes the $\mathsf{Sync1}.\mathtt{CAS}(\bot, \infty)$ operation in line 26, then the operation fails. From an inspection of Figure 10, $\mathcal{Q}$ either returns from its $\mathtt{lock()}$ call in line 17 or line 27. In both these lines, $\mathcal{Q}$ returns the non-$\bot$ value stored in $\mathsf{Sync1}$. Since $\mathcal{K}$ is the only process apart from $\mathcal{Q}$ that can write to $\mathsf{Sync1}$ $\mathcal{Q}$ returns the value j that $\mathcal{K}$ wrote during its $\mathtt{release}_{\mathcal{K}}\mathtt{(}j\mathtt{)}$ call. ☐

Now consider an implementation of object $\mathsf{ALockArray}_N$, where instance $\mathsf{PawnSet}$ is implemented using object $\mathsf{SFMSUnivConst}\langle\mathsf{AbortableProArray}_n\rangle$, and the operations in lines 55, 61, 65, 64, and 67 are executed using the $\mathtt{doFast()}$ method, while the operation in line 21 is executed using the $\mathtt{doSlow()}$.

Claim D.37. *Lines 64,65, 67 of* $\mathtt{doPromote()}$*, all lines of* $\mathtt{doCollect()}$*, and lines 57-62 are not executed concurrently.*

Proof. From Claim D.13(b), it follows that only a releaser of L can execute any of these lines. From Claim D.29 it follows that at I_0^-, $\mathsf{Sync1} = \mathsf{Sync2} = \bot$ and $\mathsf{PawnSet}$ is candidate-empty, and $R(I_0^-) = \varnothing$. Then from Claims D.23(a), D.23(f), D.25(a), D.25(e), D.28(k), and D.28(l) it follows that L has more than one releaser only during $[I_2^-, \lambda)$ for some Ctr-cycle interval T. More specifically, there are two releasers of L only during $[I_2^-, \lambda)$, and the releasers are $\mathcal{K}$ and $\mathcal{Q}$. From Claim D.25(k) it follows that a $\mathtt{doCollect()}$ is executed only by $\mathcal{K}$ or $\mathcal{Q}$ but not both. Then it follows immediately that lines of $\mathtt{doCollect()}$ are not executed concurrently. Since $\lambda = \mathtt{min}(t_{\mathcal{K}}^{56},\ t_{\mathcal{Q}}^{56})$, it follows from an inspection of Figures 8, 9, 10, 11 and the code, that processes $\mathcal{K}$ and $\mathcal{Q}$ have not executed a call to $\mathtt{doPromote()}$ or lines 57-62 of $\mathtt{helpRelease()}$, before $t_{\mathcal{K}}^{56}$ and $t_{\mathcal{Q}}^{56}$ respectively. Then none of the lines chosen in the claim are executed concurrently, and thus our claim holds. ☐

Lemma D.38. *(a) Both* $\mathtt{helpRelease}_p\mathtt{()}$ *and* $\mathtt{doPromote}_p\mathtt{()}$ *have* $\mathcal{O}(1)$ *RMR complexity.*

(b) $\mathtt{doCollect}_p\mathtt{()}$ *has* $\mathcal{O}(n)$ *RMR complexity.*

(c) $\mathtt{abort}_p\mathtt{()}$ *has* $\mathcal{O}(n)$ *RMR complexity.*

(d) If a call to $\mathtt{release}_p\mathtt{(}j\mathtt{)}$ *returns* **true***, then* p *incurs* $\mathcal{O}(n)$ *RMRs during* $\mathtt{release}_p\mathtt{(}j\mathtt{)}$*.*

(e) If a call to $\texttt{release}_p(j)$ *returns* **false***, then* p *incurs* $\mathcal{O}(1)$ *RMRs during* $\texttt{release}_p(j)$*.*

Proof. **Proof of (a) and (b):** As per the properties of object $\mathsf{SFMSUnivConst}\langle\mathsf{AbortableProArray}_n\rangle$ (Lemma 2.2), an operation performed using the `doFast()` method has $\mathcal{O}(1)$ RMR complexity, as long as it is not executed concurrently with another `doFast()` method call. Since PawnSet is an instance of object $\mathsf{SFMSUnivConst}\langle\mathsf{AbortableProArray}_n\rangle$, where operations in lines 55, 61, 65, 64, and 67 are executed using the `doFast()` method, and each of these operations are not executed concurrently (by Claim (D.37)), it then follows that all of these operations have $\mathcal{O}(1)$ RMR complexity. Then Part (a) follows immediately from an inspection of methods `helpRelease()` and `doPromote()`. Since method `doCollect()` has a loop of size n that incurs a constant number of RMRs in each iteration, Part (b) follows.

Proof of (c), (d) and (e): As per the properties of object $\mathsf{SFMSUnivConst}\langle\mathsf{AbortableProArray}_n\rangle$ (Lemma 2.2), an operation performed using the `doSlow()` method has $\mathcal{O}(n)$ RMR complexity, where n is the maximum number of processes that can access the object concurrently. Since the operation in line 21 is executed using the `doSlow()` method, the operation has $\mathcal{O}(n)$ RMR complexity. Since `helpRelease()` and `doPromote()` have an RMR complexity of $\mathcal{O}(1)$ (by Part (a)), and `doCollect()` has an RMR complexity of $\mathcal{O}(n)$ (by Part (b)), it then follows from an inspection of `abort()`, that a call to `abort()` has an RMR complexity of $\mathcal{O}(n)$. Thus Part (b) follows.

If a call to $\texttt{release}_p(j)$ returns **true**, then p does execute a call to $\texttt{doCollect}_p\texttt{()}$ in line 38, else it does not. Then from an inspection of $\texttt{release}_p(j)$, Parts (d) and (e) follow immediately. ☐

Lemma 3.1 follows from Lemmas D.2, D.30, D.33, D.34, D.35, D.36, and D.38.

E The Tree Based Randomized Abortable Lock

E.1 Implementation / Low Level Description

We assume that the tree structure $\mathcal{T}$ provides a function `getNode()`, such that, for a leaf node leaf and integer ℓ, the function $\texttt{getNode}(\mathsf{leaf}, \ell)$ returns a pair $\langle u, i\rangle$, where u is the ℓ-th node on the path from leaf to the root node, and i is the index of the child node of u that lies on the path.

We now describe the implementation of the abortable lock (see Figure 17).

Description of the $\texttt{lock}_p\texttt{()}$ **method.** Suppose process p executes a call to $\texttt{lock}_p\texttt{()}$. With every iteration of the while-loop, process p captures at least one node on its path from leaf_p to $\mathcal{T}.\mathsf{root}$. Suppose p executes an iteration of while-loop (lines 1-10) and $\ell_p = k$ at line 1 for some arbitrary integer k. In line 2, process p determines the k-th node (say u) on path_p and the index (say r) of u's child node that lies on path_p, and stores them in local variables v_p and i_p. The variables v_p and i_p are unchanged during the rest of the iteration. In line 3, process p attempts to capture $u.\mathsf{L}$, and thus node u by executing a call to $u.\mathsf{L}.\texttt{lock()}$ with pseudo-ID r. If p's $u.\mathsf{L}.\texttt{lock}_r\texttt{()}$ returns an integer value (say j) then p has been transferred all nodes on its path up to height j (we ensure $j \geq \mathsf{h}_u$). If p's $u.\mathsf{L}.\texttt{lock()}$ returns ∞ then p has captured lock $u.\mathsf{L}$. In lines 4 and 5, p stores the height of the highest captured node in its local variable ℓ_p. In line 6, p checks whether it has received a signal to abort. In this case p releases all its captured nodes by executing a call to $\texttt{release}_p\texttt{()}$ in line 7 and then returns from its call to $\texttt{lock}_p\texttt{()}$ in line 8 with value $\perp$. Otherwise p continues its while-loop. On completing its while-loop, p owns the root node, and thus returns with value ∞ in line 11 to indicate a successful `lock()` call.

Description of the $\texttt{release}_p\texttt{()}$ **method.** Suppose process p executes a call to $\texttt{release}_p\texttt{()}$. Let s be the highest node p owns at the beginning of $\texttt{release}_p\texttt{()}$. We later prove that $\mathsf{h}_s = \ell_p$.

Algorithm: `Implementation of the abortable lock`

define Node: struct { L: ALockArray$_\Delta$ }
shared: $\mathcal{T}$: complete Δ-ary tree of height Δ and node type Node
local: v: Node **init** $\perp$; i, ℓ, k: **int init** 0; $abort\_signal$: **boolean init false**;

define function $\mathcal{T}$.`getNode`(Node leaf, **int** ℓ): returns a pair $\langle u, i\rangle$, where u is the ℓ-th node on the path from leaf to the root node of $\mathcal{T}$, and i is the index of the child node of u that lies on the path.

Method `lock`$_p$`()`

```
while ℓ < T.height do
    (v, i) ← T.getNode(leaf_p, ℓ + 1)
    val ← v.L.lock_i()
    if val = ∞ then ℓ ← ℓ + 1
    if val ∉ {⊥, ∞} then ℓ ← val
    if abort_signal = true then
        release_p()
        return ⊥
    end
end
return ∞
```

Method `release`$_p$`()`

```
while k ≤ ℓ do
    (v, i) ← T.getNode(leaf_p, k)
    if v.L.release_i(ℓ) then
    break
    k ← k + 1
end
```

Figure 17: Implementation of the abortable lock

During an iteration of the while-loop (lines 12-16), process p either releases a node on its path from leaf_p to s, or p hands over all remaining nodes that it owns to some process.

Consider the execution of an iteration of the while-loop where $k_p = t$ at line 12 for some integer $t \leq \mathsf{h}_s$. In line 13, process p determines the t-th node (say u) on path_p and the index (say r) of u's child node that lies on path_p, and stores them in local variables v_p and i_p. In line 14, process p releases $u.\mathsf{L}$, and thus node u, by executing a call to $u.\mathsf{L}.\mathtt{release}(\mathsf{h}_s)$ with pseudo-ID r. If p's $u.\mathsf{L}.\mathtt{release}_r(\mathsf{h}_s)$ returns **false** then p has successfully released lock $u.\mathsf{L}$, and thus node u. If p's $u.\mathsf{L}.\mathtt{release}_r(\mathsf{h}_s)$ returns **true** then p has successfully handed over all nodes from u to s on path_p to some process that is executing a concurrent call to $u.\mathsf{L}.\mathtt{lock()}$. If p has handed over all its nodes, then p breaks out of the while-loop in line 14, and returns from its call to $\mathtt{release}_p\mathtt{()}$. If p has not handed over all its nodes then p increases k_p in line 15 and continues its while-loop.

Notice that our strategy to release node locks is to climb up the tree until all node locks are released or a hand over of remaining locks is made. Climbing up the tree is necessary (as opposed to climbing down) in order to hand over node locks to a process, say q, such that the handed over nodes lie on path_q. There is however a side effect of this strategy which is as follows: Suppose p owns nodes v and u on path_p such that $\langle u, i\rangle = \mathtt{getNode}(\mathsf{leaf}_p,\mathsf{h}_u)$ and v is the i-th child on node u. Now suppose p releases lock $v.\mathsf{L}$ at node v. Since the lock at node v is now released, some process $r \neq p$ may now capture lock $v.\mathsf{L}$ and then proceed to call $u.\mathsf{L}.\mathtt{lock}_i\mathtt{()}$. If process p has not yet released $u.\mathsf{L}$ by completing its call to $u.\mathsf{L}.\mathtt{release}_i\mathtt{()}$, then we have a situation where a call to $u.\mathsf{L}.\mathtt{lock}_i\mathtt{()}$ is made before a call to $u.\mathsf{L}.\mathtt{release}_i\mathtt{()}$ is completed. Since there can be at most one owner of lock $v.\mathsf{L}$ there can be at most one such call to $u.\mathsf{L}.\mathtt{lock}_i\mathtt{()}$ concurrent to $u.\mathsf{L}.\mathtt{release}_i\mathtt{()}$. This is precisely the reason why we designed object $\mathsf{ALockArray}_n$ to be accessed by at most $n+1$ processes concurrently.

E.2 Analysis and Proofs of Correctness

In this section, we formally prove all properties of our abortable lock for the CC model. We first, establish the safety conditions on the usage of the object.

Condition E.1. *(a) If process p executes a successful* $\mathtt{lock}_p\mathtt{()}$ *call, then process p eventually executes a* $\mathtt{release}_p\mathtt{()}$ *call.*

(b) A process calls method $\mathtt{release()}$ *if and only if its last access of the lock object was a successful* $\mathtt{lock()}$ *call.*

(c) Methods $\mathtt{lock}_p\mathtt{()}$ *and* $\mathtt{release}_p\mathtt{()}$ *are called only by process p, where $p \in \{0, \ldots, N-1\}$.*

(d) For every $\mathtt{release}_p\mathtt{()}$ *call, there must exist a unique successful* $\mathtt{lock}_p\mathtt{()}$ *call that has been executed.*

Notations and Definitions. Let H be an arbitrary history of an algorithm that accesses an instance L of our abortable lock where Condition E.1 is satisfied. Consider an arbitrary node u on the tree $\mathcal{T}$. Let h_u denote the height of node u.

A node u is said to be *handed over* from process p to process q, when p executes a $v.\mathsf{L}.\mathtt{release}(j)$ call that returns **true**, where $j \geq \mathsf{h}_u > \mathsf{h}_v$ and q executes a concurrent $v.\mathsf{L}.\mathtt{lock()}$ call that returns j. Process p is said to start to *own* node u when p captures $u.\mathsf{L}$ or when it is handed over node u from the previous owner of node u. Process p *ceases* to own node u when p releases $u.\mathsf{L}$, or when p hands over node u to some other process.

Claim E.2. *Consider an arbitrary process p and some node u on* path_p.

(a) *If p executes a $u.\mathsf{L}.\texttt{lock()}$ operation that returns value $j \notin \{\bot, \infty\}$, then $j \geq \mathsf{h}_u$.*

(b) *The value of ℓ_p is increased every time p writes to it.*

(c) *If $\ell_p = k$, then process p owns all nodes on path_p up to height k.*

Proof. **Proof of (a) :** Then from the properties of object $\mathsf{ALockArray}_\Delta$ (Lemma 3.1), it follows that some process (say q) executed a concurrent $u.\mathsf{L}.\texttt{release}(j)$ operation. Then from the code structure, q executed a $u.\mathsf{L}.\texttt{release}(j)$ in line 14, where $\ell_q = j$. Then q also executed a $\mathcal{T}.\texttt{getNode}(\mathsf{leaf}_q, k)$ operation in line 13 that returned $\langle u, i\rangle$, for some i, such that $\mathsf{h}_u = k_q$ (from the semantics of the $\texttt{getNode()}$ method). Since $j = \ell_q \geq k_q = \mathsf{h}_u$, our claim follows.

Proof of (b): Process p writes to its local variable ℓ_p only in lines 4 and 5. Clearly, p increases ℓ_p every time it executes line 4. Now, suppose p executes line 5 where it writes the value of val_p to ℓ_p, where $v_p = u$, for some node u. Since p satisfies the if-condition of line 5 and the $\mathsf{ALockArray}_\Delta$ method $\texttt{lock()}$ only returns a value in $\{\bot, \infty\} \cup \mathbb{N}$, it follows that p's call to $u.\mathsf{L}.\texttt{lock()}$ returned a non-$\{\bot, \infty\}$ value. Then from Part (a), $val_p \geq \mathsf{h}_u$. Since p also executed a $\mathcal{T}.\texttt{getNode}(\mathsf{leaf}_p, b)$ operation in line 2, where $b = \ell_p + 1$ that returned $\langle u, i\rangle$, for some i, such that $\mathsf{h}_u = b$ (from the semantics of the $\texttt{getNode()}$ method), it follows that $val_p \geq \mathsf{h}_u = \ell_p + 1$. Then, p increases ℓ_p when p writes val_p to ℓ_p in line 5.

Proof of (c): Let t^i be the point in time such that p writes to its local variable ℓ_p for the i-th time. We prove our claim by induction over i

Basis ($i = 0$): Since the initial value of ℓ_p is 0 and ℓ_p is written to for the first time only at $t^1 > t^0$, the claim holds.

Induction step ($i > 0$): Let the value of ℓ_p be j after the $(i-1)$-th write to it. Then from the induction hypothesis, p owns all nodes on path_p up to height j. Consider the iteration of the while-loop during which p writes to ℓ_p for the i-th time, and specifically the $\mathcal{T}.\texttt{getNode}(\mathsf{leaf}_p, \ell + 1)$ operation in line 2. Since $\ell_p = j$, at the beginning of this while-loop iteration, it follows from the semantics of the $\texttt{getNode()}$ operation, that the operation returned the pair $\langle u, i\rangle$, for some i, where $\mathsf{h}_u = j + 1$. Now, process p writes to its local variable ℓ_p only in lines 4 and 5.

Case a - p writes to ℓ_p in line 4. Then p increased ℓ_p from j to $j + 1$ in line 4. Then, to prove our claim we need to show that p owns the node with height $j + 1$ on path_p. Since p satisfies the if-condition of line 4, it follows from the code structure that p's $u.\mathsf{L}.\texttt{lock()}$ method in line 3 returned the special value ∞, where $v_p = u$. Since $\mathsf{h}_u = j + 1$, and p successfully captured lock $u.\mathsf{L}$, it follows that p owns the $j + 1$-th node on path_p.

Case b - p writes to ℓ_p in line 5. Let $val_p = x$ when p writes to ℓ_p in line 5. From Part (b), it follows that ℓ_p is increased every time it is written to, and therefore $val_p = x > \ell_p$ when p writes to ℓ_p in line 5. Thus, to prove our claim we need to show that p owns all nodes on path_p with heights in the range $\{j, \ldots, x\}$. Since p satisfies the if-condition of line 5 and the $\mathsf{ALockArray}_\Delta$ method $\texttt{lock()}$ only returns a value in $\{\bot, \infty\} \cup \mathbb{N}$, it follows that p's call to $u.\mathsf{L}.\texttt{lock()}$ returned a non-$\{\bot, \infty\}$ value. Thus, p has captured $u.\mathsf{L}$ and now owns node u. It also follows that p has been handed over all nodes on path_p with heights in the range $\{\mathsf{h}_u + 1, \ldots, x\}$. Since $\mathsf{h}_u = j$, our claim follows. ◻

A process is said to *attempt to capture node u* if it executes a $u.\mathsf{L}.\texttt{lock()}$ method in line 3.

Claim E.3. *(a) If two distinct processes p and q attempt to capture node v, then their local variables i have different values.*

(b) *A node has at most one owner at any point in time.*

Proof. We prove our claims for all nodes of height at most h, by induction over integer h.

Basis ($h = 1$) Consider an arbitrary node u of height 1, such that two distinct processes p and q attempt to capture node u. Then processes p and q executed a $\texttt{getNode(}\langle \mathsf{leaf}_p, 1\rangle\texttt{)}$ and $\texttt{getNode(}\langle \mathsf{leaf}_q, 1\rangle\texttt{)}$ in line 2, and received pairs $\langle u, i\rangle$ and $\langle u, j\rangle$, and set their local variables i_p and i_q to i and j respectively. Since p and q are distinct, leaf_p and leaf_q are distinct leaf nodes of tree T, and thus from the semantics of the `getNode()` method it follows that $i \neq j$, and thus Part (a) follows.

Consider an arbitrary node u of height 1. From Part (a), it follows that no two processes execute a concurrent call to $u.\mathsf{L}.\texttt{lock}_i\texttt{()}$ for the same i, and thus it follows from the mutual exclusion property of object $\mathsf{ALockArray}_\Delta$, that at most one process captures $u.\mathsf{L}$. By definition, a process can become an owner of node u only if it captures $u.\mathsf{L}$ or if it is handed over node u from some other process q. If a node u is handed over from some other process q, then q also ceases to be the owner of node u at that point, and thus the number of owners of u does not increase upon a hand over. Thus it follows that node u has at most one owner at any point in time, and thus Part (b) follows.

Induction Step ($h > 1$) Consider an arbitrary node u of height h, such that two distinct processes p and q attempt to capture node u. Then processes p and q executed a $\texttt{getNode(}\langle \mathsf{leaf}_p, h\rangle\texttt{)}$ and $\texttt{getNode(}\langle \mathsf{leaf}_q, h\rangle\texttt{)}$ in line 2, and received pairs $\langle u, i\rangle$ and $\langle u, j\rangle$, and set their local variables i_p and i_q to i and j, respectively. For the purpose of a contradiction, assume $i = j$. From the semantics of `getNode()` method, $i = j$ only if the $(h-1)$-th nodes on path_p and path_q are the same (say w). From the induction hypothesis of Part (b) for $h-1$, w has at most one owner at any point in time. Since $\ell_p = \ell_q = h-1$ when p and q attempt to capture node u, it follows from Claim E.2(c), that p and q own all nodes up to height $h-1$ on their individual paths path_p and path_q. Then p and q are both the owners of w – a contradiction. Thus, Part (a) follows.

Since Part (a) holds for h, Part (b) holds for h, as argued in the **Basis** case. □

Lemma E.4. *The mutual exclusion property is satisfied during history H.*

Proof. Assume two processes p and q are in their Critical Section at the same time, i.e., both processes returned a non-$\perp$ value from their last `lock()` call. Then both processes executed line 11 and thus $\ell_p = \ell_q = \mathcal{T}.height$ holds. Then from Claim E.2(c) it follows that both p and q own node $\mathcal{T}.\mathsf{root}$. But from Claim E.3(b), at most one process may own $\mathcal{T}.\mathsf{root}$ at any point in time – a contradiction. □

Claim E.5. *Process p repeats the while-loop in* `lock()` *at most Δ times.*

Proof. Consider an arbitrary process p that calls `lock()`. From the code structure of `lock()`, it follows that if p repeats an iteration of the while-loop then p either executed line 4 or line 5 in its previous iteration. Then it follows from Claim E.2(b) that p increases ℓ_p every time it repeats an iteration of the while-loop. Since the height of the $\mathcal{T}$ is Δ, our claim follows. □

Lemma E.6. *No process starves in history H.*

Proof. Since no two processes execute a concurrent call to $u.\mathsf{L}.\texttt{lock}_i\texttt{()}$ for the same i (from Claim E.3 (a)), it follows from the starvation-freedom property of object $\mathsf{ALockArray}_\Delta$, that a process does not starve during a call to $u.\mathsf{L}.\texttt{lock()}$ for some node u on its path.

Consider an arbitrary process p that calls `lock()`. Since p repeats the while-loop in `lock()` at most Δ times before returning from line 11 (follows from Claim E.5), it follows that p starves only if p starves during a call to $u.\mathsf{L}.\texttt{lock()}$ in line 3 for some node u. As already argued, this cannot happen, and thus our claim follows. □

Lemma E.7. *Process p incurs $\mathcal{O}(\Delta)$ RMRs during* $\texttt{release}_p\texttt{()}$.

Proof. Consider p's call to `release()`. Since $\ell_p \leq \mathcal{T}.height = \Delta$, it follows from an inspection of the code that during `release()`, p executes at most Δ calls to `L.release()` (in line 14), and at most one of the `L.release()` calls returns **true**. As per the properties of object $\mathsf{ALockArray}_\Delta$ (Lemma 3.1), a process incurs $\mathcal{O}(\Delta)$ RMRs during a call to `L.release()`, if the call returns **true**, otherwise $\mathcal{O}(1)$ RMRs. Then our claim follows immediately. ☐

Lemma E.8. *Process p incurs $\mathcal{O}(\Delta)$ RMRs in expectation during* $\texttt{lock}_p\texttt{()}$.

Proof. A process may or may not receive a signal to abort during $\texttt{lock}_p\texttt{()}$.

Case a - p does not receive a signal to abort during $\texttt{lock}_p\texttt{()}$. As per the properties of object $\mathsf{ALockArray}_\Delta$ (Lemma 3.1), if a process does not receive a signal to abort during a call to `L.lock()`, then the process incurs $\mathcal{O}(1)$ RMRs in expectation during the call. Since p repeats the while-loop in `lock()` at most Δ times (by Claim E.5), and p does not receive a signal to abort during $\texttt{lock}_p\texttt{()}$, it follows that p incurs $\mathcal{O}(\Delta)$ RMRs in expectation during $\texttt{lock}_p\texttt{()}$.

Case b - p receives a signal to abort during $\texttt{lock}_p\texttt{()}$. As per the properties of object $\mathsf{ALockArray}_\Delta$ (Theorem 3.1), if a process aborts during a call to `L.lock()`, then the process incurs $\mathcal{O}(\Delta)$ RMRs in expectation during the call. Since p repeats the while-loop in `lock()` at most Δ times (by Claim E.5), and p executes at most one call to u.`L.lock()` after having received an abort signal, it follows that p incurs $\mathcal{O}(\Delta)$ RMRs in expectation during $\texttt{lock}_p\texttt{()}$. ☐

Lemma E.9. *Method* `release()` *is wait-free.*

Proof. As per the bounded exit property of object $\mathsf{ALockArray}_\Delta$, method `release()` of the object is wait-free. Then our claim follows immediately from an inspection of the code of `release()`. ☐

Lemma E.10. *The abort-way is wait-free and has $\mathcal{O}(\Delta)$ RMR complexity.*

Proof. The abort-way of a process p consists of the steps executed by the process after receiving a signal to abort and before completing its passage. From Lemma E.9 and E.7, method $\texttt{release}_p\texttt{()}$ is wait-free, and has $\mathcal{O}(\Delta)$ RMR complexity. From Claim E.5, a process repeats the while-loop in $\texttt{lock}_p\texttt{()}$ at most Δ times. Then from an inspection of the code it follows that a process executes all steps during its passage in a wait-free manner, except the call to u.`L.lock()` in line 3, and that a process incurs at most $\mathcal{O}(\Delta)$ RMRs during all these steps.

To complete our proof we now show that if a process has received a signal to abort and it executes a call to u.`L.lock()` in line 3, for some node u, then the process executes u.`L.lock()` in a wait-free manner and incurs $\mathcal{O}(\Delta)$ RMR during the call, and does not call v.`L.lock()` for any other node v.

Suppose that p has received a signal to abort, and p executes a call to u.`L.lock()` call in line 3. Since p has received a signal to abort, it follows that p executes the abort-way of the node lock u.L. As per the properties of object $\mathsf{ALockArray}_\Delta$ (Lemma 3.1), its abort-way is wait-free and has $\mathcal{O}(\Delta)$ RMR complexity. Then p executes the u.`L.lock()` call in line 3 in a wait-free manner and incurs $\mathcal{O}(\Delta)$ RMR complexity. It then goes on to satisfy the if-condition of line 6, and executes a call to `release()` in line 7 and returns $\perp$ in line 8, thereby completing its abort-way. Thus, our claim holds. ☐

Theorem 1.1 follows from Lemmas E.4, E.6, E.7, E.8, E.9 and E.10.

F Remaining Proofs of Properties of $\mathsf{ALockArray}_n$

Claim F.1. *Suppose a process p executes a call to $\texttt{lock}_p\texttt{()}$ during a passage. The value of* $\mathsf{Role}[p]$ *at various times is as follows.*

Points in time	*Value of* $\mathsf{Role}[p]$
t_p^5	$\{\infty, \mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN}\}$
$[t_p^7, t_p^8]$	PAWN
t_p^9	$\mathsf{PAWN\_P}$
t_p^{13-}	$\{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$
t_p^{14}	QUEEN
$[t_p^{16}, t_p^{17}]$	$\{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$

Proof. Since the values returned by a $\mathsf{Ctr}.\texttt{inc()}$ operation are in $\{\infty, 0, 1, 2\} = \{\infty, \mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN}\}$, $\mathsf{Role}[p]$ is set to one of these values in line 5. Hence, $\mathsf{Role}[p] \in \{\infty, \mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN}\}$ at t_p^5. If p satisfies the if-condition of line 6, then $\mathsf{Role}[p] = \mathsf{PAWN}$, and p changes $\mathsf{Role}[p]$ next only in line 9. Hence, $\mathsf{Role}[p] = \mathsf{PAWN}$ during $[t_p^7, t_p^8]$. In line 9 p changes $\mathsf{Role}[p]$ to $\mathsf{PAWN\_P}$ and does not change $\mathsf{Role}[p]$ thereafter. Hence, $\mathsf{Role}[p] = \mathsf{PAWN\_P}$ at t_p^9.

Process p does not change $\mathsf{Role}[p]$ after line 9. To break out of the getLock loop, $\mathsf{Role}[p] \in \{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$ must be satisfied when p executes line 12. Hence, $\mathsf{Role}[p] = \{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$ during $[t_p^{16}, t_p^{17}]$. Since p executes line 13 only after breaking out of the getLock loop, $\mathsf{Role}[p] \in \{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$ at t_p^{13-}. If p satisfies the if-condition of line 13, then $\mathsf{Role}[p] = \mathsf{QUEEN}$, and since p does not change $\mathsf{Role}[p]$ thereafter, $\mathsf{Role}[p] = \mathsf{QUEEN}$ at t_p^{14}. ☐

Claim F.2. *Suppose a process p executes a call to $\texttt{abort}_p\texttt{()}$. The value of* $\mathsf{Role}[p]$ *at various points in time is as follows.*

Points in time	*Value of* $\mathsf{Role}[p]$
$[t_p^{19}, t_p^{20-}]$	$\{\mathsf{QUEEN}, \mathsf{PAWN}\}$
t_p^{21}	PAWN
$[t_p^{22}, t_p^{23}]$	$\mathsf{PAWN\_P}$
$[t_p^{26-}, t_p^{30}]$	QUEEN

Proof. Process p calls $\texttt{abort}_p\texttt{()}$ only if p has received a signal to abort and p is busy waiting in one of lines 2, 7, or 14. Then, the last line executed by p before calling $\texttt{abort}_p\texttt{()}$ is line 2, 7, or line 14. From Claim F.1, it follows that $\mathsf{Role}[p] = \mathsf{PAWN}$ at t_p^7, and $\mathsf{Role}[p] = \mathsf{QUEEN}$ at t_p^{14}.

Now, p's local variable $flag$ is set to value $\texttt{true}$ for the first time in line 3. If p fails the if-condition of line 18, then p must have executed line 3, and thus p broke out of the busy-wait loop of line 2. Then, p last executed line 7 or line 14 before calling $\texttt{abort}_p\texttt{()}$. Hence, $\mathsf{Role}[p] \in \{\mathsf{PAWN}, \mathsf{QUEEN}\}$ in $[t_p^{19}, t_p^{20}]$, since p changes $\mathsf{Role}[p]$ next only in line 22.

If p satisfies the if-condition of line 20, then $\mathsf{Role}[p] = \mathsf{PAWN}$, and p changes $\mathsf{Role}[p]$ next only in line 22. Hence, $\mathsf{Role}[p] = \mathsf{PAWN}$ at t_p^{21}. In line 22 p changes $\mathsf{Role}[p]$ to $\mathsf{PAWN\_P}$ and p does not change $\mathsf{Role}[p]$ after that. Hence, $\mathsf{Role}[p] = \mathsf{PAWN\_P}$ during $[t_p^{22}, t_p^{23}]$. If p does not satisfy the if-condition of line 20, then $\mathsf{Role}[p] = \mathsf{QUEEN}$ at $[t_p^{26-}, t_p^{30}]$ follows. ☐

Claim F.3. *Suppose a process p executes a call to $\texttt{release}_p\texttt{(}j\texttt{)}$ during a passage. The value of* $\mathsf{Role}[p]$ *at various points in time is as follows.*

Points in time	*Value of* $\mathsf{Role}[p]$
$[t_p^{34-}, t_p^{35-}]$	$\{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$
$[t_p^{36-}, t_p^{39}]$	KING
t_p^{43-}	QUEEN
t_p^{46-}	$\mathsf{PAWN\_P}$
$[t_p^{49-}, t_p^{50}]$	$\{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$

Proof. Suppose the point in time t_p^{34-}. Then, p is is executing a call to $\texttt{release}_p(j)$, and p last executed a call to $\texttt{lock}_p\texttt{()}$ that returned a non-$\perp$ value. Then, p's call to $\texttt{lock}_p\texttt{()}$ either returned from line 17 in $\texttt{lock}_p\texttt{()}$ or from line 23 or line 27 in $\texttt{abort}_p\texttt{()}$. From Claim F.1, $\mathsf{Role}[p] \in \{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$ at time t_p^{17-} and from Claim F.2, $\mathsf{Role}[p] = \mathsf{PAWN\_P}$ at t_p^{23-} and $\mathsf{Role}[p] = \mathsf{QUEEN}$ at t_p^{27-}. Therefore, $\mathsf{Role}[p] \in \{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$ at time t_p^{34-}.

From Claim D.4(b), $\mathsf{Role}[p]$ is unchanged during $\texttt{release}_p\texttt{()}$. Therefore, $\mathsf{Role}[p] \in \{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$ during $[t_p^{34-}, t_p^{35-}]$ and $[t_p^{49-}, t_p^{50}]$. Then, from the if-conditions of lines 35, 42 and 45, it follows immediately that $\mathsf{Role}[p] = \mathsf{KING}$ during $[t_p^{36-}, t_p^{39}]$, and $\mathsf{Role}[p] = \mathsf{QUEEN}$ at t_p^{43-}, and $\mathsf{Role}[p] = \mathsf{PAWN\_P}$ at t_p^{46-}.

$\square$

Claim F.4. *Suppose a process p executes a call to* $\texttt{doCollect}_p\texttt{()}$*,* $\texttt{helpRelease}_p\texttt{()}$ *or* $\texttt{doPromote}_p\texttt{()}$ *during a passage. The value of* $\mathsf{Role}[p]$ *at various points in time is as follows.*

Points in time	*Value of* $\mathsf{Role}[p]$
$[t_p^{51-}, t_p^{55}]$	$\{\mathsf{KING}, \mathsf{QUEEN}\}$
$[t_p^{56-}, t_p^{63}]$	$\{\mathsf{KING}, \mathsf{QUEEN}\}$
$[t_p^{65-}, t_p^{71}]$	$\{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$

Proof. From the code structure, p does not change $\mathsf{Role}[p]$ during $\texttt{doPromote()}$, $\texttt{doCollect}_p\texttt{()}$ and $\texttt{helpRelease()}$.

From a code inspection, $\texttt{doCollect}_p\texttt{()}$ is called by p only in lines 29, and 38. From Claim F.2, $\mathsf{Role}[p] = \mathsf{QUEEN}$ at t_p^{29-} and from Claim F.3, $\mathsf{Role}[p] = \mathsf{KING}$ at t_p^{38-}. Since $\mathsf{Role}[p]$ is unchanged during $\texttt{doCollect}_p\texttt{()}$, it follows that $\mathsf{Role}[p] \in \{\mathsf{KING}, \mathsf{QUEEN}\}$ during $[t_p^{51-}, t_p^{55}]$.

Now, suppose p executes a call $\texttt{helpRelease}_p\texttt{()}$. From a code inspection, $\texttt{helpRelease}_p\texttt{()}$ is called by p only in lines 30, 39 and 43. From Claim F.2, $\mathsf{Role}[p] = \mathsf{QUEEN}$ at t_p^{30-} and from Claim F.3, $\mathsf{Role}[p] = \mathsf{KING}$ at t_p^{39-} and $\mathsf{Role}[p] = \mathsf{QUEEN}$ at t_p^{43-} . Since $\mathsf{Role}[p]$ is unchanged during $\texttt{helpRelease()}$, it follows that $\mathsf{Role}[p] \in \{\mathsf{KING}, \mathsf{QUEEN}\}$ during $[t_p^{56-}, t_p^{63}]$.

Now, suppose p executes a call $\texttt{doPromote}_p\texttt{()}$. From a code inspection, $\texttt{doPromote}_p\texttt{()}$ is called by p only in lines 46 and 62. From Claim F.3, $\mathsf{Role}[p] = \mathsf{PAWN\_P}$ at t_p^{46-} and from earlier in this claim, $\mathsf{Role}[p] \in \{\mathsf{KING}, \mathsf{QUEEN}\}$ at t_p^{62-}. Since $\mathsf{Role}[p]$ is unchanged during $\texttt{doPromote()}$, it follows that $\mathsf{Role}[p] \in \{\mathsf{KING}, \mathsf{QUEEN}, \mathsf{PAWN\_P}\}$ during $[t_p^{65-}, t_p^{71}]$. $\square$