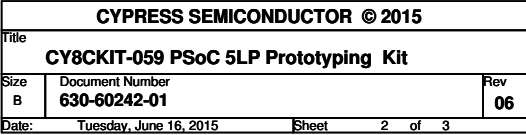


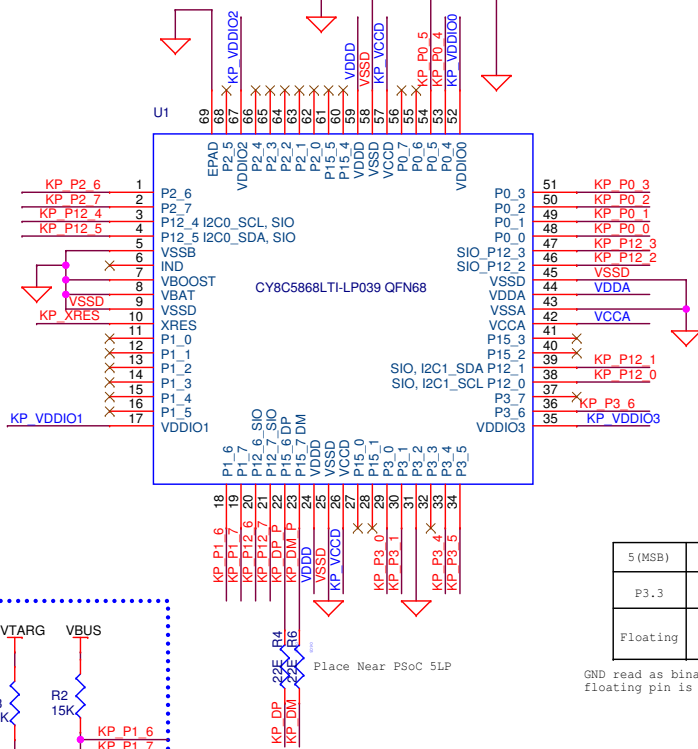
PCA: 121-60210-01
 PCB: 600-60243-01
 FAB DRW: 610-60235-01
 ASSY DRW: 620-60243-01
 SCH: 630-60242-01

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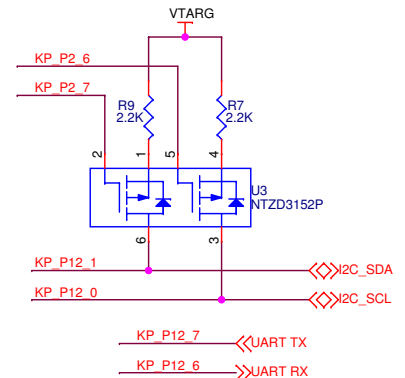
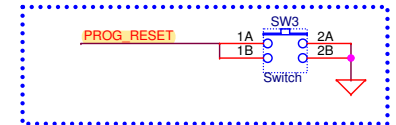
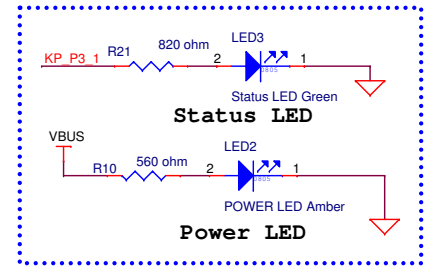


The schematic diagram illustrates the power supply network for the K157UD1 microcontroller. It includes the following components and connections:

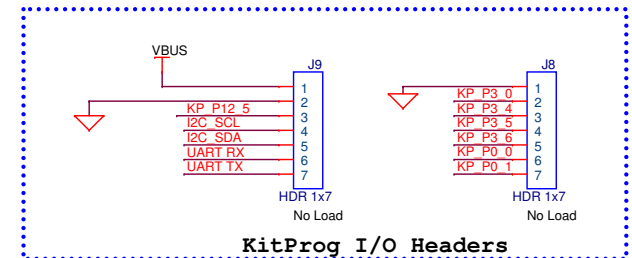
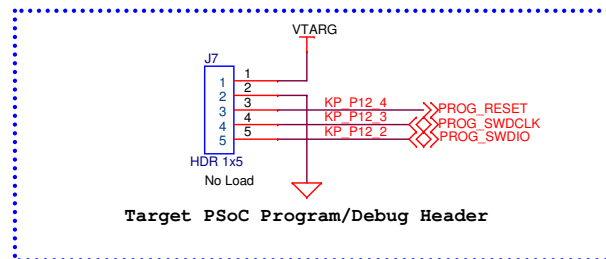
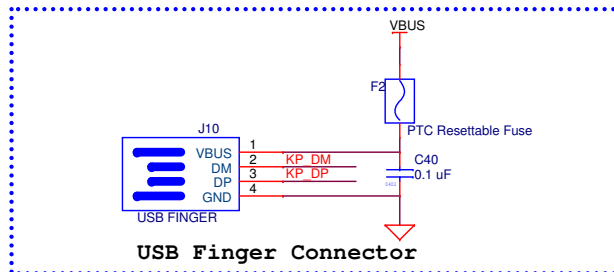
- IO Pins:** KP_VDDIO0, KP_VDDIO1, KP_VDDIO2, and KP_VDDIO3 are connected to a common line that branches to capacitors C11, C1, C3, and C14, respectively. All capacitors are connected to ground.
- VDDIO Pins:** C11 (0.1 uF), C1 (0.1 uF), C3 (0.1 uF), and C14 (0.1 uF) are connected to ground.
- VDDD:** Connected to KP_VDD. It branches to a network of capacitors C24 (1.0 uF) and C5 (0.1 uF) to ground, and another network of C25 (1.0 uF) and C6 (0.1 uF) to ground.
- VDDA:** Connected to the VDDD network and branches to a network of C39 (0.1 uF) and C38 (1.0 uF) to ground.
- VCCA:** Connected to a capacitor C30 (1.0 uF) to ground.
- KP_VCCD:** Connected to a network of capacitors C10 (0.1 uF) and C28 (1.0 uF) to ground.



5 (MSB)	4	3	2	1	0 (LSB)
P3.3	P3.2	P0.7	P0.6	P0.5	P0.4
Floating	GND	Floating	Floating	GND	Floating



I2C & UART Connection



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