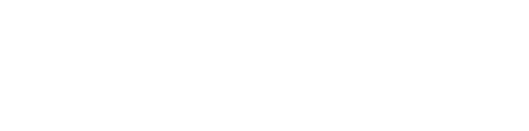
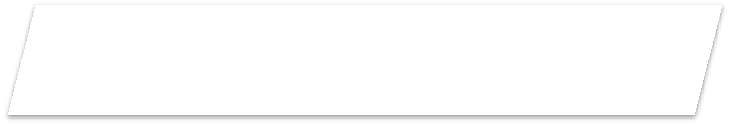
# 数据存储的顺序

**addr=&a**

**MSB**

### 



**int a = 0x01234567**

**LSB**

**内存地址增大方向**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| 大端方式  (**Big Endian**) |  | 0x100 | 0x101 | 0x102 | 0x103 |
|  | 01 | 23 | 45 | 67 |

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| 小端方式  (**Little Endian**) |  | 0x100 | 0x101 | 0x102 | 0x103 |
|  | 67 | 45 | 23 | 01 |

